SPEC® CFP2006 Result

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Fujitsu Siemens Computers

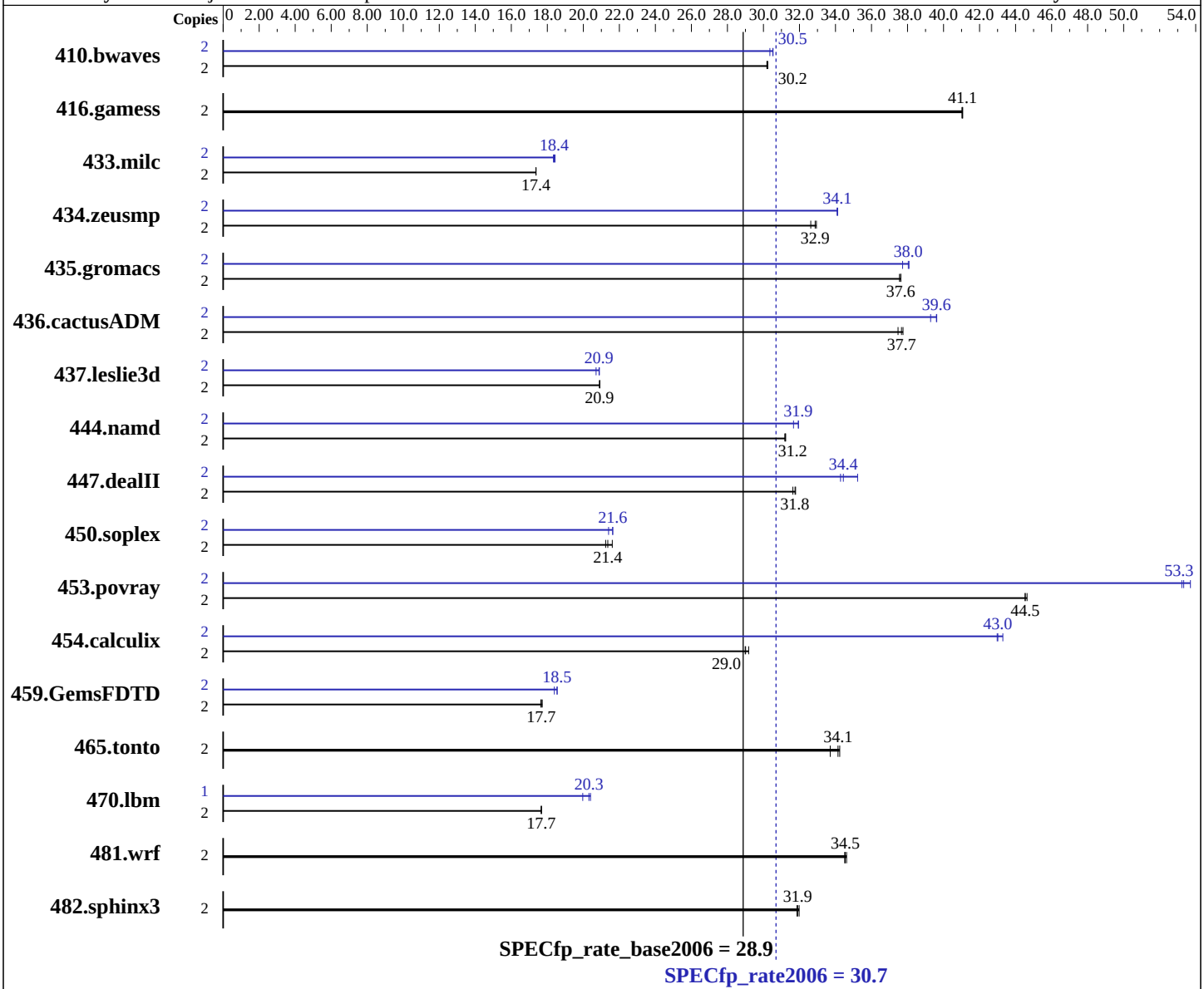
CELSIUS M460, Intel Core 2 Duo E6850 processor

SPECfp®_rate2006 = 30.7

SPECfp_rate_base2006 = 28.9

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Dec-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Duo E6850
CPU Characteristics:	
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

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Software

Operating System:	Microsoft Windows Vista Ultimate (x64)
Compiler:	Intel C++ and Fortran Compilers for Intel64, Version 10.1, Build 20070913 Microsoft Visual Studio 2005 with SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: None
Other Cache: None
Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)
Disk Subsystem: 1 x 400 GB SATA II 7200 RPM
Other Hardware: None

System State: Default
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: MicroQuill SmartHeap Library, Version 8.0 (64 bit)

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	899	30.2	900	30.2	**900**	**30.2**	2	**891**	**30.5**	890	30.5	895	30.4
416.gamess	2	955	41.0	**954**	**41.1**	954	41.1	2	955	41.0	**954**	**41.1**	954	41.1
433.milc	2	**1057**	**17.4**	1057	17.4	1057	17.4	2	**999**	**18.4**	996	18.4	1001	18.3
434.zeusmp	2	552	32.9	558	32.6	**554**	**32.9**	2	534	34.1	**534**	**34.1**	533	34.1
435.gromacs	2	380	37.6	**380**	**37.6**	380	37.6	2	375	38.1	**375**	**38.0**	379	37.7
436.cactusADM	2	**635**	**37.7**	633	37.8	638	37.5	2	603	39.6	**603**	**39.6**	608	39.3
437.leslie3d	2	900	20.9	**899**	**20.9**	899	20.9	2	900	20.9	**901**	**20.9**	908	20.7
444.namd	2	514	31.2	**514**	**31.2**	514	31.2	2	**502**	**31.9**	502	31.9	506	31.7
447.dealII	2	720	31.8	723	31.6	**721**	**31.8**	2	667	34.3	**664**	**34.4**	649	35.2
450.soplex	2	785	21.2	**781**	**21.4**	772	21.6	2	771	21.6	**771**	**21.6**	779	21.4
453.povray	2	239	44.5	238	44.6	**239**	**44.5**	2	198	53.7	**199**	**53.3**	200	53.2
454.calculix	2	569	29.0	565	29.2	**569**	**29.0**	2	381	43.3	384	43.0	**384**	**43.0**
459.GemsFDTD	2	**1201**	**17.7**	1198	17.7	1203	17.6	2	1154	18.4	**1145**	**18.5**	1144	18.5
465.tonto	2	575	34.2	**576**	**34.1**	584	33.7	2	575	34.2	**576**	**34.1**	584	33.7
470.lbm	2	1555	17.7	1556	17.7	**1555**	**17.7**	1	674	20.4	688	20.0	**677**	**20.3**
481.wrf	2	647	34.5	**647**	**34.5**	645	34.6	2	647	34.5	**647**	**34.5**	645	34.6
482.sphinx3	2	1219	32.0	1223	31.9	**1222**	**31.9**	2	1219	32.0	1223	31.9	**1222**	**31.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
BIOS default settings have been used.
```

General Notes

```
For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase -assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 -TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-fast -F950000000 shlW64M.lib

C++ benchmarks:
-fast -Qcxx-features -F950000000 shlW64M.lib

Fortran benchmarks:
-fast -F950000000

Benchmarks using both Fortran and C:
-fast -F950000000

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc8 -Qc99 ifort
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

433.milc: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -F950000000`

470.lbm: `-fast -Qunroll2 -Qscalar-rep- -Qprefetch -F950000000`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `-fast -Oa -F950000000 shlW64M.lib`

447.dealII: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx-features -F950000000`

450.soplex: `Same as 447.dealII`

453.povray: `Same as 447.dealII`

Fortran benchmarks:

410.bwaves: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -F950000000`

416.gamess: `basepeak = yes`

434.zeusmp: `-QxT -O2 -Qprec-div- -Qunroll0 -Qscalar-rep- -F950000000`

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Peak Optimization Flags (Continued)

437.leslie3d: `Same as 410.bwaves`

459.GemsFDTD: `-fast -Qunroll2 -Ob0 -Qprefetch -F950000000`

465.tonto: `basepeak = yes`

Benchmarks using both Fortran and C:

435.gromacs: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -F950000000`

436.cactusADM: `-fast -Qunroll2 -Qprefetch -F950000000`

454.calculix: `-fast -Qunroll-aggressive -F950000000`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.03.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.03.xml`

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For other inquiries, please contact webmaster@spec.org.

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