



SPEC® CFP2006 Result

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Dell Inc.

SPECfp®_rate2006 = 47.2

Dell Precision T3400 (Intel QX9650, 3.00 GHz)

SPECfp_rate_base2006 = 44.9

CPU2006 license: 55

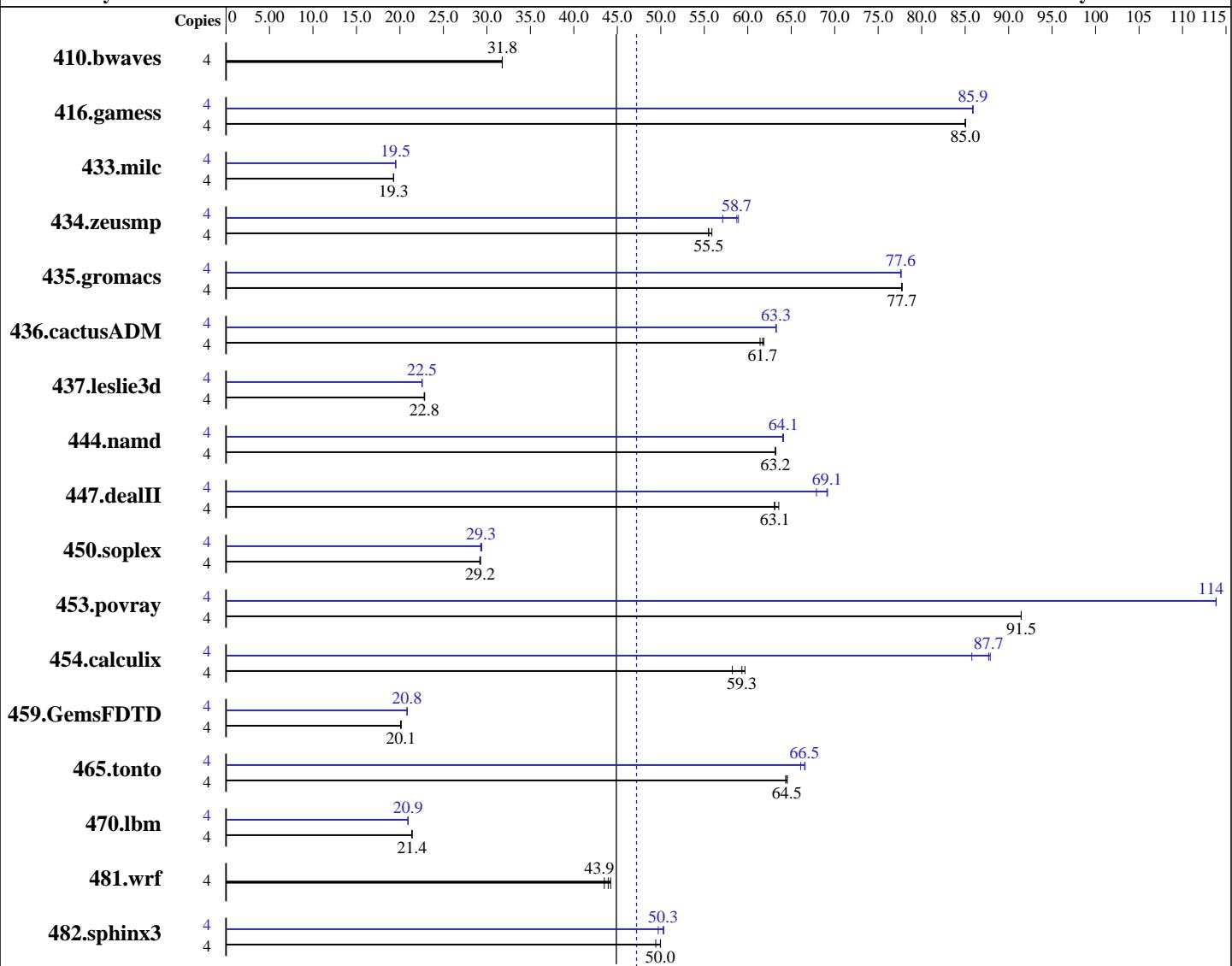
Test date: Dec-2007

Test sponsor: Dell Inc.

Hardware Availability: Jan-2008

Tested by: Dell Inc.

Software Availability: Nov-2007



CPU Name: Intel Core 2 Extreme QX9650
CPU Characteristics: 1333 MHz Bus Speed
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Hardware

Operating System:
Compiler:

Windows XP Professional x64 Edition SP2
Intel C++ Compiler for Intel 64, Version 10.1
Build 20070809 Package ID: w_cc_p_10.1.011
Intel Visual Fortran Compiler for Intel 64,
Version 10.0
Build 20070809 Package ID: w_fc_p_10.1.011
Microsoft Visual Studio 2005 SP1

Auto Parallel:
File System:

No
NTFS

Continued on next page

Software



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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icl -Qstd=c99 ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64
```

Base Optimization Flags

C benchmarks:

```
-fast -Qauto-ilp32 /F10000000000 shlw64m.lib
      -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qauto-ilp32 -Qcxx_features /F10000000000 shlw64m.lib
      -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
-fast -Qauto-ilp32 /F10000000000           -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:

```
-fast -Qauto-ilp32 /F10000000000           -link /FORCE:MULTIPLE
```

Peak Compiler Invocation

C benchmarks:

icl -Qstd=c99

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Peak Compiler Invocation (Continued)

C++ benchmarks:

 icl

Fortran benchmarks:

 ifort

Benchmarks using both Fortran and C:

 icl -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32  
          -Qunroll2 -Oa /F10000000000 shlw64m.lib  
          -link /FORCE:MULTIPLE
```

```
470.lbm: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32  
          -Qunroll2 -Qscalar-rep- -Qprefetch /F10000000000  
          shlw64m.lib           -link /FORCE:MULTIPLE
```

```
482.sphinx3: -fast -Qauto-ilp32 -Qunroll2 /F10000000000 shlw64m.lib  
             -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
444.namd: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32  
          -Oa -Qcxx_features /F10000000000 shlw64m.lib  
          -link /FORCE:MULTIPLE
```

```
447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32  
            -Qunroll2 -Qprefetch -Qcxx_features /F10000000000  
            shlw64m.lib           -link /FORCE:MULTIPLE
```

```
450.soplex: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32  
            -Qcxx_features /F10000000000 shlw64m.lib  
            -link /FORCE:MULTIPLE
```

```
453.povray: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32  
            -Qunroll4 -Qansi-alias -Qcxx_features /F10000000000  
            shlw64m.lib           -link /FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
-Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000
-link /FORCE:MULTIPLE

434.zeusmp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qprec-div-
-Qunroll0 -Qscalar-rep- /F1000000000
-link /FORCE:MULTIPLE

437.leslie3d: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
-Qprefetch /F1000000000 -link /FORCE:MULTIPLE

459.GemsFDTD: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
-Qunroll2 -Ob0 -Qprefetch /F1000000000
-link /FORCE:MULTIPLE

465.tonto: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
-Qunroll4 -Qauto /F1000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
-Oa -Qprefetch /F1000000000
-link /FORCE:MULTIPLE

436.cactusADM: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
-Qunroll2 -Qprefetch /F1000000000
-link /FORCE:MULTIPLE

454.calculix: -fast -Qauto-ilp32 -Qunroll-aggressive /F1000000000
-link /FORCE:MULTIPLE

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.01.xml>



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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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