



SPEC® CINT2006 Result

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Acer Incorporated

SPECint_rate2006 = 62.6

Acer Altos G540 (Intel Xeon X5365)

SPECint_rate_base2006 = 55.1

CPU2006 license: 97

Test sponsor: Acer Incorporated

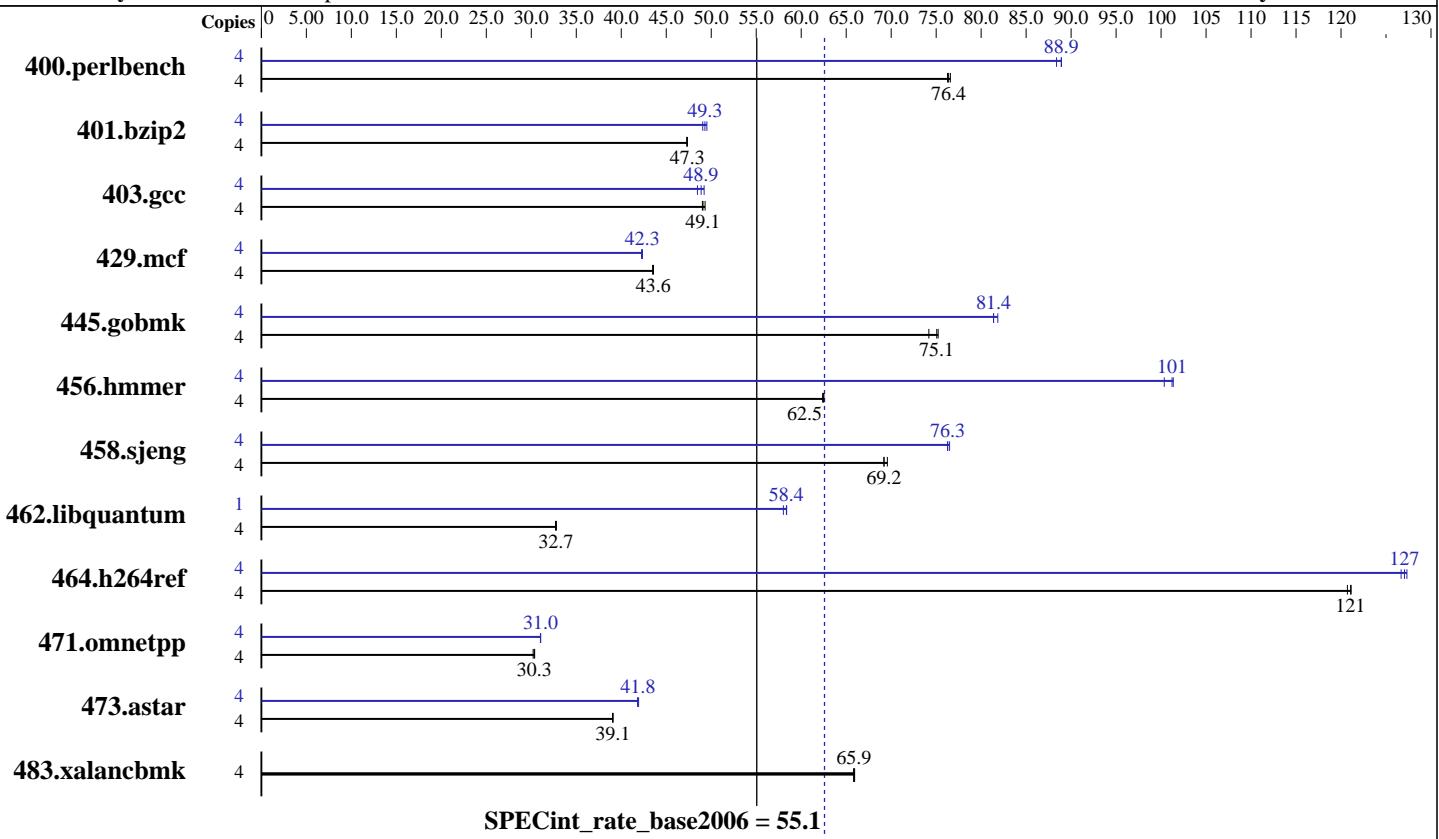
Tested by: Acer Incorporated

Test date:

Nov-2007

Hardware Availability: Nov-2006

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5365
CPU Characteristics: 1333MHz system bus
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1, 2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem: 1 x 73 GB 10000RPM SAS HDD
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 EM64T (kernel 2.6.16.21-0.8-smp)
Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel: Yes
File System: ReiserFS
System State: Multi-user, run level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap Library 8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	510	76.6	512	76.4	512	76.3	4	439	88.9	442	88.4	440	88.9
401.bzip2	4	815	47.3	816	47.3	816	47.3	4	787	49.1	783	49.3	780	49.5
403.gcc	4	653	49.3	656	49.1	657	49.0	4	654	49.2	664	48.5	659	48.9
429.mcf	4	837	43.6	837	43.6	839	43.5	4	863	42.3	862	42.3	861	42.3
445.gobmk	4	558	75.2	559	75.1	566	74.2	4	516	81.4	516	81.4	513	81.9
456.hmmer	4	598	62.4	597	62.5	597	62.5	4	372	100	369	101	368	101
458.sjeng	4	696	69.6	699	69.2	700	69.2	4	635	76.3	633	76.5	634	76.3
462.libquantum	4	2528	32.8	2532	32.7	2535	32.7	1	357	58.0	355	58.4	355	58.4
464.h264ref	4	733	121	731	121	731	121	4	697	127	699	127	695	127
471.omnetpp	4	828	30.2	823	30.4	824	30.3	4	805	31.0	806	31.0	806	31.0
473.astar	4	719	39.0	718	39.1	719	39.1	4	670	41.9	672	41.8	671	41.8
483.xalancbmk	4	419	65.9	419	65.9	419	65.8	4	419	65.9	419	65.9	419	65.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All results used taskset to bind processes to cores.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmpllr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
          -L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
          -I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

```
456.hmmr: /home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
          -L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
          -I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmr: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
              -prefetch
```

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Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll12 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll14 -O0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.xml>



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Tested with SPEC CPU2006 v1.0.

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