SPEC® CINT2006 Result

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Acer Incorporated

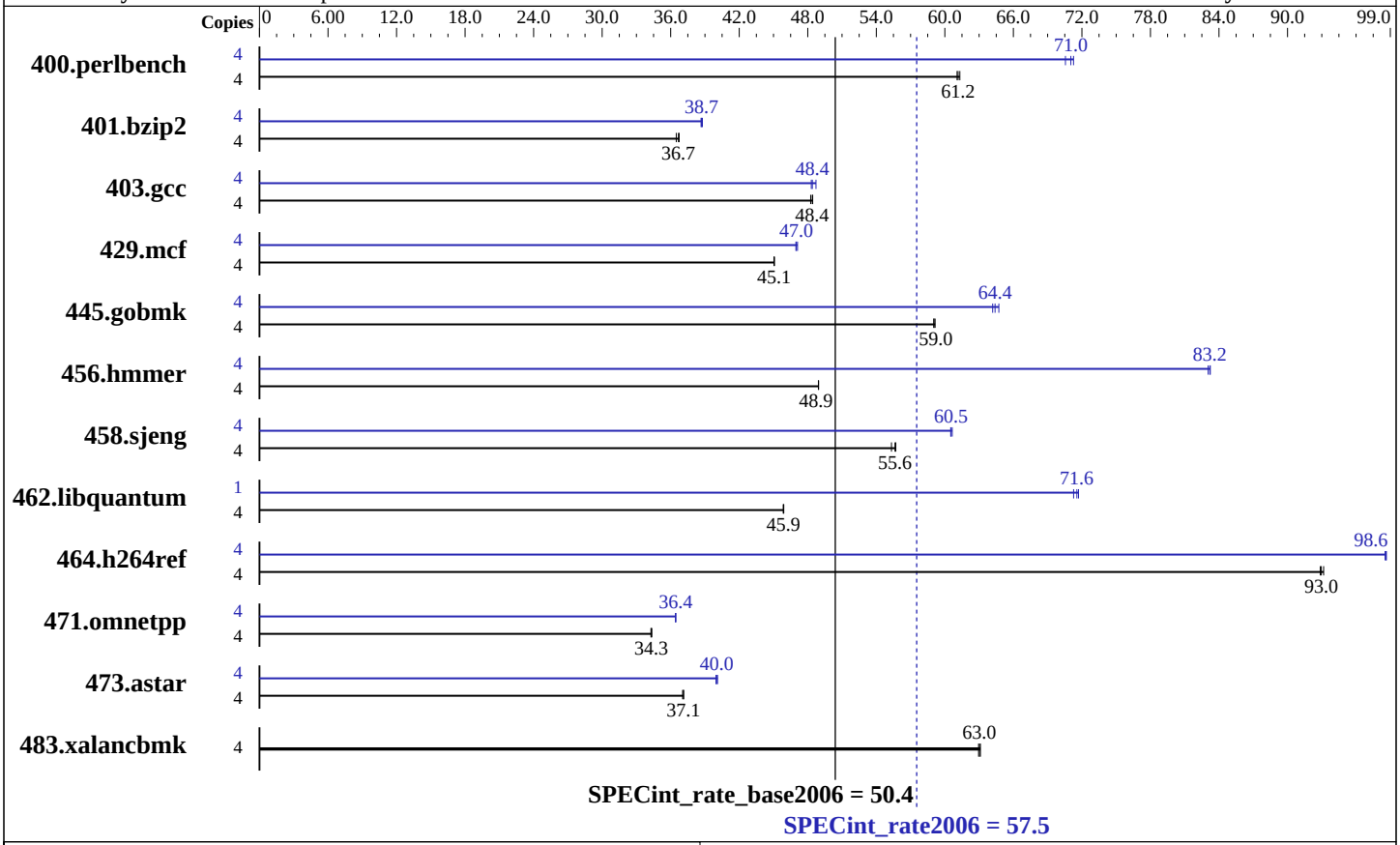
Acer Altos R720 (Intel Xeon 5140)

SPECint®_rate2006 = 57.5

SPECint_rate_base2006 = 50.4

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Nov-2007
Hardware Availability: Nov-2006
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon 5140
CPU Characteristics:	1333MHz system bus
CPU MHz:	2330
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem:	1 x 73GB 10000PRM SAS HDD
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 SP1 EM64T (kernel 2.6.16.46-0.12-smp)
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	637	61.3	640	61.1	**639**	**61.2**	4	**550**	**71.0**	548	71.3	554	70.6
401.bzip2	4	1051	36.7	**1052**	**36.7**	1058	36.5	4	999	38.7	995	38.8	**997**	**38.7**
403.gcc	4	665	48.4	**665**	**48.4**	667	48.3	4	**665**	**48.4**	661	48.7	667	48.3
429.mcf	4	809	45.1	810	45.0	**810**	**45.1**	4	777	47.0	**776**	**47.0**	774	47.1
445.gobmk	4	**711**	**59.0**	711	59.0	709	59.1	4	654	64.2	648	64.7	**652**	**64.4**
456.hmmer	4	**763**	**48.9**	763	48.9	762	48.9	4	**449**	**83.2**	448	83.2	449	83.0
458.sjeng	4	875	55.3	**870**	**55.6**	869	55.7	4	798	60.6	800	60.5	**799**	**60.5**
462.libquantum	4	1807	45.9	**1807**	**45.9**	1806	45.9	1	291	71.3	289	71.7	**290**	**71.6**
464.h264ref	4	**952**	**93.0**	950	93.2	953	92.9	4	898	98.5	897	98.6	**898**	**98.6**
471.omnetpp	4	729	34.3	728	34.3	**729**	**34.3**	4	687	36.4	**686**	**36.4**	686	36.4
473.astar	4	**756**	**37.1**	758	37.0	756	37.1	4	700	40.1	**702**	**40.0**	703	40.0
483.xalancbmk	4	437	63.1	438	63.0	**438**	**63.0**	4	437	63.1	438	63.0	**438**	**63.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
This result was measured on the Acer Altos R520.
The Altos R720 and Altos R520 are electronically equivalent.

BIOS Settings
  Hardware Prefetcher set to ON

  Adjacent Cache Line Prefetch set to OFF

  Optimization for high-frequency FSB applications set to ON

All results used taskset to bind processes to cores.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.xml`

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