SPEC® CINT2006 Result

Fujitsu Siemens Computers

PRIMERGY BX620 S4, Intel Xeon processor E5420, 2.50 GHz

SPECint®_rate2006 =   97.4

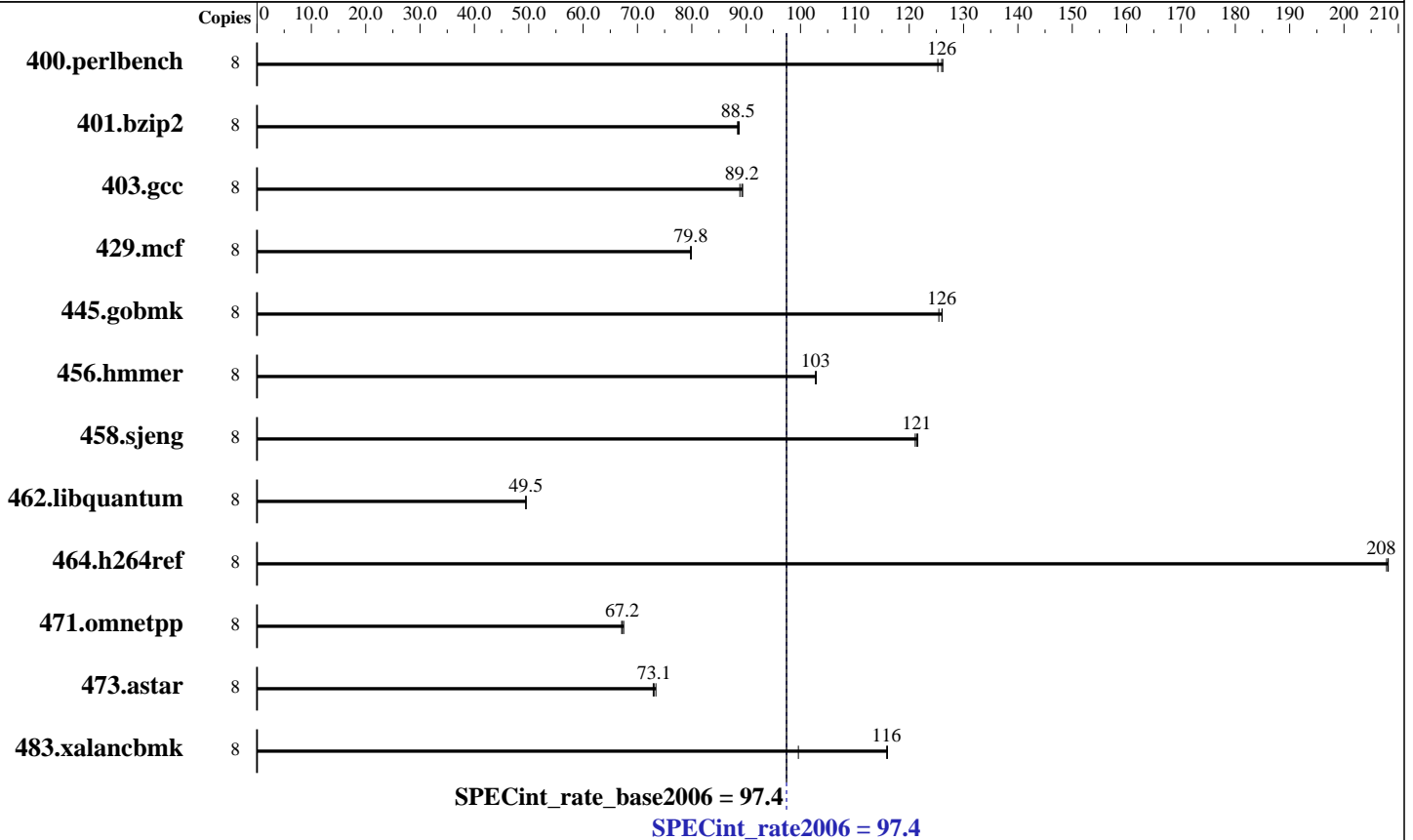SPECint_rate_base2006 = 97.4

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007

Copies	Benchmark	Result
8	400.perlbench	126
8	401.bzip2	88.5
8	403.gcc	89.2
8	429.mcf	79.8
8	445.gobmk	126
8	456.hmmer	103
8	458.sjeng	121
8	462.libquantum	49.5
8	464.h264ref	208
8	471.omnetpp	67.2
8	473.astar	73.1
8	483.xalancbmk	116

SPECint_rate_base2006 = 97.4

SPECint_rate2006 = 97.4

Hardware

CPU Name: Intel Xeon E5420

CPU Characteristics: 1333 MHz system bus

CPU MHz: 2500

FPU: Integrated

CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip

CPU(s) orderable: 1,2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

L3 Cache: None

Other Cache: None

Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)

Disk Subsystem: Seagate ST973451SS (SAS, 73GB, 15000rpm)

Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp

Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725

Auto Parallel: No

File System: ext2

System State: Multiuser, Runlevel 3

Base Pointers: 32-bit

Peak Pointers: 32-bit

Other Software: Smart Heap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY BX620 S4, Intel Xeon processor E5420, 2.50 GHz

SPECint_rate2006 = 97.4

SPECint_rate_base2006 = 97.4

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Nov-2007
Hardware Availability: Dec-2007
Software Availability: Nov-2007

Results Table

Benchmark		Base							Peak					
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**620**	**126**	624	125	619	126	8	**620**	**126**	624	125	619	126
401.bzip2	8	873	88.4	**872**	**88.5**	870	88.7	8	873	88.4	**872**	**88.5**	870	88.7
403.gcc	8	**722**	**89.2**	725	88.8	721	89.4	8	**722**	**89.2**	725	88.8	721	89.4
429.mcf	8	913	79.9	914	79.8	**914**	**79.8**	8	913	79.9	914	79.8	**914**	**79.8**
445.gobmk	8	**666**	**126**	666	126	669	125	8	**666**	**126**	666	126	669	125
456.hmmer	8	725	103	726	103	**726**	**103**	8	725	103	726	103	**726**	**103**
458.sjeng	8	796	122	**798**	**121**	799	121	8	796	122	**798**	**121**	799	121
462.libquantum	8	**3350**	**49.5**	3349	49.5	3351	49.5	8	**3350**	**49.5**	3349	49.5	3351	49.5
464.h264ref	8	852	208	**851**	**208**	851	208	8	852	208	**851**	**208**	851	208
471.omnetpp	8	741	67.5	**744**	**67.2**	745	67.1	8	741	67.5	**744**	**67.2**	745	67.1
473.astar	8	**769**	**73.1**	770	72.9	765	73.4	8	**769**	**73.1**	770	72.9	765	73.4
483.xalancbmk	8	476	116	554	99.6	**476**	**116**	8	476	116	554	99.6	**476**	**116**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com

Base Compiler Invocation

C benchmarks:
        icc

C++ benchmarks:
        icpc

Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY BX620 S4, Intel Xeon processor E5420, 2.50 GHz

SPECint_rate2006 =   97.4

SPECint_rate_base2006 = 97.4

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Nov-2007
Hardware Availability: Dec-2007
Software Availability: Nov-2007

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Optimization Flags

C benchmarks:

400.perlbench: basepeak = yes

401.bzip2: basepeak = yes

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY BX620 S4, Intel Xeon processor E5420, 2.50 GHz

SPECint_rate2006 = 97.4

SPECint_rate_base2006 = 97.4

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.06.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.06.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 13:48:38 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 11 December 2007.