SPEC® CINT2006 Result

Fujitsu Siemens Computers

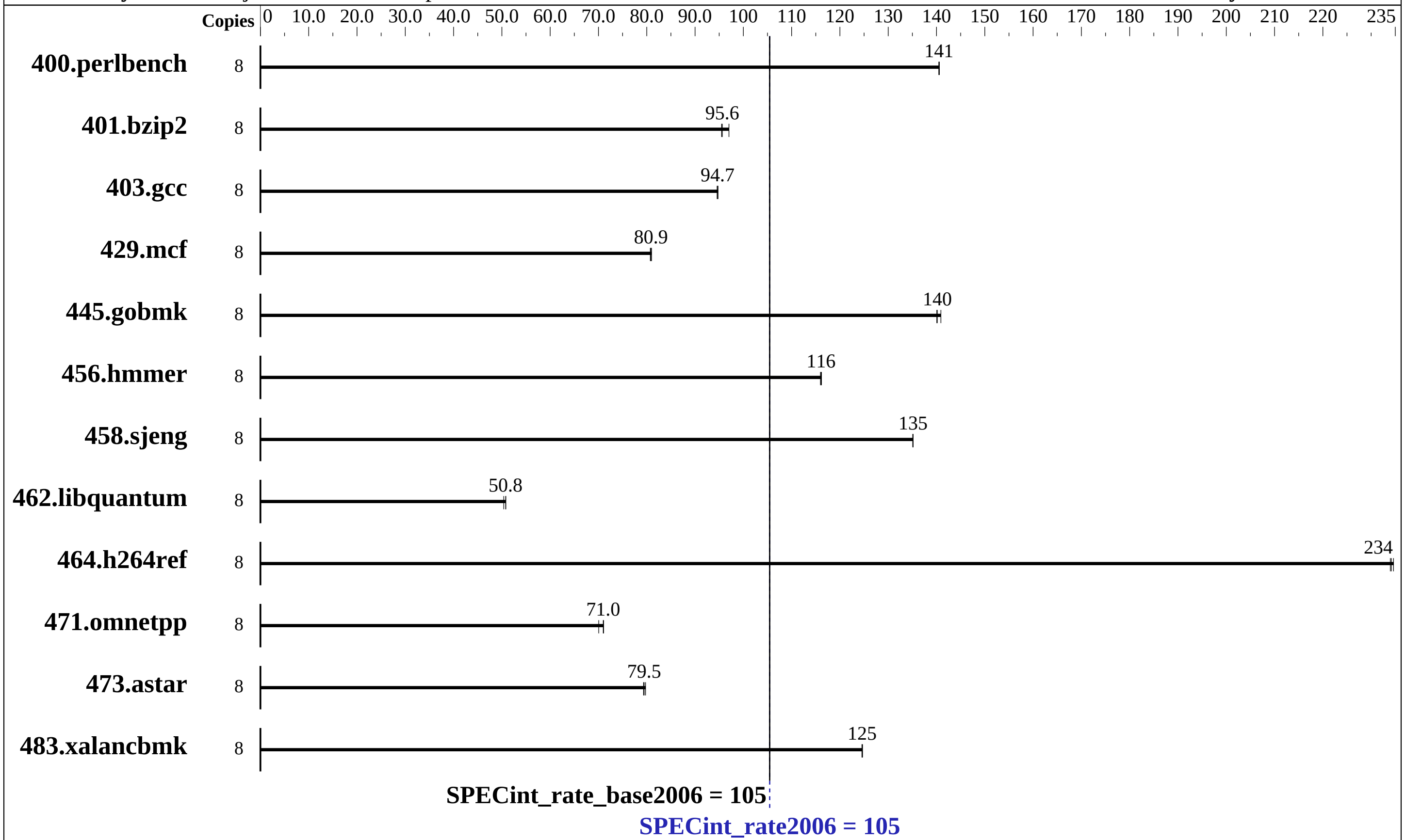
PRIMERGY RX300 S4, Intel Xeon processor E5440, 2.83 GHz

SPECint®_rate2006 = 105

SPECint_rate_base2006 = 105

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Oct-2007
Hardware Availability: Dec-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5440
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2833
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	Seagate ST973451SS (SAS, 73GB, 15000rpm)
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	Smart Heap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	556	140	**556**	**141**	556	141	8	556	140	**556**	**141**	556	141
401.bzip2	8	**807**	**95.6**	808	95.5	796	97.0	8	**807**	**95.6**	808	95.5	796	97.0
403.gcc	8	**680**	**94.7**	681	94.6	680	94.8	8	**680**	**94.7**	681	94.6	680	94.8
429.mcf	8	**902**	**80.9**	901	81.0	904	80.7	8	**902**	**80.9**	901	81.0	904	80.7
445.gobmk	8	599	140	**599**	**140**	596	141	8	599	140	**599**	**140**	596	141
456.hmmer	8	**643**	**116**	642	116	644	116	8	**643**	**116**	642	116	644	116
458.sjeng	8	**716**	**135**	717	135	716	135	8	**716**	**135**	717	135	716	135
462.libquantum	8	3289	50.4	**3264**	**50.8**	3261	50.8	8	3289	50.4	**3264**	**50.8**	3261	50.8
464.h264ref	8	755	235	757	234	**756**	**234**	8	755	235	757	234	**756**	**234**
471.omnetpp	8	714	70.1	**704**	**71.0**	704	71.1	8	714	70.1	**704**	**71.0**	704	71.1
473.astar	8	708	79.3	704	79.8	**707**	**79.5**	8	708	79.3	704	79.8	**707**	**79.5**
483.xalancbmk	8	443	125	443	125	**443**	**125**	8	443	125	443	125	**443**	**125**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Optimization Flags

C benchmarks:

400.perlbench: `basepeak = yes`

401.bzip2: `basepeak = yes`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `basepeak = yes`

456.hmmer: `basepeak = yes`

458.sjeng: `basepeak = yes`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

C++ benchmarks:

471.omnetpp: `basepeak = yes`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 14:33:38 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 November 2007.