



# SPEC<sup>®</sup> CINT2006 Result

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## Hewlett-Packard Company

SPECint<sup>®</sup>\_rate2006 = 112

ProLiant DL140 G3  
(3.0 GHz, Intel Xeon processor X5365)

SPECint\_rate\_base2006 = 94.1

CPU2006 license: 3

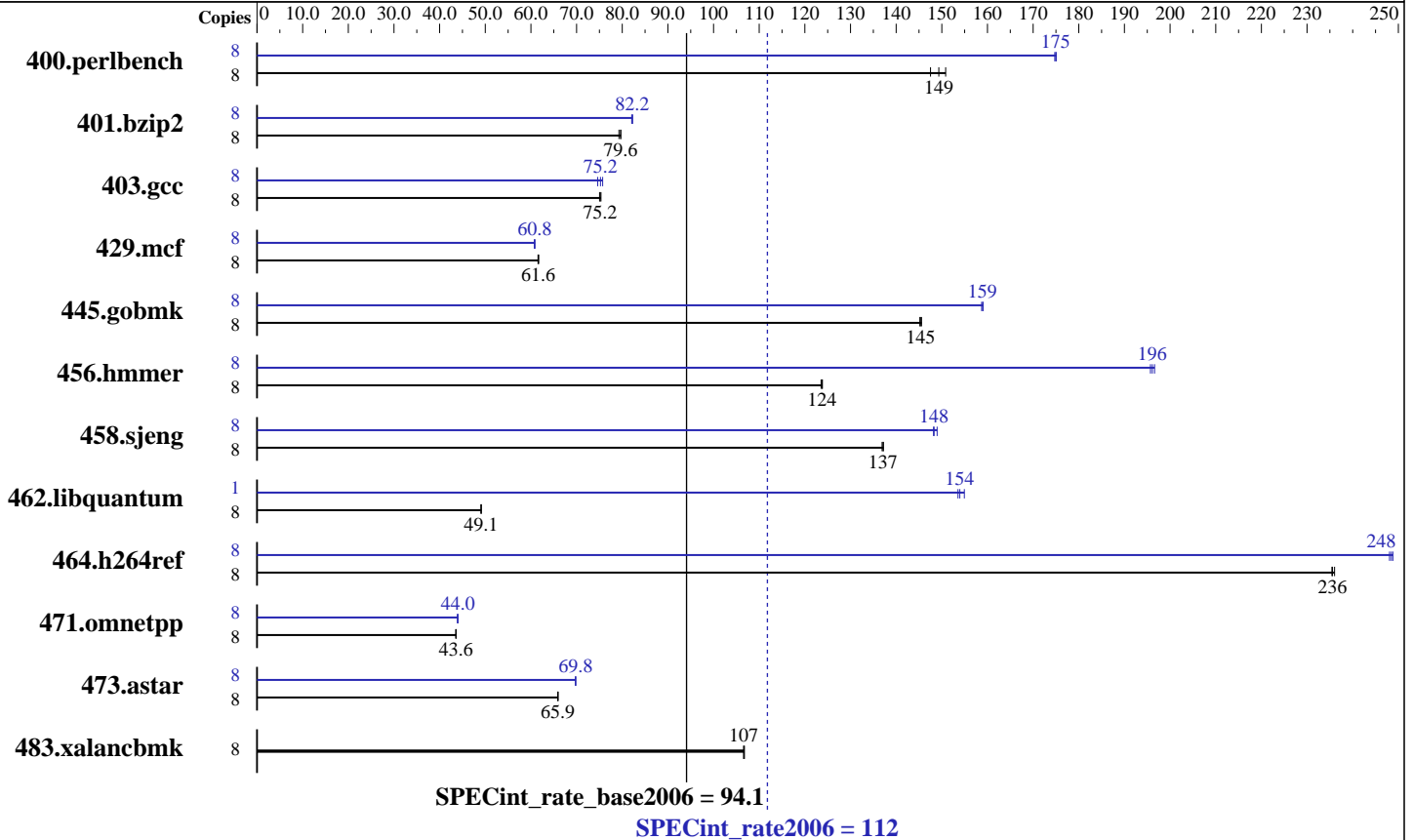
Test date: Oct-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5365  
 CPU Characteristics: 3.0 GHz, 2x4 MB L2 shared, 1333 MHz system bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1 or 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F CL5)  
 Disk Subsystem: 1x160 GB 7.2 K SATA  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1  
 kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and  
 Linux64 version 10.1  
 Build 20070725  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Multi-user run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library 8.1  
 binutils-2.17.50



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	530	148	518	151	<u>523</u>	<u>149</u>	8	<u>447</u>	<u>175</u>	446	175	447	175
401.bzip2	8	969	79.7	974	79.3	<u>969</u>	<u>79.6</u>	8	<u>939</u>	<u>82.2</u>	939	82.2	941	82.1
403.gcc	8	858	75.0	855	75.3	<u>857</u>	<u>75.2</u>	8	851	75.7	<u>856</u>	<u>75.2</u>	863	74.6
429.mcf	8	1182	61.7	<u>1184</u>	<u>61.6</u>	1185	61.6	8	<u>1200</u>	<u>60.8</u>	1201	60.8	1199	60.9
445.gobmk	8	<u>577</u>	<u>145</u>	577	146	578	145	8	529	159	<u>528</u>	<u>159</u>	528	159
456.hammer	8	<u>603</u>	<u>124</u>	603	124	604	124	8	<u>381</u>	<u>196</u>	381	196	380	197
458.sjeng	8	707	137	705	137	<u>706</u>	<u>137</u>	8	<u>653</u>	<u>148</u>	650	149	653	148
462.libquantum	8	<u>3377</u>	<u>49.1</u>	3376	49.1	3377	49.1	1	134	155	135	154	<u>135</u>	<u>154</u>
464.h264ref	8	752	235	<u>752</u>	<u>236</u>	750	236	8	<u>713</u>	<u>248</u>	714	248	712	249
471.omnetpp	8	<u>1147</u>	<u>43.6</u>	1147	43.6	1148	43.6	8	1137	44.0	<u>1137</u>	<u>44.0</u>	1139	43.9
473.astar	8	852	65.9	852	65.9	<u>852</u>	<u>65.9</u>	8	<u>805</u>	<u>69.8</u>	804	69.8	805	69.8
483.xalancbmk	8	517	107	<u>518</u>	<u>107</u>	518	107	8	517	107	<u>518</u>	<u>107</u>	518	107

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to physical,0  
KMP\_STACKSIZE set to 64M

## Platform Notes

BIOS configuration:  
Power Regulator set to Static High Performance Mode  
Adjacent Sector Prefetch Disabled

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

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## Base Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 14:23:57 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 27 November 2007.