



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp[®]_rate2006 = Not Run

IBM BladeCenter HS21 XM (Intel Xeon E5450)

SPECfp_rate_base2006 = 68.0

CPU2006 license: 11

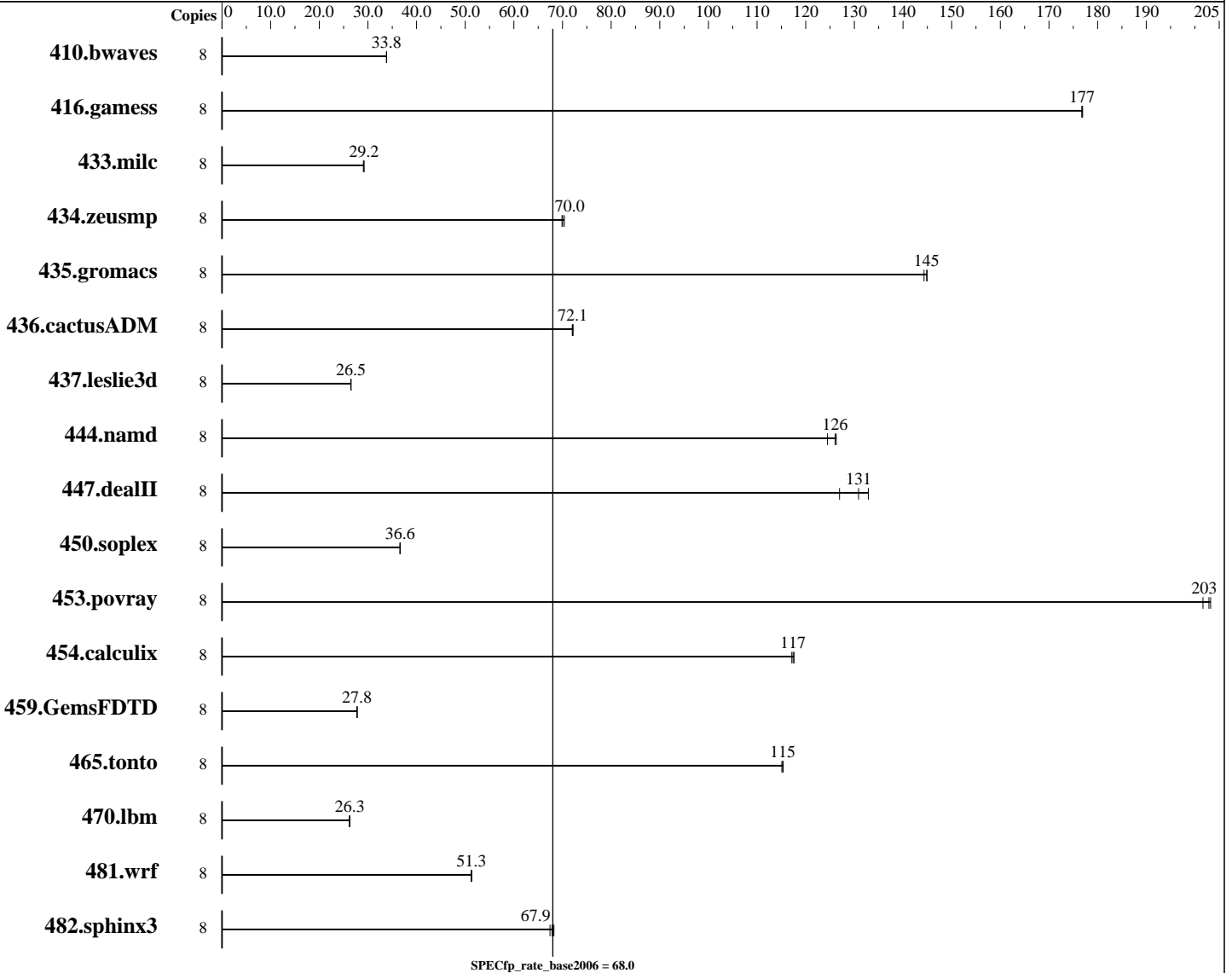
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Nov-2007

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5450
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System: SLES10 (x86_64), 2.6.16.21-0.8-smp
 Compiler: Intel C++ and Fortran Compiler 10.1 for Linux
 Build 20070913 Package ID: l_cc_p_10.1.008,
 l_fc_p_10.1.008
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = Not Run

IBM BladeCenter HS21 XM (Intel Xeon E5450)

SPECfp_rate_base2006 = 68.0

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Nov-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007

L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem: 1 x 36 GB SAS, 10000 RPM
Other Hardware: None

Other Software: None
Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3213	33.8	3215	33.8	<u>3213</u>	<u>33.8</u>							
416.gamess	8	886	177	885	177	<u>886</u>	<u>177</u>							
433.milc	8	<u>2516</u>	<u>29.2</u>	2516	29.2	2527	29.1							
434.zeusmp	8	1034	70.4	1042	69.9	<u>1039</u>	<u>70.0</u>							
435.gromacs	8	396	144	394	145	<u>394</u>	<u>145</u>							
436.cactusADM	8	<u>1326</u>	<u>72.1</u>	1325	72.2	1328	72.0							
437.leslie3d	8	<u>2838</u>	<u>26.5</u>	2838	26.5	2834	26.5							
444.namd	8	508	126	515	124	<u>509</u>	<u>126</u>							
447.dealII	8	<u>699</u>	<u>131</u>	689	133	721	127							
450.soplex	8	<u>1823</u>	<u>36.6</u>	1823	36.6	1822	36.6							
453.povray	8	211	202	209	203	<u>210</u>	<u>203</u>							
454.calculix	8	563	117	561	118	<u>562</u>	<u>117</u>							
459.GemsFDTD	8	3053	27.8	3059	27.8	<u>3057</u>	<u>27.8</u>							
465.tonto	8	682	115	684	115	<u>683</u>	<u>115</u>							
470.lbm	8	<u>4185</u>	<u>26.3</u>	4202	26.2	4185	26.3							
481.wrf	8	<u>1742</u>	<u>51.3</u>	1740	51.4	1745	51.2							
482.sphinx3	8	2312	67.4	2285	68.2	<u>2298</u>	<u>67.9</u>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 64-bit mode except 450.soplex, 470.lbm and 482.sphinx3, at peak, are compiled in 32-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
taskset utility used to bind CPU(s) to processes

Base Compiler Invocation

C benchmarks:
icc

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = Not Run

IBM BladeCenter HS21 XM (Intel Xeon E5450)

SPECfp_rate_base2006 = 68.0

CPU2006 license: 11

Test date: Nov-2007

Test sponsor: IBM Corporation

Hardware Availability: Jan-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = Not Run

IBM BladeCenter HS21 XM (Intel Xeon E5450)

SPECfp_rate_base2006 = 68.0

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Nov-2007

Hardware Availability: Jan-2008

Software Availability: Nov-2007

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.26.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.26.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 14:25:02 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 November 2007.