SPEC® CINT2006 Result

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Itautec

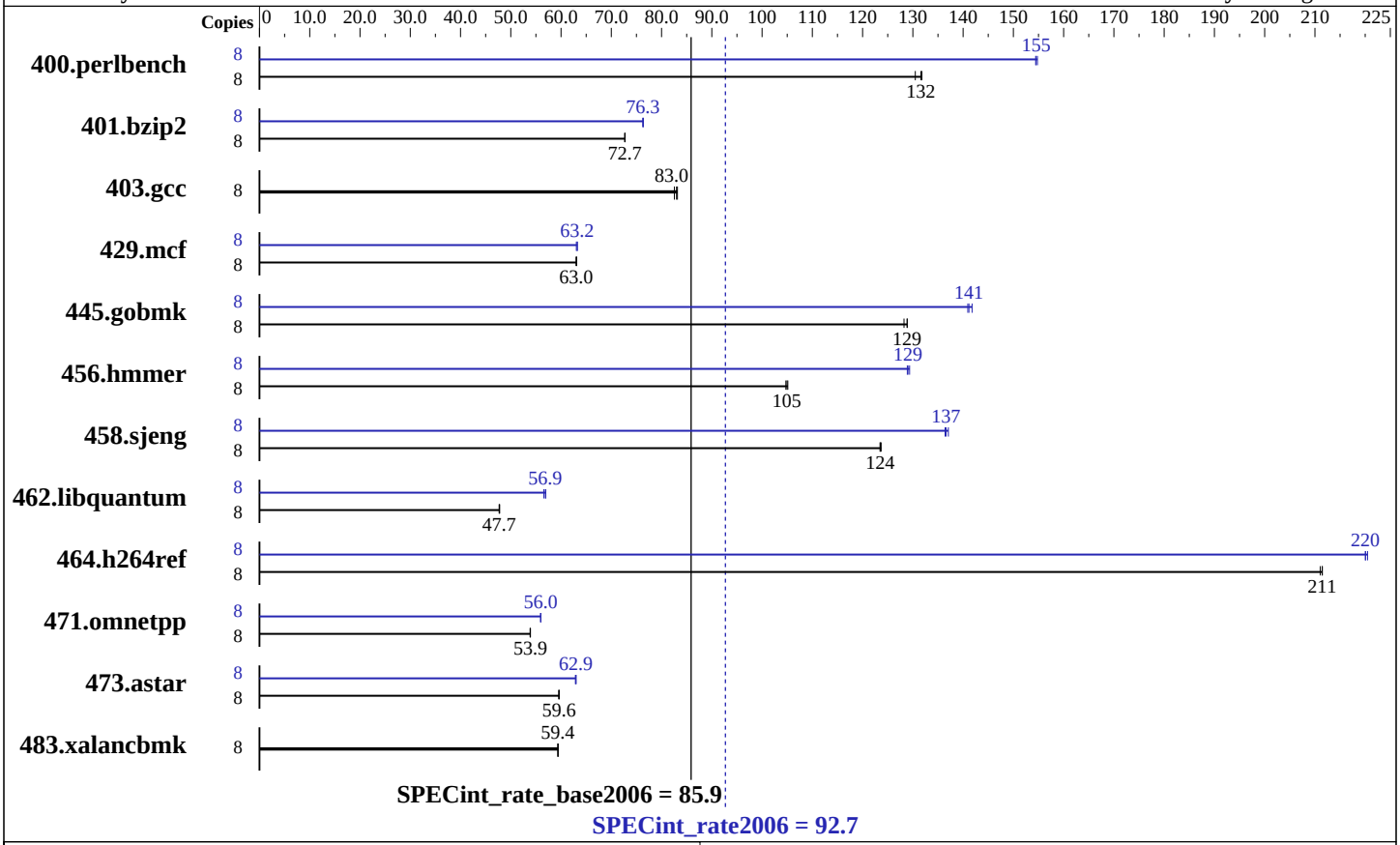
Servidor Itautec MX201 (Intel Xeon X5355)

SPECint®_rate2006 = 92.7

SPECint_rate_base2006 = 85.9

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Nov-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2007



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	Quad Core, 2.66 GHz
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 * 2GB DDR2 5300F, 2 rank, CL5 ECC)
Disk Subsystem:	73GB SCSI, 15000rpm
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Linux32 version 10.0 Build 20070809 Package ID: l_cc_p_10.0.026
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.16.91.0.7

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	593	132	**594**	**132**	599	130	8	505	155	**506**	**155**	506	154
401.bzip2	8	1062	72.7	1061	72.8	**1062**	**72.7**	8	**1012**	**76.3**	1011	76.4	1012	76.3
403.gcc	8	780	82.5	**776**	**83.0**	774	83.2	8	780	82.5	**776**	**83.0**	774	83.2
429.mcf	8	**1157**	**63.0**	1158	63.0	1156	63.1	8	1154	63.2	**1154**	**63.2**	1157	63.0
445.gobmk	8	651	129	**651**	**129**	654	128	8	595	141	592	142	**595**	**141**
456.hmmer	8	710	105	**711**	**105**	713	105	8	577	129	**578**	**129**	579	129
458.sjeng	8	**783**	**124**	783	124	784	123	8	710	136	706	137	**709**	**137**
462.libquantum	8	3468	47.8	3475	47.7	**3475**	**47.7**	8	**2914**	**56.9**	2930	56.6	2910	57.0
464.h264ref	8	**837**	**211**	837	211	839	211	8	**805**	**220**	805	220	803	220
471.omnetpp	8	927	53.9	929	53.8	**928**	**53.9**	8	894	56.0	893	56.0	**893**	**56.0**
473.astar	8	943	59.5	**942**	**59.6**	942	59.6	8	**892**	**62.9**	893	62.9	891	63.0
483.xalancbmk	8	929	59.4	**930**	**59.4**	930	59.4	8	929	59.4	**930**	**59.4**	930	59.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios settings:
Hardware Prefetcher:      Disabled
Adjacent Sector Prefetch: Disabled
ulimit -s unlimited used to set stack size to unlimited
taskset command used to assign copies to cores
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
This result was measured on the Servidor Itautec MX201.
The Servidor Itautec MX201 and the Servidor Itautec MX221 are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/10.0.026/bin/icc`

456.hmmer: `/opt/intel/cce/10.0.026/bin/icc`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-L/opt/intel/cce/10.0.026/lib -I/opt/intel/cce/10.0.026/include -prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

Continued on next page

Peak Optimization Flags (Continued)

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec_div -ansi-alias

456.hmmer: -L/opt/intel/cce/10.0.026/lib -I/opt/intel/cce/10.0.026/include -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-ic10-ia32-intel64-linux-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-ic10-ia32-intel64-linux-flags.xml

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