SPEC® CINT2006 Result

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Itautec

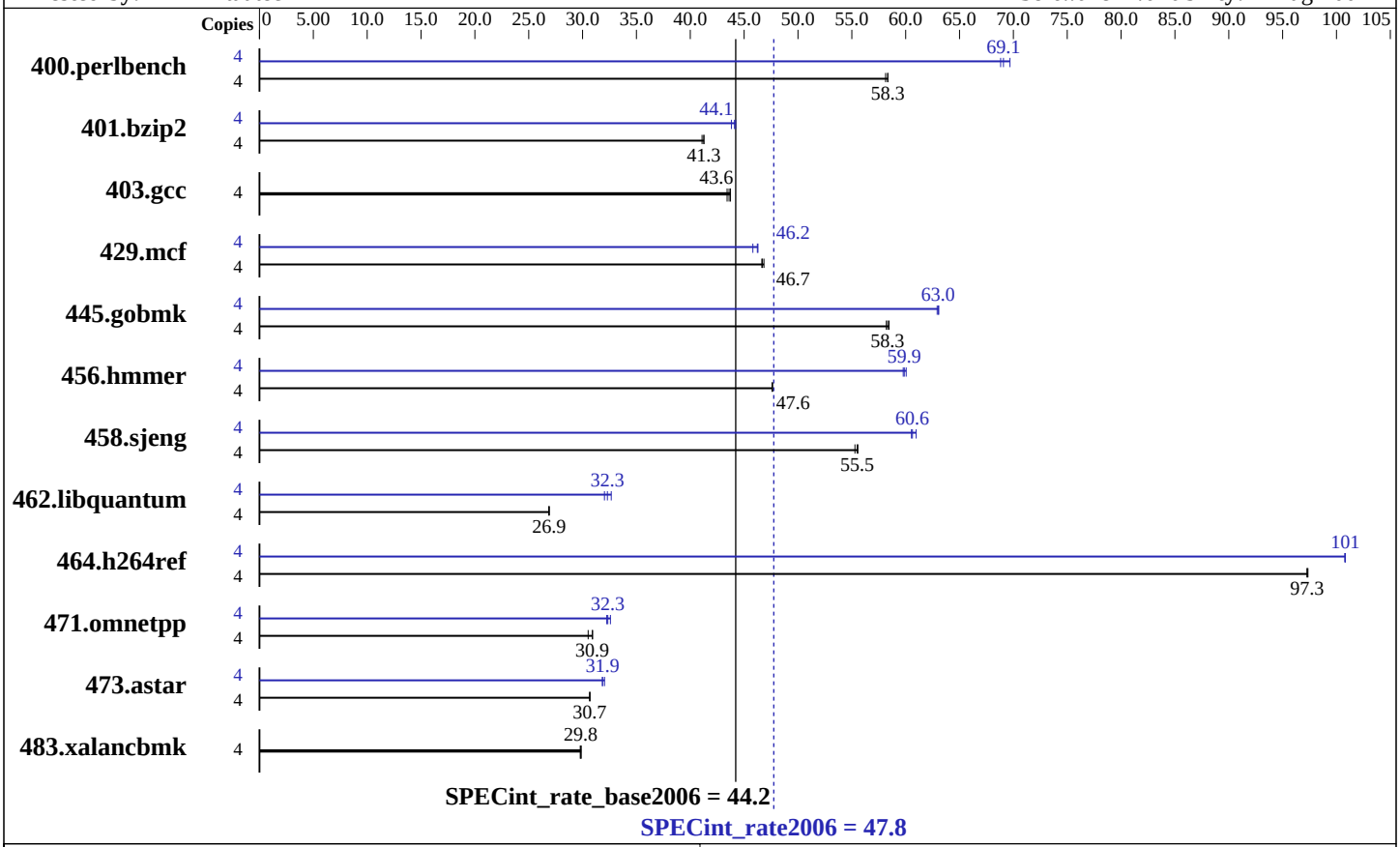
Servidor Itautec LX201 (Intel Xeon E5410)

SPECint®_rate2006 = 47.8

SPECint_rate_base2006 = 44.2

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Nov-2007
Hardware Availability: Dec-2007
Software Availability: Aug-2007



Hardware

CPU Name:	Intel Xeon E5410
CPU Characteristics:	
CPU MHz:	2330
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8 * 1GB DDR2 5300F, 2 rank, CL5 ECC)
Disk Subsystem:	SATA, 300GB, 7200rpm
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Linux32 version 10.0 Build 20070809 Package ID: l_cc_p_10.0.026
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.16.91.0.7

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**670**	**58.3**	670	58.3	672	58.1	4	**566**	**69.1**	568	68.8	561	69.7
401.bzip2	4	**935**	**41.3**	939	41.1	935	41.3	4	**875**	**44.1**	881	43.8	873	44.2
403.gcc	4	742	43.4	736	43.7	**738**	**43.6**	4	742	43.4	736	43.7	**738**	**43.6**
429.mcf	4	**781**	**46.7**	782	46.6	779	46.8	4	**789**	**46.2**	788	46.3	796	45.8
445.gobmk	4	718	58.5	**719**	**58.3**	721	58.2	4	**666**	**63.0**	665	63.1	667	62.9
456.hmmer	4	**784**	**47.6**	783	47.6	784	47.6	4	624	59.8	622	60.0	**623**	**59.9**
458.sjeng	4	875	55.3	871	55.6	**872**	**55.5**	4	794	61.0	**798**	**60.6**	800	60.5
462.libquantum	4	3080	26.9	3085	26.9	**3083**	**26.9**	4	2537	32.7	**2566**	**32.3**	2587	32.0
464.h264ref	4	909	97.3	**910**	**97.3**	910	97.3	4	878	101	**878**	**101**	878	101
471.omnetpp	4	**809**	**30.9**	808	30.9	819	30.5	4	767	32.6	775	32.2	**773**	**32.3**
473.astar	4	**916**	**30.7**	916	30.7	915	30.7	4	876	32.0	882	31.8	**882**	**31.9**
483.xalancbmk	4	924	29.9	927	29.8	**925**	**29.8**	4	924	29.9	927	29.8	**925**	**29.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios settings:
Hardware Prefetcher:      Disabled
Adjacent Sector Prefetch: Disabled
ulimit -s unlimited used to set stack size to unlimited
taskset command used to assign copies to cores
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/home/richard/intel/cce/10.0.026/bin/icc`

456.hmmer: `/home/richard/intel/cce/10.0.026/bin/icc`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-L/home/richard/intel/cce/10.0.026/lib -I/home/richard/intel/cce/10.0.026/include -prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

Continued on next page

Peak Optimization Flags (Continued)

403.gcc: `basepeak = yes`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec_div -ansi-alias`

456.hmmer: `-L/home/richard/intel/cce/10.0.026/lib -I/home/richard/intel/cce/10.0.026/include -prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-ic10-ia32-intel64-linux-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Itautec-ic10-ia32-intel64-linux-flags.20090714.xml`

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 14:23:01 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 November 2007.