SPEC® CINT2006 Result

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Supermicro

(Test Sponsor: Intel Corporation)

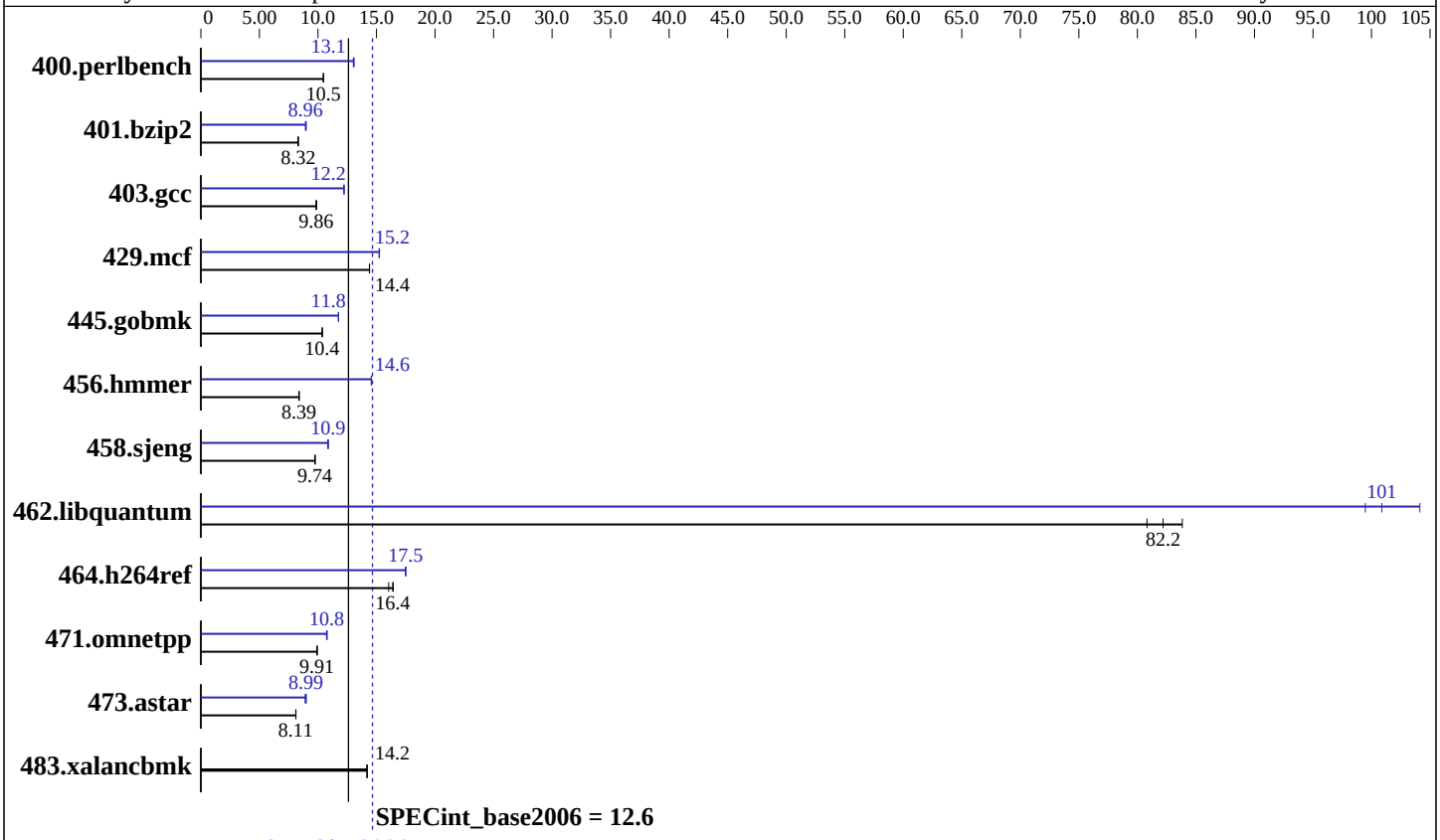
Supermicro X7DB8+ (Intel Xeon processor E5310, 1.60 GHz)

SPECint®2006 = 14.7

SPECint_base2006 = 12.6

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Aug-2007
Hardware Availability: Jul-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5310
CPU Characteristics:	Quad Core, 1.60 GHz
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (16*1GB Micron DDR2 4200F CL4-4-4, ECC)
Disk Subsystem:	Seagate, SCSI, 73GB, 10Krpm, 1 disk only
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10 SP1 RC1, Kernel linux-cbgm 2.6.16.43-0.5-smp for x86_64
Compiler:	Intel C++ Compiler for Linux32 and Linux64 Version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap library V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	934	10.5	**933**	**10.5**	933	10.5	747	13.1	**748**	**13.1**	750	13.0
401.bzip2	1158	8.33	1165	8.28	**1160**	**8.32**	1072	9.00	1081	8.93	**1077**	**8.96**
403.gcc	**817**	**9.86**	814	9.88	818	9.84	659	12.2	658	12.2	**659**	**12.2**
429.mcf	**633**	**14.4**	633	14.4	633	14.4	598	15.3	599	15.2	**599**	**15.2**
445.gobmk	1012	10.4	**1012**	**10.4**	1011	10.4	893	11.8	**893**	**11.8**	892	11.8
456.hmmer	1112	8.39	1113	8.39	**1113**	**8.39**	641	14.6	640	14.6	**641**	**14.6**
458.sjeng	1239	9.77	**1242**	**9.74**	1242	9.74	1111	10.9	**1113**	**10.9**	1116	10.8
462.libquantum	256	80.8	**252**	**82.2**	247	83.8	199	104	**205**	**101**	208	99.5
464.h264ref	**1351**	**16.4**	1346	16.4	1379	16.0	**1265**	**17.5**	1265	17.5	1264	17.5
471.omnetpp	628	9.95	**630**	**9.91**	631	9.91	**580**	**10.8**	580	10.8	582	10.7
473.astar	**865**	**8.11**	866	8.11	864	8.12	789	8.90	**781**	**8.99**	780	9.00
483.xalancbmk	486	14.2	486	14.2	**486**	**14.2**	486	14.2	486	14.2	**486**	**14.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios settings:
Hardware Prefetcher:      Enabled
Adjacent Sector Prefetch: Enabled
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -vec-guard-write -parallel -par-runtime-control`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc`
`-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib`
`-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include`

456.hmmer: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc`
`-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib`
`-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -auto-ilp32`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive -auto-ilp32`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.xml

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 14:56:09 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 16 October 2007.