SPEC® CINT2006 Result

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Acer Incorporated

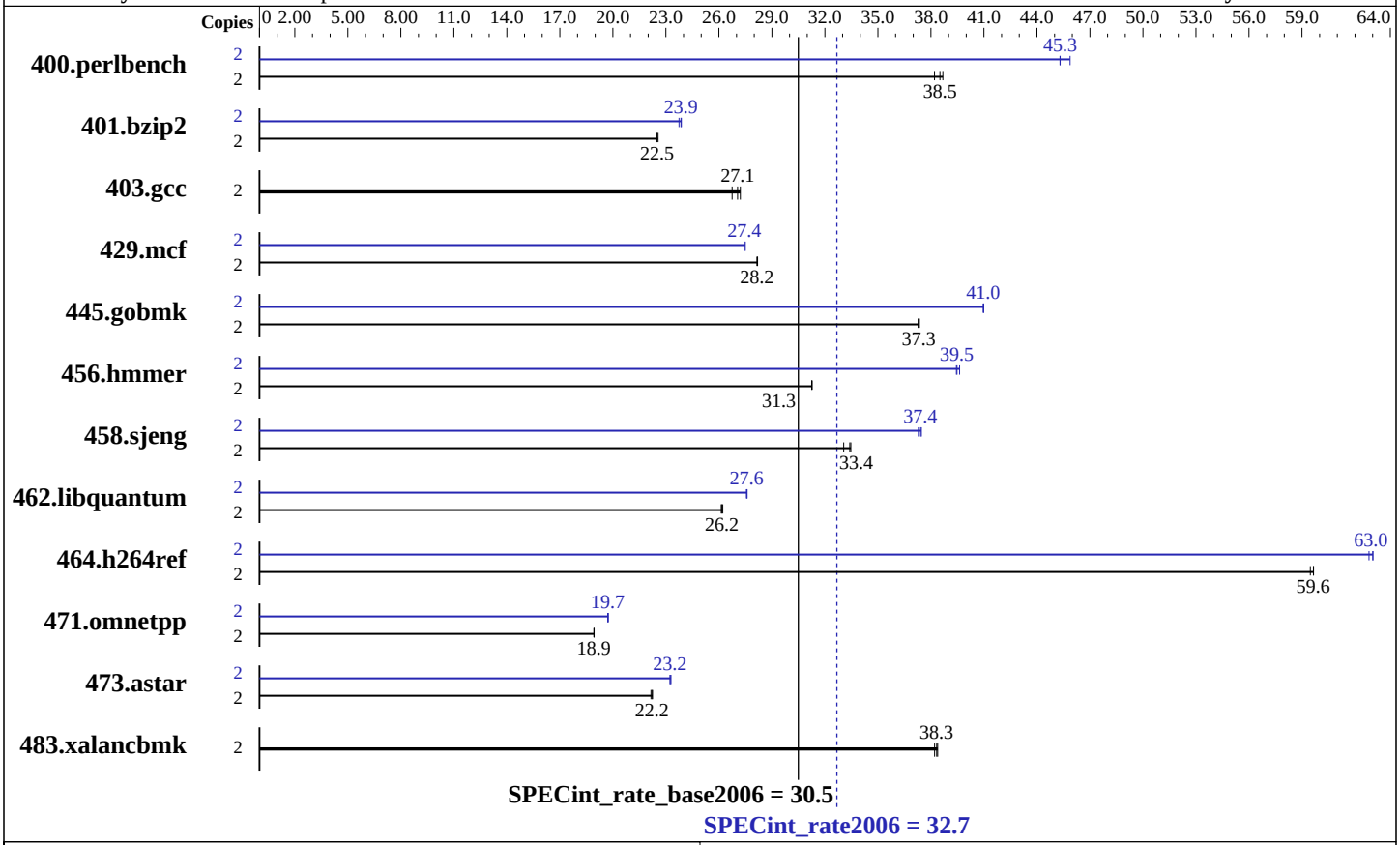
Acer Altos R720 (Intel Xeon 5160)

SPECint®_rate2006 = 32.7

SPECint_rate_base2006 = 30.5

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Aug-2007
Hardware Availability: Nov-2006
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon 5160
CPU Characteristics:	1333MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem:	1 x 73GB 10000RPM SAS HDD
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 EM64T kernel
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	511	38.2	**507**	**38.5**	505	38.7	2	426	45.9	431	45.3	**431**	**45.3**
401.bzip2	2	859	22.5	**858**	**22.5**	856	22.6	2	808	23.9	**809**	**23.9**	813	23.7
403.gcc	2	**595**	**27.1**	602	26.7	592	27.2	2	**595**	**27.1**	602	26.7	592	27.2
429.mcf	2	648	28.2	**648**	**28.2**	647	28.2	2	**665**	**27.4**	665	27.4	663	27.5
445.gobmk	2	**562**	**37.3**	563	37.3	562	37.3	2	**512**	**41.0**	512	41.0	512	41.0
456.hmmer	2	**597**	**31.3**	597	31.3	597	31.3	2	473	39.4	**473**	**39.5**	471	39.6
458.sjeng	2	732	33.1	723	33.5	**724**	**33.4**	2	649	37.3	**647**	**37.4**	646	37.5
462.libquantum	2	1580	26.2	**1584**	**26.2**	1586	26.1	2	1504	27.6	**1503**	**27.6**	1502	27.6
464.h264ref	2	742	59.7	744	59.5	**742**	**59.6**	2	702	63.0	705	62.8	**702**	**63.0**
471.omnetpp	2	660	18.9	661	18.9	**660**	**18.9**	2	**634**	**19.7**	634	19.7	633	19.7
473.astar	2	634	22.2	**632**	**22.2**	631	22.2	2	**604**	**23.2**	603	23.3	604	23.2
483.xalancbmk	2	360	38.4	361	38.2	**360**	**38.3**	2	360	38.4	361	38.2	**360**	**38.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
This result was measured on the Acer Altos R520
The Altos R720 and Altos R520 are electronically equivalent.

BIOS Settings
  Snoop Filter set to OFF
   Snoop Filter preserves cache coherency while
   minimizing snoops to remote nodes.

  Adjacent Cache Line Prefetch set to OFF
   Prefetch data in order to shorten execution cycles
   and maximize data processing efficiency.

  Optimization for high-frequency FSB applications set to ON

All results used taskset to bind processes to cores
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.23.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.23.xml

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