Supermicro
Motherboard C2SBA+

SPECint_rate2006 = 37.2
SPECint_rate_base2006 = 33.4

CPU2006 license: 001176
Test date: Sep-2007
Test sponsor: Supermicro
Hardware Availability: Jul-2007
Tested by: Supermicro
Software Availability: May-2007

Hardware

<table>
<thead>
<tr>
<th>Component</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Core 2 Duo E6850</td>
</tr>
<tr>
<td>CPU Characteristics</td>
<td>3.00GHz, 1333MHz Bus</td>
</tr>
<tr>
<td>CPU MHZ</td>
<td>3000</td>
</tr>
<tr>
<td>FPU</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled</td>
<td>2 cores, 1 chip, 2 cores/chip</td>
</tr>
<tr>
<td>CPU(s) orderable</td>
<td>1 chip</td>
</tr>
<tr>
<td>Primary Cache</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache</td>
<td>4 MB I+D on chip per chip</td>
</tr>
<tr>
<td>L3 Cache</td>
<td>None</td>
</tr>
<tr>
<td>Other Cache</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>8 GB (4 X 2GB PC2-6400, CL5)</td>
</tr>
<tr>
<td>Disk Subsystem</td>
<td>74GB SATA, 7200RPM</td>
</tr>
<tr>
<td>Other Hardware</td>
<td>None</td>
</tr>
</tbody>
</table>

Software

<table>
<thead>
<tr>
<th>Component</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating System</td>
<td>Windows Vista Ultimate (32-bit)</td>
</tr>
<tr>
<td>Compiler</td>
<td>Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)</td>
</tr>
<tr>
<td>Auto Parallel</td>
<td>No</td>
</tr>
<tr>
<td>File System</td>
<td>NTFS</td>
</tr>
<tr>
<td>System State</td>
<td>Default</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>32-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32-bit</td>
</tr>
<tr>
<td>Other Software</td>
<td>SmartHeap Library Version 8.0 from <a href="http://www.microquill.com/">http://www.microquill.com/</a></td>
</tr>
</tbody>
</table>
Supermicro
Motherboard C2SBA+

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SPECint_rate_base2006 = 33.4

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Base</td>
<td></td>
<td>Peak</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
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<td>Seconds</td>
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<td>Seconds</td>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>400.perlbench</td>
<td>2</td>
<td>473</td>
<td>41.4</td>
<td>471</td>
<td>41.5</td>
<td>473</td>
<td>41.3</td>
<td>471</td>
<td>41.5</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>2</td>
<td>708</td>
<td>27.3</td>
<td>706</td>
<td>27.3</td>
<td>710</td>
<td>27.2</td>
<td>688</td>
<td>28.1</td>
</tr>
<tr>
<td>403.gcc</td>
<td>2</td>
<td>787</td>
<td>20.5</td>
<td>787</td>
<td>20.5</td>
<td>788</td>
<td>20.4</td>
<td>513</td>
<td>31.4</td>
</tr>
<tr>
<td>429.mcf</td>
<td>2</td>
<td>478</td>
<td>38.2</td>
<td>479</td>
<td>38.1</td>
<td>495</td>
<td>36.9</td>
<td>478</td>
<td>38.2</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>2</td>
<td>536</td>
<td>39.1</td>
<td>537</td>
<td>39.1</td>
<td>539</td>
<td>39.0</td>
<td>486</td>
<td>43.2</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>2</td>
<td>597</td>
<td>31.2</td>
<td>597</td>
<td>31.2</td>
<td>602</td>
<td>31.0</td>
<td>591</td>
<td>31.6</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>2</td>
<td>687</td>
<td>35.2</td>
<td>687</td>
<td>35.2</td>
<td>688</td>
<td>35.2</td>
<td>614</td>
<td>39.4</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>2</td>
<td>1287</td>
<td>32.2</td>
<td>1278</td>
<td>32.4</td>
<td>1277</td>
<td>32.4</td>
<td>1017</td>
<td>40.8</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>2</td>
<td>730</td>
<td>60.6</td>
<td>730</td>
<td>60.6</td>
<td>731</td>
<td>60.6</td>
<td>689</td>
<td>64.2</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>2</td>
<td>501</td>
<td>24.9</td>
<td>502</td>
<td>24.9</td>
<td>501</td>
<td>24.9</td>
<td>455</td>
<td>27.5</td>
</tr>
<tr>
<td>473.astar</td>
<td>2</td>
<td>546</td>
<td>25.7</td>
<td>546</td>
<td>25.7</td>
<td>546</td>
<td>25.7</td>
<td>515</td>
<td>27.3</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>2</td>
<td>336</td>
<td>41.1</td>
<td>336</td>
<td>41.0</td>
<td>336</td>
<td>41.1</td>
<td>335</td>
<td>41.2</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with case CSE-733i-450
To ensure system stability, a 420W (minimum) ATX power supply [4-pin +12V AND (20 or 24-pin)] is required.
Product description located at:
http://www.supermicro.com/products/motherboard/Core2Duo/G33/C2SBA+II.cfm
The system bus runs at 1333 MHz
BIOS Setting : Default

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
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Base Optimization Flags

C benchmarks:
   -fast /F512000000 shlw32m.lib
   -link /FORCE:MULTIPLE

C++ benchmarks:
   -fast -Qcxx_features /F512000000 shlw32m.lib
   -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
   403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
   icl -Qvc7.1 -Qc99

C++ benchmarks:
   icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
   400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
   -Qprefetch /F512000000 shlw32m.lib
   -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
   shlw32m.lib
   -link /FORCE:MULTIPLE

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
   -link /FORCE:MULTIPLE

429.mcf: basepeak = yes

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**SPEC CINT2006 Result**

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<thead>
<tr>
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**CPU2006 license:** 001176  
**Test sponsor:** Supermicro  
**Tested by:** Supermicro

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**Peak Optimization Flags (Continued)**

- 445.gobmk:  
  - Qprof_gen(pass 1)  
  - Qprof_use(pass 2)  
  - QxT  
  - O2  
  - Qipo  
  - Qprec_div  
  - Qansi-alias /F5120000000  
  - link /FORCE:MULTIPLE

- 456.hmmer:  
  - Qprof_gen(pass 1)  
  - Qprof_use(pass 2)  
  - fast  
  - Qunroll2  
  - Qansi-alias /F5120000000 shlw32m.lib  
  - link /FORCE:MULTIPLE

- 458.sjeng:  
  - Qprof_gen(pass 1)  
  - Qprof_use(pass 2)  
  - fast  
  - Qunroll4  
  - shlw32m.lib  
  - link /FORCE:MULTIPLE

- 462.libquantum:  
  - Qprof_gen(pass 1)  
  - Qprof_use(pass 2)  
  - fast  
  - Qunroll4  
  - Ob0  
  - Qprefetch  
  - Qopt-streaming-stores:always /F5120000000 shlw32m.lib  
  - link /FORCE:MULTIPLE

- 464.h264ref: Same as 456.hmmer

**Peak Other Flags**

**C benchmarks:**

- 403.gcc: -Dalloca=alloca

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The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.xml

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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.  
Originally published on 2 October 2007.