SPEC® CINT2006 Result

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Fujitsu Siemens Computers

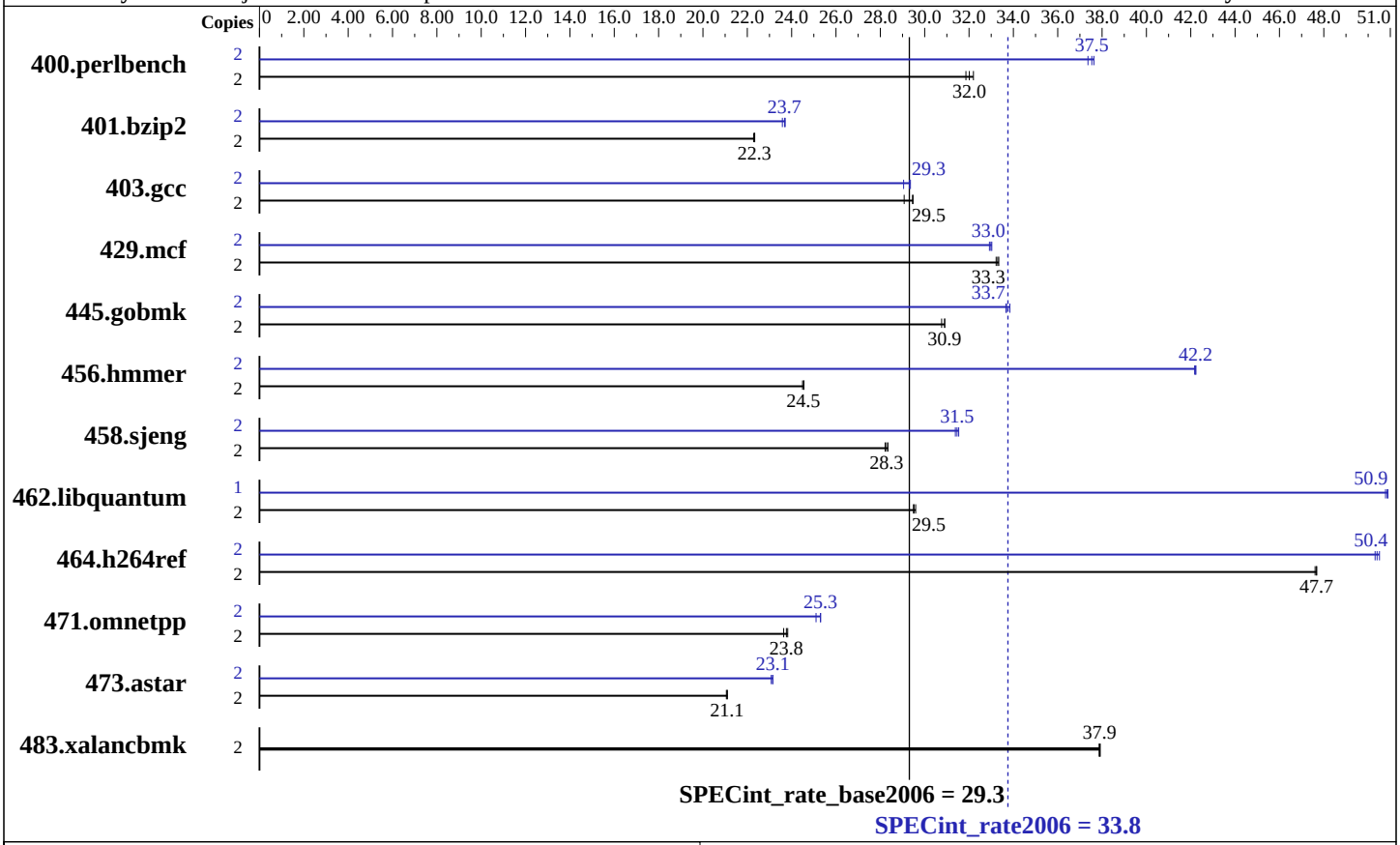
PRIMERGY TX150 S6, Intel Xeon processor 3065, 2.33 GHz

SPECint®_rate2006 = 33.8

SPECint_rate_base2006 = 29.3

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Sep-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon 3065
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4x1 GB DDR2 PC2-6400E, 2 rank, CAS 6-6-6, with ECC)
Disk Subsystem:	Fujtsu MAY2036RC (SAS, 36GB, 10000rpm)
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	613	31.9	**610**	**32.0**	607	32.2	2	523	37.4	519	37.6	**521**	**37.5**
401.bzip2	2	864	22.3	**864**	**22.3**	866	22.3	2	814	23.7	**815**	**23.7**	818	23.6
403.gcc	2	**547**	**29.5**	546	29.5	554	29.1	2	**549**	**29.3**	554	29.0	548	29.4
429.mcf	2	**548**	**33.3**	547	33.3	549	33.2	2	554	32.9	552	33.0	**553**	**33.0**
445.gobmk	2	682	30.8	**679**	**30.9**	679	30.9	2	620	33.8	**623**	**33.7**	623	33.7
456.hmmer	2	**760**	**24.5**	760	24.6	762	24.5	2	442	42.2	442	42.2	**442**	**42.2**
458.sjeng	2	854	28.3	858	28.2	**856**	**28.3**	2	767	31.5	**769**	**31.5**	771	31.4
462.libquantum	2	1405	29.5	1400	29.6	**1404**	**29.5**	1	407	50.9	**407**	**50.9**	408	50.8
464.h264ref	2	**928**	**47.7**	930	47.6	928	47.7	2	880	50.3	**878**	**50.4**	876	50.5
471.omnetpp	2	525	23.8	529	23.6	**526**	**23.8**	2	498	25.1	494	25.3	**494**	**25.3**
473.astar	2	**666**	**21.1**	665	21.1	667	21.1	2	606	23.1	608	23.1	**607**	**23.1**
483.xalancbmk	2	364	37.9	364	37.9	**364**	**37.9**	2	364	37.9	364	37.9	**364**	**37.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc`
`-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib`
`-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include`

456.hmmer: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc`
`-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib`
`-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include`

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.xml`

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For other inquiries, please contact webmaster@spec.org.

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