



# SPEC® CINT2006 Result

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## Acer Incorporated

## SPECint®\_rate2006 = 36.5

## Acer Altos R520 (Intel Xeon E5310)

## SPECint\_rate\_base2006 = 33.6

CPU2006 license: 97

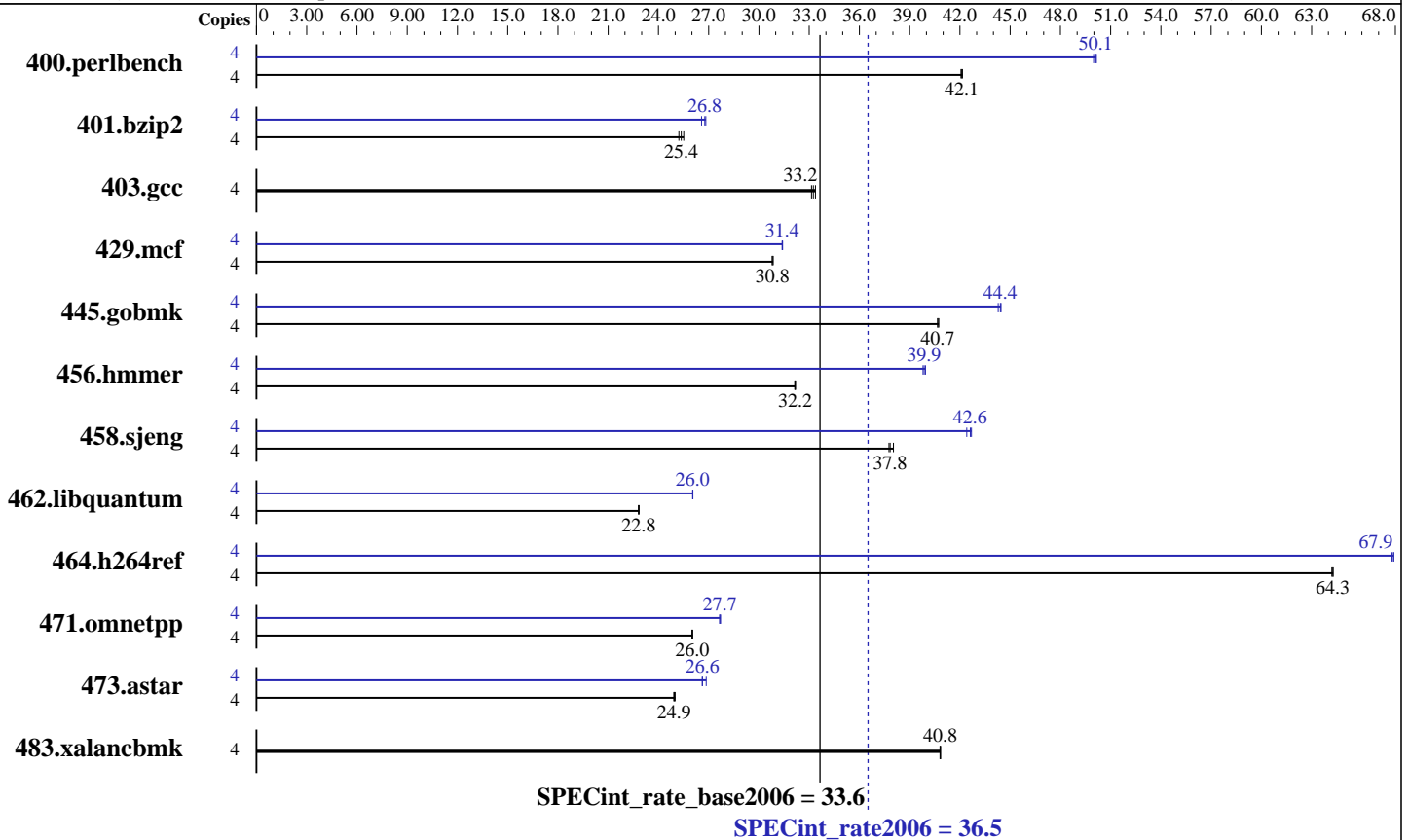
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Aug-2007

Hardware Availability: Nov-2006

Software Availability: Jun-2007



### Hardware

CPU Name: Intel Xeon E5310  
 CPU Characteristics: 1066MHz system bus  
 CPU MHz: 1600  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)  
 Disk Subsystem: 1 x 73GB 10000RPM SAS HDD  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 EM64T kernel  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.0  
 Build 20070426 Package ID: l\_cc\_p\_10.0.023  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multi-user, level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library 8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	929	42.1	927	42.2	<b>928</b>	<b>42.1</b>	4	782	50.0	<b>780</b>	<b>50.1</b>	779	50.1
401.bzip2	4	1513	25.5	<b>1522</b>	<b>25.4</b>	1530	25.2	4	1453	26.6	<b>1442</b>	<b>26.8</b>	1439	26.8
403.gcc	4	965	33.4	<b>968</b>	<b>33.2</b>	972	33.1	4	965	33.4	<b>968</b>	<b>33.2</b>	972	33.1
429.mcf	4	1182	30.9	1185	30.8	<b>1184</b>	<b>30.8</b>	4	1162	31.4	<b>1162</b>	<b>31.4</b>	1161	31.4
445.gobmk	4	1032	40.7	1030	40.7	<b>1032</b>	<b>40.7</b>	4	947	44.3	<b>945</b>	<b>44.4</b>	944	44.5
456.hammer	4	1160	32.2	<b>1160</b>	<b>32.2</b>	1161	32.1	4	<b>936</b>	<b>39.9</b>	938	39.8	934	39.9
458.sjeng	4	<b>1279</b>	<b>37.8</b>	1282	37.8	1273	38.0	4	<b>1136</b>	<b>42.6</b>	1134	42.7	1141	42.4
462.libquantum	4	<b>3632</b>	<b>22.8</b>	3628	22.8	3633	22.8	4	<b>3182</b>	<b>26.0</b>	3182	26.0	3181	26.1
464.h264ref	4	1377	64.3	1379	64.2	<b>1377</b>	<b>64.3</b>	4	1306	67.8	<b>1304</b>	<b>67.9</b>	1304	67.9
471.omnetpp	4	961	26.0	960	26.0	<b>961</b>	<b>26.0</b>	4	904	27.7	<b>904</b>	<b>27.7</b>	902	27.7
473.astar	4	<b>1126</b>	<b>24.9</b>	1127	24.9	1123	25.0	4	1046	26.9	<b>1055</b>	<b>26.6</b>	1055	26.6
483.xalancbmk	4	<b>676</b>	<b>40.8</b>	676	40.8	676	40.8	4	<b>676</b>	<b>40.8</b>	676	40.8	676	40.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

This result was measured on the Acer Altos R520  
The Altos R720 and Altos R520 are electronically equivalent.

### BIOS Settings

Snoop Filter set to OFF

Snoop Filter preserves cache coherency while  
minimizing snoops to remote nodes.

Adjacent Cache Line Prefetch set to OFF

Prefetch data in order to shorten execution cycles  
and maximize data processing efficiency.

Optimization for high-frequency FSB applications set to ON

taskset was used to bind copies to cores

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec\_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0  
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec\_div -ansi-alias -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.16.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.16.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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