



# SPEC® CINT2006 Result

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Dell Inc.  
(Test Sponsor: Intel Corporation)

SPECint®\_rate2006 = 20.8

XPS M1710 (Intel Core Duo T2600) SPECint\_rate\_base2006 = 19.1

CPU2006 license: 13

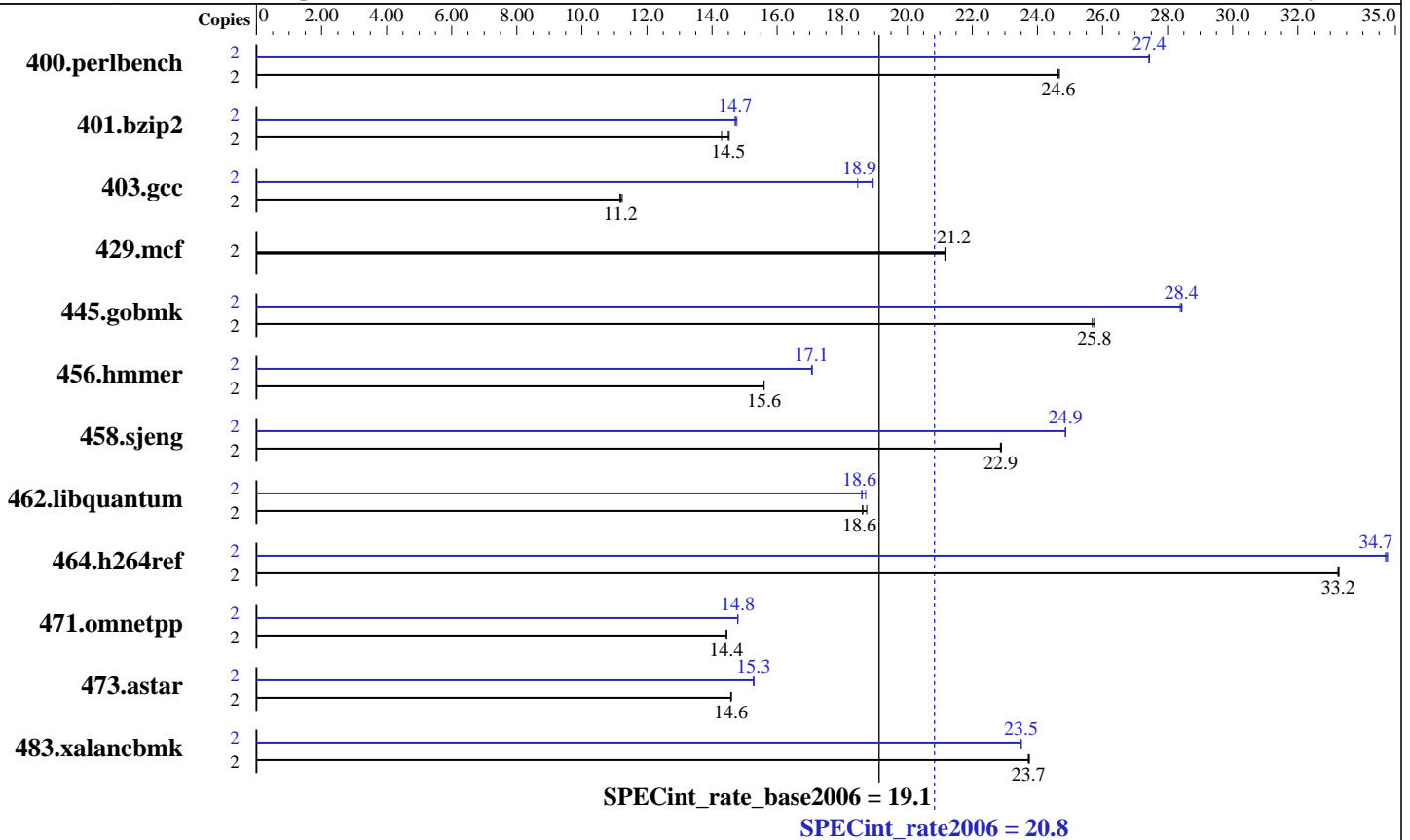
Test date: Jul-2007

Test sponsor: Intel Corporation

Hardware Availability: Jul-2007

Tested by: Intel Corporation

Software Availability: May-2007



## Hardware

CPU Name: Intel Core Duo T2600  
 CPU Characteristics:  
 CPU MHz: 2167  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 2 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 2 GB (2x1GB Qimonda DDR2-667 CL5)  
 Disk Subsystem: 100GB SATA, 7200 RPM  
 Other Hardware: None

## Software

Operating System: Windows Vista Ultimate (32-bit)  
 Compiler: Intel C++ Compiler for IA32 version 10.0  
 Build 20070426 Package ID: W\_CC\_P\_10.0.025  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: SmartHeap Library Version 8.0 from <http://www.microquill.com/>



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	<b>793</b>	<b>24.6</b>	792	24.7	793	24.6	2	<b>712</b>	<b>27.4</b>	713	27.4	712	27.4
401.bzip2	2	1350	14.3	1329	14.5	<b>1331</b>	<b>14.5</b>	2	1307	14.8	1312	14.7	<b>1312</b>	<b>14.7</b>
403.gcc	2	<b>1439</b>	<b>11.2</b>	1432	11.2	1442	11.2	2	871	18.5	850	19.0	<b>850</b>	<b>18.9</b>
429.mcf	2	861	21.2	862	21.2	<b>861</b>	<b>21.2</b>	2	861	21.2	862	21.2	<b>861</b>	<b>21.2</b>
445.gobmk	2	814	25.8	817	25.7	<b>815</b>	<b>25.8</b>	2	738	28.4	739	28.4	<b>738</b>	<b>28.4</b>
456.hammer	2	1196	15.6	<b>1196</b>	<b>15.6</b>	1197	15.6	2	1092	17.1	<b>1094</b>	<b>17.1</b>	1094	17.1
458.sjeng	2	1059	22.9	1057	22.9	<b>1058</b>	<b>22.9</b>	2	974	24.9	<b>973</b>	<b>24.9</b>	973	24.9
462.libquantum	2	2210	18.8	<b>2224</b>	<b>18.6</b>	2225	18.6	2	2228	18.6	2214	18.7	<b>2227</b>	<b>18.6</b>
464.h264ref	2	1332	33.2	1330	33.3	<b>1331</b>	<b>33.2</b>	2	<b>1274</b>	<b>34.7</b>	1276	34.7	1273	34.8
471.omnetpp	2	866	14.4	<b>865</b>	<b>14.4</b>	865	14.4	2	845	14.8	845	14.8	<b>845</b>	<b>14.8</b>
473.astar	2	962	14.6	<b>963</b>	<b>14.6</b>	963	14.6	2	919	15.3	<b>919</b>	<b>15.3</b>	919	15.3
483.xalancbmk	2	581	23.8	582	23.7	<b>582</b>	<b>23.7</b>	2	587	23.5	588	23.5	<b>587</b>	<b>23.5</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

nVidia GeForce Go 7900 discrete graphics card  
Binaries were built on Windows XP Professional SP2  
The start command with the /affinity switch was used to bind processes to cores

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-QxP -Qipo -O3 -Qprec-div- /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

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## Base Optimization Flags (Continued)

C++ benchmarks:  
-QxP -Qipo -O3 -Qprec-div- -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Peak Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Peak Optimization Flags

C benchmarks:  
400.perlbench: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -Qipo -O3  
-Qprec-div- -Qansi-alias -Qprefetch /F512000000  
shlw32m.lib -link /FORCE:MULTIPLE  
401.bzip2: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -Qipo -O3  
-Qprec-div- /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE  
403.gcc: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -Qipo -O3  
-Qprec-div- /F512000000 -link /FORCE:MULTIPLE  
429.mcf: basepeak = yes  
445.gobmk: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -O2 -Qipo  
-Qprec\_div- -Qansi-alias /F512000000  
-link /FORCE:MULTIPLE

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## Peak Optimization Flags (Continued)

```
456.hmmcr: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
           -Qprec-div- -Qunroll2 -Qansi-alias /F512000000
           shlw32m.lib -link /FORCE:MULTIPLE
```

```
458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
           -Qprec-div- -Qunroll4 /F512000000 shlw32m.lib
           -link /FORCE:MULTIPLE
```

```
462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
                -Qprec-div- -Qunroll4 -Ob0 -Qprefetch
                -Qopt-streaming-stores:always /F512000000 shlw32m.lib
                -link /FORCE:MULTIPLE
```

464.h264ref: Same as 456.hmmcr

C++ benchmarks:

```
-Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3 -Qprec-div-
-Qansi-alias -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.47.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.47.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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