SPEC® CFP2006 Result

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Supermicro

(Test Sponsor: Intel Corporation)

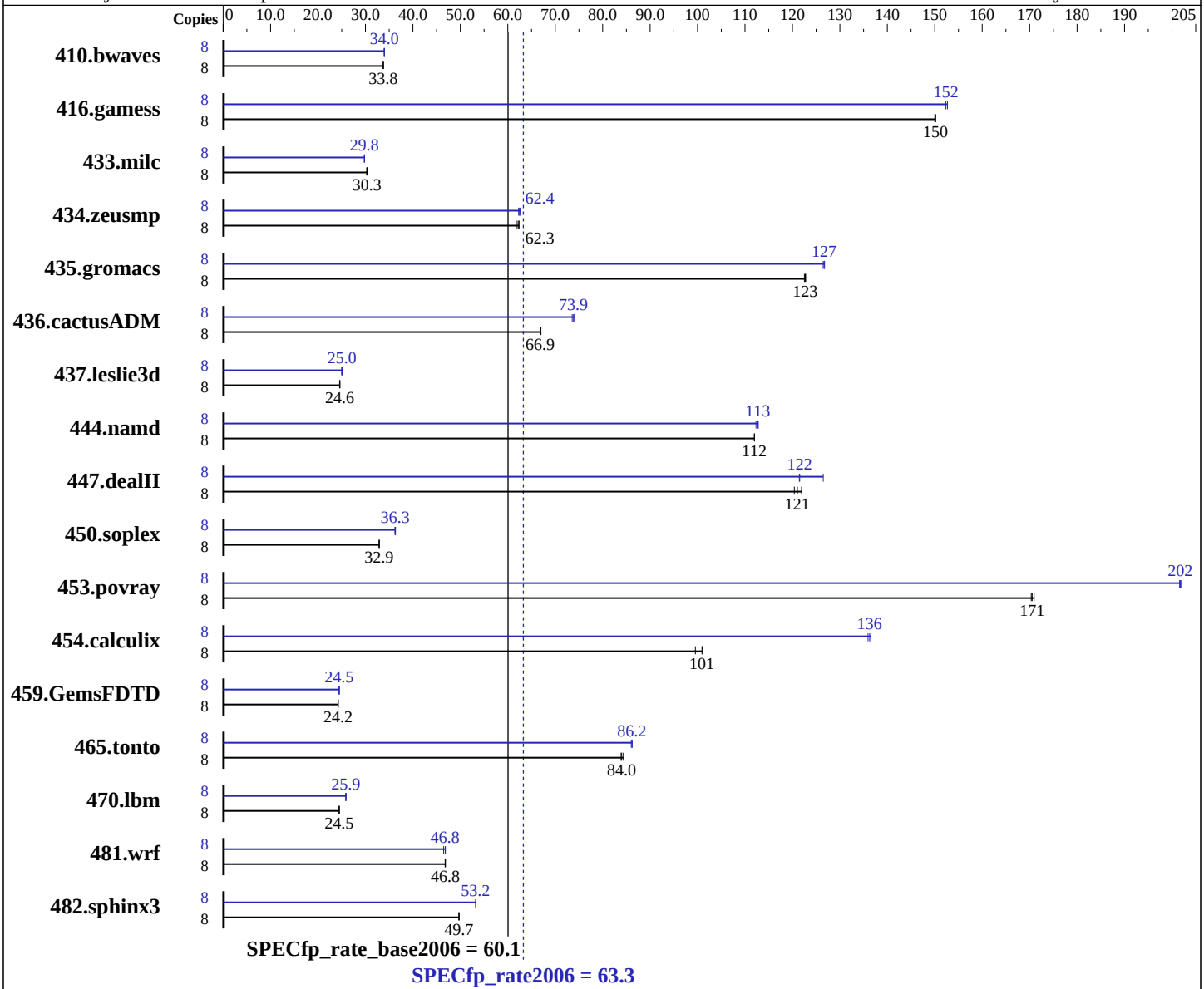
Supermicro X7DB8+ (Intel Xeon processor X5355, 2.66 GHz)

SPECfp®_rate2006 = 63.3

SPECfp_rate_base2006 = 60.1

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Aug-2007
Hardware Availability: Jul-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	Quad Core, 2.66 GHz
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10 SP1, Kernel linux-cbgm 2.6.16.43-0.5-smp for x86_64
Compiler:	Intel C++ and Fortran Compiler for Linux32 and Linux64 Version 10.1 Build 20070725
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 * 2GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3219	33.8	3222	33.7	**3221**	**33.8**	8	**3200**	**34.0**	3201	34.0	3199	34.0
416.gamess	8	1044	150	1043	150	**1043**	**150**	8	1029	152	**1028**	**152**	1026	153
433.milc	8	**2423**	**30.3**	2426	30.3	2421	30.3	8	**2466**	**29.8**	2468	29.8	2463	29.8
434.zeusmp	8	1175	62.0	**1169**	**62.3**	1167	62.4	8	**1167**	**62.4**	1163	62.6	1169	62.3
435.gromacs	8	465	123	466	123	**465**	**123**	8	**451**	**127**	452	126	451	127
436.cactusADM	8	1427	67.0	**1430**	**66.9**	1431	66.8	8	**1294**	**73.9**	1299	73.6	1293	73.9
437.leslie3d	8	3064	24.5	3056	24.6	**3056**	**24.6**	8	3010	25.0	**3003**	**25.0**	3000	25.1
444.namd	8	**573**	**112**	573	112	575	112	8	569	113	571	112	**569**	**113**
447.dealII	8	751	122	760	120	**756**	**121**	8	**753**	**122**	723	126	754	121
450.soplex	8	2029	32.9	**2029**	**32.9**	2027	32.9	8	1843	36.2	1836	36.3	**1838**	**36.3**
453.povray	8	**250**	**171**	249	171	250	170	8	211	202	**211**	**202**	211	202
454.calculix	8	**654**	**101**	663	99.5	653	101	8	483	137	486	136	**484**	**136**
459.GemsFDTD	8	3504	24.2	3499	24.3	**3502**	**24.2**	8	**3470**	**24.5**	3472	24.4	3468	24.5
465.tonto	8	**937**	**84.0**	939	83.9	933	84.4	8	**913**	**86.2**	915	86.0	913	86.2
470.lbm	8	4494	24.5	**4494**	**24.5**	4493	24.5	8	**4248**	**25.9**	4248	25.9	4248	25.9
481.wrf	8	1908	46.8	**1908**	**46.8**	1910	46.8	8	1907	46.8	1923	46.5	**1910**	**46.8**
482.sphinx3	8	3135	49.7	**3137**	**49.7**	3137	49.7	8	2929	53.2	2926	53.3	**2928**	**53.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios settings:
Hardware Prefetcher:      Disabled
Adjacent Sector Prefetch: Disabled
All benchmarks compiled in 64-bit mode except 437.leslie3d,
450.soplex, 470.lbm and 482.sphinx3, for peak, are
compiled in 32-bit mode
The taskset command was used to bind processes to cores
```

Base Compiler Invocation

C benchmarks:

```
icc
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks (except as noted below):
```
/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include
```

433.milc: `icc`

C++ benchmarks (except as noted below):
```
icpc
```

450.soplex: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/icpc -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include`

Fortran benchmarks (except as noted below):
```
ifort
```

437.leslie3d: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/ifort -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include`

Benchmarks using both Fortran and C:
```
icc ifort
```

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

Continued on next page

Peak Optimization Flags (Continued)

470.lbm: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -scalar-rep- -prefetch -opt-malloc-options=3`

482.sphinx3: `-fast -unroll2`

C++ benchmarks:

444.namd: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

447.dealII: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias -scalar-rep-`

450.soplex: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -opt-malloc-options=3`

453.povray: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -ansi-alias`

Fortran benchmarks:

410.bwaves: `-fast -prefetch`

416.gamess: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -Ob0 -ansi-alias -scalar-rep-`

434.zeusmp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

437.leslie3d: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -opt-malloc-options=3`

459.GemsFDTD: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -Ob0 -prefetch`

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -auto-ilp32`

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -prefetch -auto-ilp32`

454.calculix: `-fast -unroll-aggressive -auto-ilp32`

481.wrf: `-fast -auto-ilp32`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.32.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.32.xml

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 13:11:26 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 4 September 2007.