SPEC® CINT2006 Result

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Fujitsu Siemens Computers

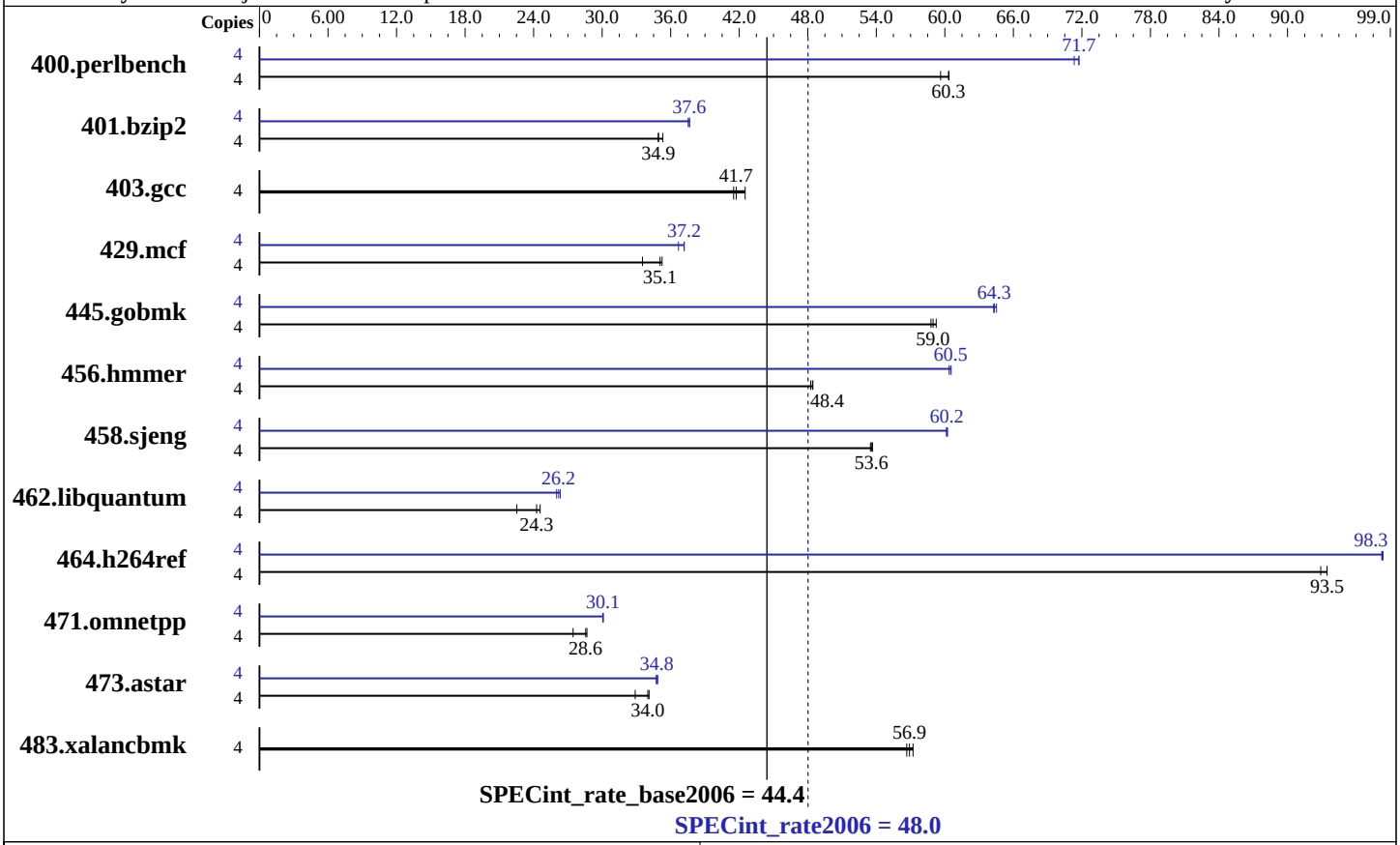
PRIMERGY TX200 S3, Intel Xeon processor E5345, 2.33 GHz

SPECint®_rate2006 = 48.0

SPECint_rate_base2006 = 44.4

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Aug-2007
Hardware Availability: Jan-2007
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon E5345
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	Seagate ST336754SS (SAS, 36GB, 15000rpm)
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**648**	**60.3**	647	60.4	655	59.6	4	548	71.3	**545**	**71.7**	545	71.7
401.bzip2	4	**1105**	**34.9**	1106	34.9	1093	35.3	4	1029	37.5	1025	37.7	**1026**	**37.6**
403.gcc	4	776	41.5	**772**	**41.7**	757	42.5	4	776	41.5	**772**	**41.7**	757	42.5
429.mcf	4	1088	33.5	**1040**	**35.1**	1035	35.2	4	994	36.7	981	37.2	**981**	**37.2**
445.gobmk	4	714	58.8	**712**	**59.0**	708	59.2	4	**652**	**64.3**	653	64.3	650	64.5
456.hmmer	4	773	48.3	**771**	**48.4**	770	48.5	4	616	60.6	618	60.4	**617**	**60.5**
458.sjeng	4	905	53.5	**903**	**53.6**	902	53.7	4	803	60.2	**804**	**60.2**	805	60.1
462.libquantum	4	3678	22.5	**3416**	**24.3**	3373	24.6	4	3186	26.0	**3164**	**26.2**	3147	26.3
464.h264ref	4	953	92.9	**947**	**93.5**	947	93.5	4	900	98.4	**901**	**98.3**	901	98.3
471.omnetpp	4	911	27.5	**875**	**28.6**	872	28.7	4	832	30.0	831	30.1	**831**	**30.1**
473.astar	4	854	32.9	**826**	**34.0**	823	34.1	4	809	34.7	**807**	**34.8**	805	34.9
483.xalancbmk	4	487	56.7	**485**	**56.9**	482	57.2	4	487	56.7	**485**	**56.9**	482	57.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

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http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xT -O3 -ipo -no-prec-div -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -prefetch -L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 400.perlbench`

456.hmmer: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll2`

458.sjeng: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll4`

462.libquantum: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -prefetch -opt-streaming-stores always`

464.h264ref: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -ansi-alias -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml`

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