SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

(Test Sponsor: Intel Corporation)

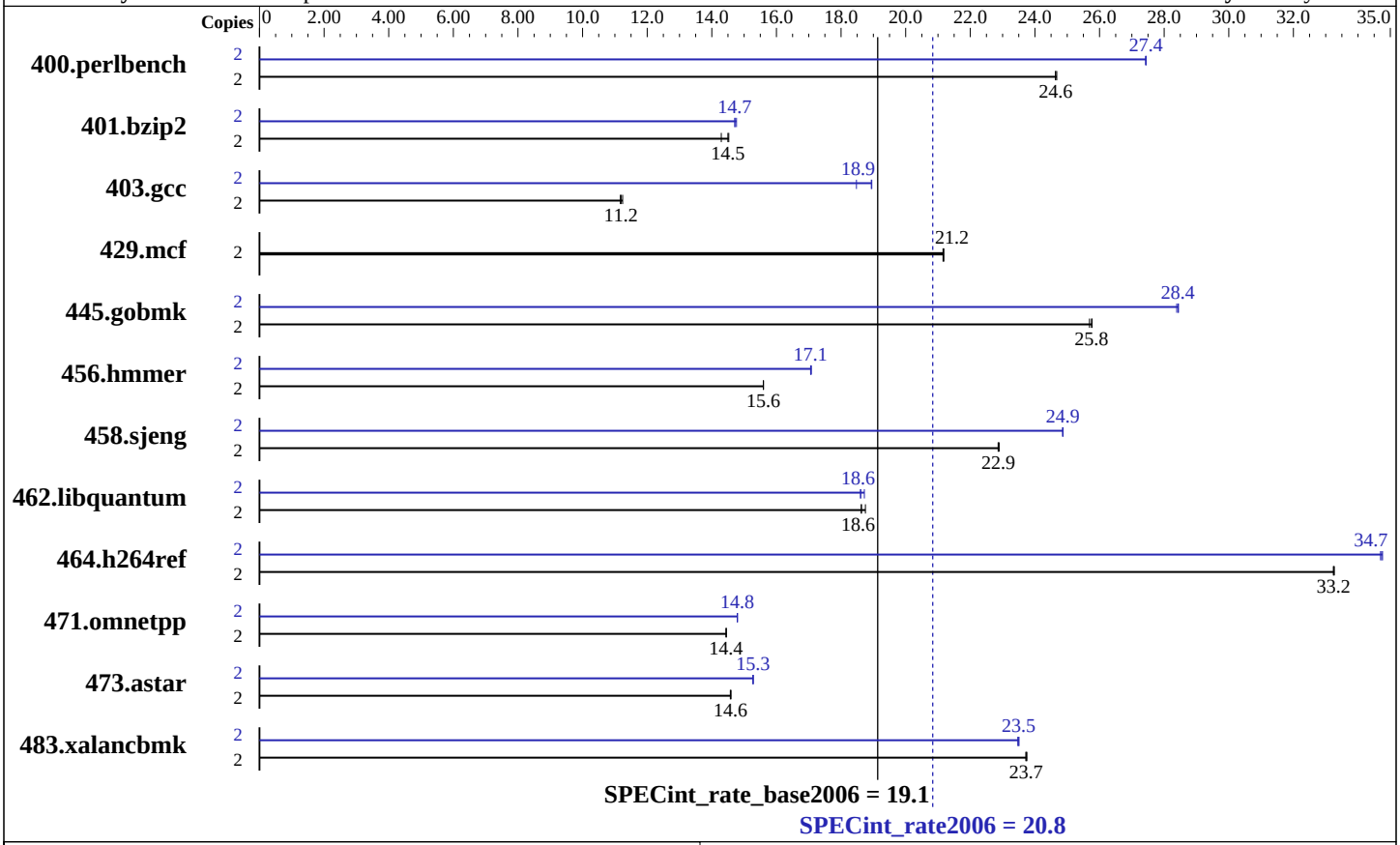
Dell XPS M1710 (Intel Core Duo T2600)

SPECint®_rate2006 = 20.8

SPECint_rate_base2006 = 19.1

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Jun-2007
Software Availability: May-2007



Hardware

CPU Name:	Intel Core Duo T2600
CPU Characteristics:	2.17 GHz, 2MB L2, 667 MHz bus
CPU MHz:	2167
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Hynix DDR2-667 CL5)
Disk Subsystem:	100GB Hitachi SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Vista32 Ultimate
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	None SmartHeap Library Version 8.0 from http://www.microquill.com/

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.
(Test Sponsor: Intel Corporation)

Dell XPS M1710 (Intel Core Duo T2600)

SPECint_rate2006 = 20.8

SPECint_rate_base2006 = 19.1

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Jun-2007
Software Availability: May-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**793**	**24.6**	792	24.7	793	24.6	2	**712**	**27.4**	713	27.4	712	27.4
401.bzip2	2	1350	14.3	1329	14.5	**1331**	**14.5**	2	1307	14.8	1312	14.7	**1312**	**14.7**
403.gcc	2	**1439**	**11.2**	1432	11.2	1442	11.2	2	871	18.5	850	19.0	**850**	**18.9**
429.mcf	2	861	21.2	862	21.2	**861**	**21.2**	2	861	21.2	862	21.2	**861**	**21.2**
445.gobmk	2	814	25.8	817	25.7	**815**	**25.8**	2	738	28.4	739	28.4	**738**	**28.4**
456.hmmer	2	1196	15.6	**1196**	**15.6**	1197	15.6	2	1092	17.1	**1094**	**17.1**	1094	17.1
458.sjeng	2	1059	22.9	1057	22.9	**1058**	**22.9**	2	974	24.9	**973**	**24.9**	973	24.9
462.libquantum	2	2210	18.8	**2224**	**18.6**	2225	18.6	2	2228	18.6	2214	18.7	**2227**	**18.6**
464.h264ref	2	1332	33.2	1330	33.3	**1331**	**33.2**	2	**1274**	**34.7**	1276	34.7	1273	34.8
471.omnetpp	2	866	14.4	**865**	**14.4**	865	14.4	2	845	14.8	845	14.8	**845**	**14.8**
473.astar	2	962	14.6	**963**	**14.6**	963	14.6	2	919	15.3	**919**	**15.3**	919	15.3
483.xalancbmk	2	581	23.8	582	23.7	**582**	**23.7**	2	587	23.5	588	23.5	**587**	**23.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
The system bus runs at 667 MHz
System was configured with an nVIDIA GeForce Go 7900 GTX graphics card
Binaries were built on Windows XP Professional SP2
The start command with the /affinity switch was used to bind processes to cores
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-QxP -Qipo -O3 -Qprec-div- /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-QxP -Qipo -O3 -Qprec-div- -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qansi-alias -Qprefetch /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- /F512000000 -link /FORCE:MULTIPLE

429.mcf: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -O2 -Qipo -Qprec_div- -Qansi-alias /F512000000 -link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3 -Qprec-div- -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3 -Qprec-div- -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.43.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.43.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 12:17:43 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 August 2007.