SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

ASUS Computer International

(Test Sponsor: Intel Corporation)

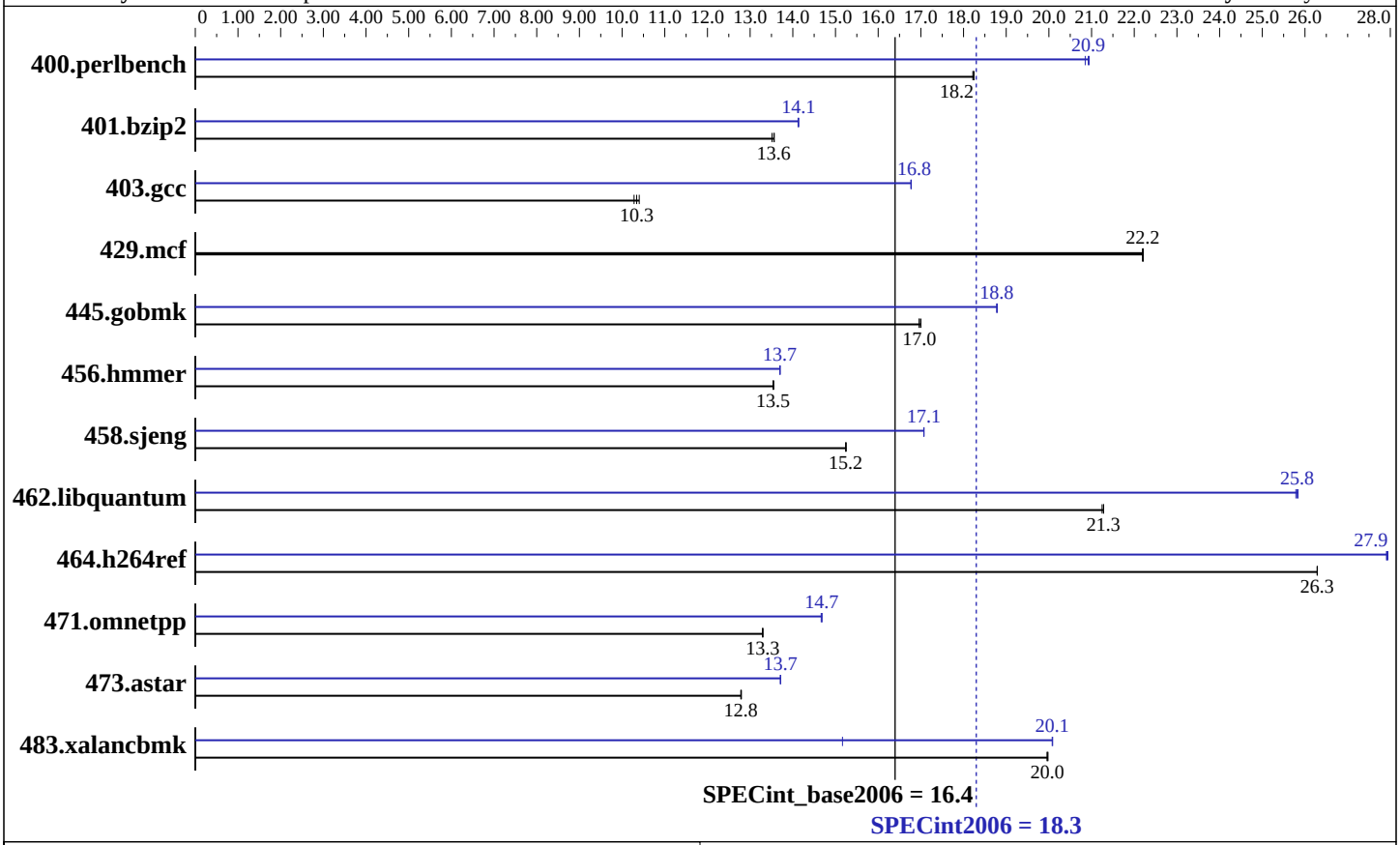
Asus G2S (Intel Core 2 Duo X7800)

SPECint®2006 = 18.3

SPECint_base2006 = 16.4

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Jun-2007
Software Availability: May-2007



Hardware

CPU Name:	Intel Core 2 Duo X7800
CPU Characteristics:	2.60 GHz, 4MB L2, 800 MHz bus
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Hynix DDR2-667 CL5)
Disk Subsystem:	160GB Hitachi SATA, 5400RPM
Other Hardware:	None

Software

Operating System:	Windows Vista32 Ultimate
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	None SmartHeap Library Version 8.0 from http://www.microquill.com/

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

ASUS Computer International
(Test Sponsor: Intel Corporation)

SPECint2006 = 18.3

Asus G2S (Intel Core 2 Duo X7800)

SPECint_base2006 = 16.4

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Jun-2007
Software Availability: May-2007

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	536	18.2	536	18.2	**536**	**18.2**	**467**	**20.9**	469	20.9	467	20.9
401.bzip2	714	13.5	**712**	**13.6**	711	13.6	**683**	**14.1**	683	14.1	683	14.1
403.gcc	**778**	**10.3**	783	10.3	774	10.4	480	16.8	480	16.8	**480**	**16.8**
429.mcf	**411**	**22.2**	411	22.2	411	22.2	**411**	**22.2**	411	22.2	411	22.2
445.gobmk	**618**	**17.0**	619	17.0	617	17.0	558	18.8	**559**	**18.8**	559	18.8
456.hmmer	**689**	**13.5**	689	13.5	688	13.6	681	13.7	681	13.7	**681**	**13.7**
458.sjeng	794	15.2	**794**	**15.2**	793	15.3	**709**	**17.1**	709	17.1	709	17.1
462.libquantum	**974**	**21.3**	975	21.2	974	21.3	804	25.8	**803**	**25.8**	802	25.8
464.h264ref	**842**	**26.3**	842	26.3	842	26.3	792	27.9	**793**	**27.9**	793	27.9
471.omnetpp	**470**	**13.3**	470	13.3	470	13.3	**426**	**14.7**	426	14.7	426	14.7
473.astar	549	12.8	549	12.8	**549**	**12.8**	**512**	**13.7**	512	13.7	512	13.7
483.xalancbmk	**346**	**20.0**	345	20.0	346	20.0	455	15.2	**344**	**20.1**	343	20.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
The system bus runs at 667 MHz
System was configured with an nVIDIA 8600M GT graphics card
Binaries were built on Windows XP Professional SP2
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
-link /FORCE:MULTIPLE

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec_div- -Qansi-alias /F512000000
-link /FORCE:MULTIPLE

Continued on next page

Peak Optimization Flags (Continued)

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 12:27:22 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 August 2007.