SPEC® CINT2006 Result

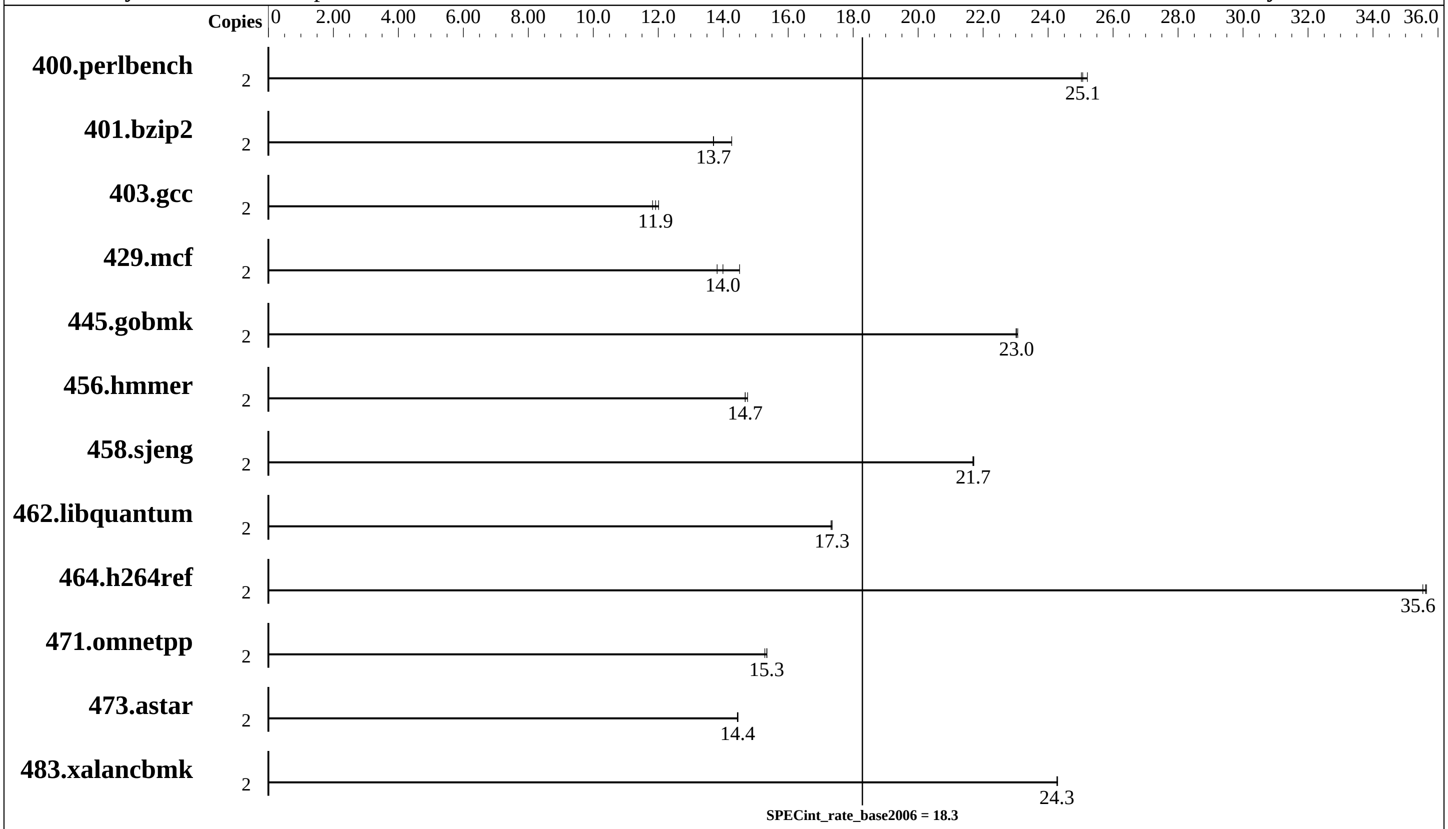
Lenovo Group Limited
(Test Sponsor: Intel Corporation)

Lenovo ThinkPad T61(Intel Core 2 Duo T7100)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 18.3

CPU2006 license: 13		**Test date:**	Jun-2007
Test sponsor:	Intel Corporation	**Hardware Availability:**	Jun-2007
Tested by:	Intel Corporation	**Software Availability:**	Feb-2007



Hardware

CPU Name:	Intel Core 2 Duo T7100
CPU Characteristics:	1.80 GHz 2MB L2, 800 MHz Bus
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Qimonda* PC-5300 DDR2-667 5-5-5-12)
Disk Subsystem:	Hitachi* HTS721010G9 SATA 100GB 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Vista32 Business
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	SmartHeap library V7.4 from Microquill

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**780**	**25.1**	775	25.2	781	25.0							
401.bzip2	2	1353	14.3	1409	13.7	**1408**	**13.7**							
403.gcc	2	1340	12.0	**1351**	**11.9**	1361	11.8							
429.mcf	2	1321	13.8	1258	14.5	**1304**	**14.0**							
445.gobmk	2	912	23.0	910	23.1	**911**	**23.0**							
456.hmmer	2	1272	14.7	**1271**	**14.7**	1265	14.8							
458.sjeng	2	1114	21.7	1116	21.7	**1116**	**21.7**							
462.libquantum	2	2388	17.4	2393	17.3	**2389**	**17.3**							
464.h264ref	2	1246	35.5	**1243**	**35.6**	1242	35.6							
471.omnetpp	2	818	15.3	**815**	**15.3**	815	15.3							
473.astar	2	971	14.5	**972**	**14.4**	973	14.4							
483.xalancbmk	2	569	24.3	**569**	**24.3**	568	24.3							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
      -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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