



SPEC® CFP2006 Result

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Lenovo Group Limited

(Test Sponsor: Intel Corporation)

SPECfp®2006 = **Not Run**

Lenovo ThinkPad T61(Intel Core 2 Duo T7100)

SPECfp_base2006 = **11.2**

CPU2006 license: 13

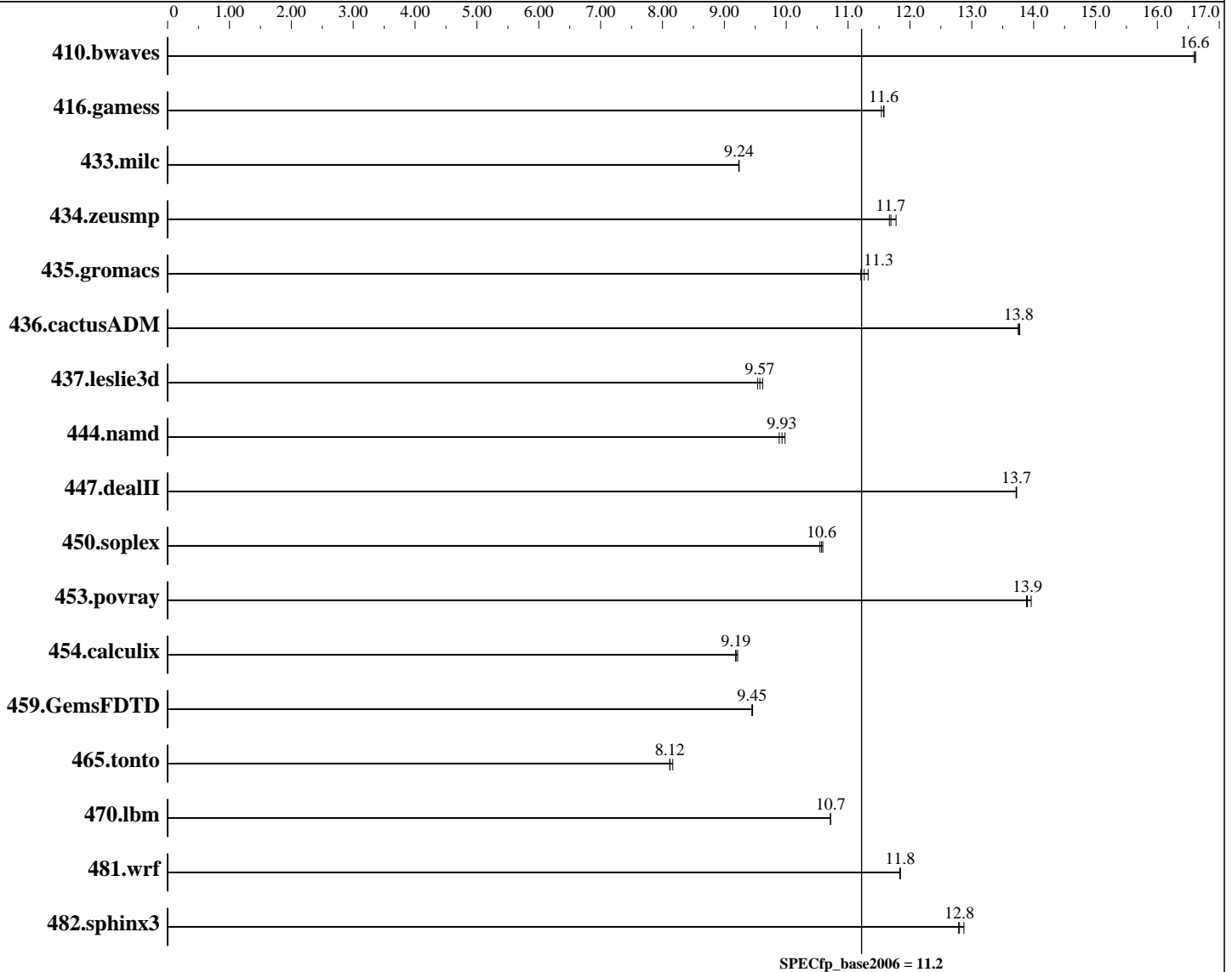
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2007

Hardware Availability: Jun-2007

Software Availability: Feb-2007



Hardware

CPU Name: Intel Core 2 Duo T7100
 CPU Characteristics: 1.80 GHz, 2MB L2, 800 MHz Bus
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 2 MB I+D on chip per chip

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Software

Operating System: Windows XP Professional w/ SP2
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Intel Fortran Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default

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L3 Cache: None
Other Cache: None
Memory: 2 GB (2x1GB Qimonda* PC-5300 DDR2-667 5-5-5-12)
Disk Subsystem: Hitachi* HTS721010G9 SATA 100GB 7200RPM
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: SmartHeap library V7.4 from Microquill

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|-------------|-------------|-------------|-------------|---------|-------|---------|-------|---------|-------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 819 | 16.6 | 818 | 16.6 | 819 | 16.6 | | | | | | |
| 416.gamess | 1692 | 11.6 | 1691 | 11.6 | 1697 | 11.5 | | | | | | |
| 433.milc | 994 | 9.23 | 994 | 9.24 | 994 | 9.24 | | | | | | |
| 434.zeusmp | 778 | 11.7 | 773 | 11.8 | 780 | 11.7 | | | | | | |
| 435.gromacs | 631 | 11.3 | 634 | 11.3 | 637 | 11.2 | | | | | | |
| 436.cactusADM | 868 | 13.8 | 869 | 13.8 | 869 | 13.8 | | | | | | |
| 437.leslie3d | 986 | 9.54 | 977 | 9.62 | 982 | 9.57 | | | | | | |
| 444.namd | 807 | 9.93 | 804 | 9.98 | 811 | 9.89 | | | | | | |
| 447.dealII | 834 | 13.7 | 834 | 13.7 | 834 | 13.7 | | | | | | |
| 450.soplex | 789 | 10.6 | 787 | 10.6 | 791 | 10.5 | | | | | | |
| 453.povray | 383 | 13.9 | 383 | 13.9 | 381 | 14.0 | | | | | | |
| 454.calculix | 895 | 9.21 | 898 | 9.19 | 898 | 9.18 | | | | | | |
| 459.GemsFDTD | 1122 | 9.46 | 1123 | 9.45 | 1123 | 9.45 | | | | | | |
| 465.tonto | 1212 | 8.12 | 1212 | 8.12 | 1205 | 8.17 | | | | | | |
| 470.lbm | 1282 | 10.7 | 1282 | 10.7 | 1282 | 10.7 | | | | | | |
| 481.wrf | 943 | 11.8 | 944 | 11.8 | 943 | 11.8 | | | | | | |
| 482.sphinx3 | 1514 | 12.9 | 1523 | 12.8 | 1524 | 12.8 | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries built on Windows XP Professional w/ SP2
XP Power Profile set to AC/High Performance

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore

444.namd: -TP

447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T

453.povray: -DSPEC_CPU_WINDOWS_ICL

454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase

481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:

-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:

-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

-fast /F950000000 -link /FORCE:MULTIPLE

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.xml>



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For other inquiries, please contact webmaster@spec.org.

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