SPEC® CINT2006 Result

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Hewlett-Packard Company

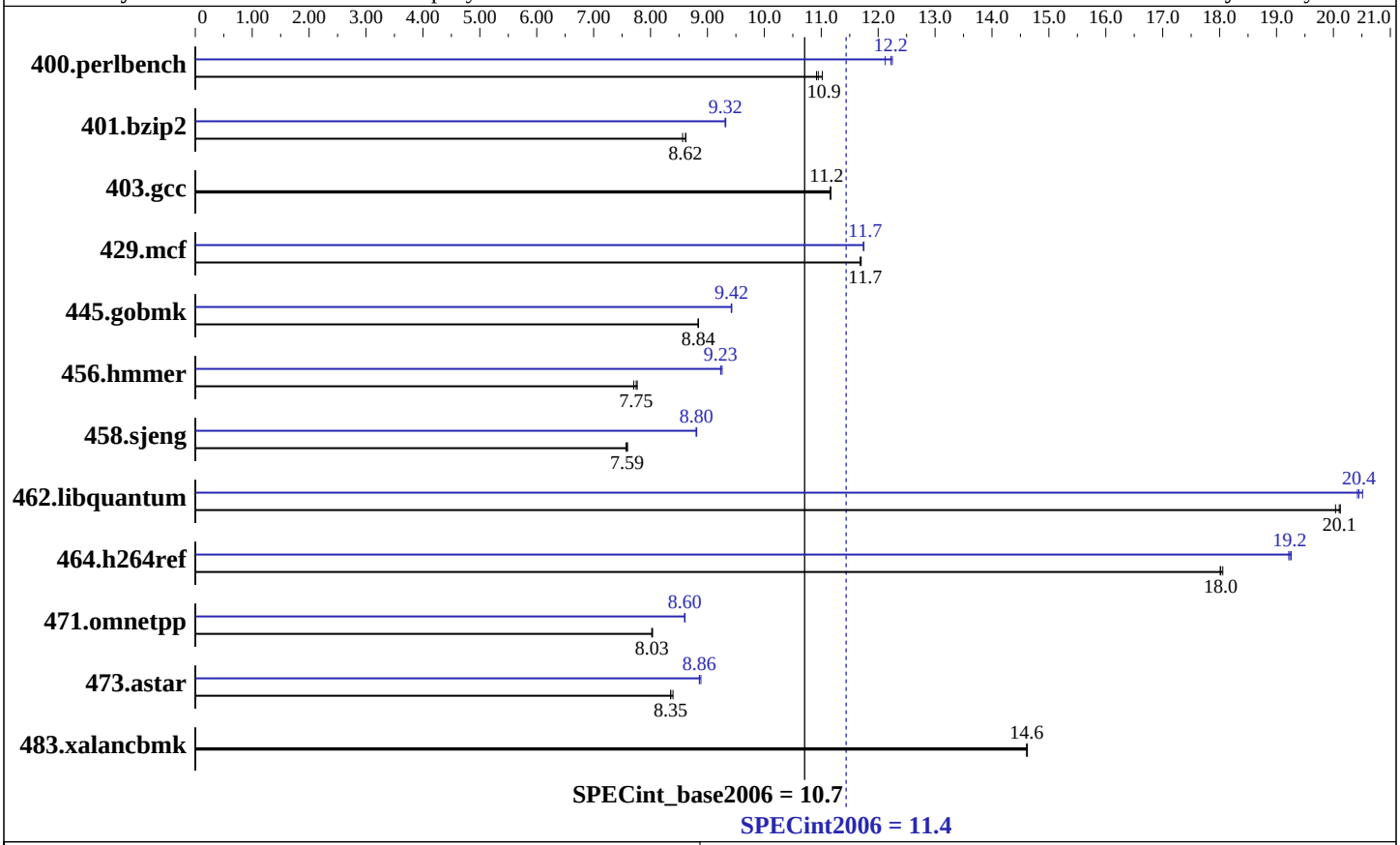
ProLiant DL580 G4
(3.2 GHz, Intel Xeon processor 7130M)

SPECint®2006 = 11.4

SPECint_base2006 = 10.7

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jul-2007
Hardware Availability: Aug-2006
Software Availability: May-2007



Hardware

CPU Name:	Intel Xeon 7130M
CPU Characteristics:	3.2 GHz, 800 MHz system bus
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16x2 GB PC2-3200R)
Disk Subsystem:	1x72 GB 15 K SAS
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for IA32/EM64T applications, Version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multi-user run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap 8.1 libraries

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	887	11.0	895	10.9	**893**	**10.9**	**799**	**12.2**	798	12.2	806	12.1
401.bzip2	1127	8.56	1120	8.62	**1120**	**8.62**	1036	9.31	**1036**	**9.32**	1035	9.32
403.gcc	**721**	**11.2**	721	11.2	721	11.2	**721**	**11.2**	721	11.2	721	11.2
429.mcf	779	11.7	780	11.7	**780**	**11.7**	**777**	**11.7**	777	11.7	776	11.8
445.gobmk	1186	8.84	1188	8.83	**1186**	**8.84**	**1113**	**9.42**	1114	9.42	1113	9.43
456.hmmer	1211	7.70	1201	7.77	**1204**	**7.75**	**1010**	**9.23**	1011	9.23	1008	9.26
458.sjeng	1593	7.59	1599	7.57	**1594**	**7.59**	1373	8.81	1375	8.80	**1374**	**8.80**
462.libquantum	1030	20.1	1034	20.0	**1031**	**20.1**	1010	20.5	1015	20.4	**1013**	**20.4**
464.h264ref	1229	18.0	**1228**	**18.0**	1226	18.1	**1151**	**19.2**	1149	19.3	1152	19.2
471.omnetpp	**779**	**8.03**	779	8.02	778	8.04	**726**	**8.60**	726	8.61	727	8.60
473.astar	**840**	**8.35**	836	8.39	840	8.35	793	8.86	**792**	**8.86**	790	8.89
483.xalancbmk	**472**	**14.6**	472	14.6	472	14.6	**472**	**14.6**	472	14.6	472	14.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

```
BIOS configuration:
 Power Regulator Disabled
 Adjacent Sector Prefetch Enabled
 Hardware Prefetcher Enabled
 Hyper-Threading Technology Enabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xP -ipo -O3 -static -no-prec-div

C++ benchmarks:
-xP -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -no-prec-div -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -no-prec-div`

403.gcc: `basepeak = yes`

429.mcf: `-xP -ipo -O3 -static -no-prec-div -prefetch -L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -O2 -ipo -no-prec_div -ansi-alias`

456.hmmer: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -static -no-prec-div -unroll2 -ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -static -no-prec-div -unroll4`

462.libquantum: `Same as 458.sjeng`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.03.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.03.xml`

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