SPEC® CINT2006 Result

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Hewlett-Packard Company

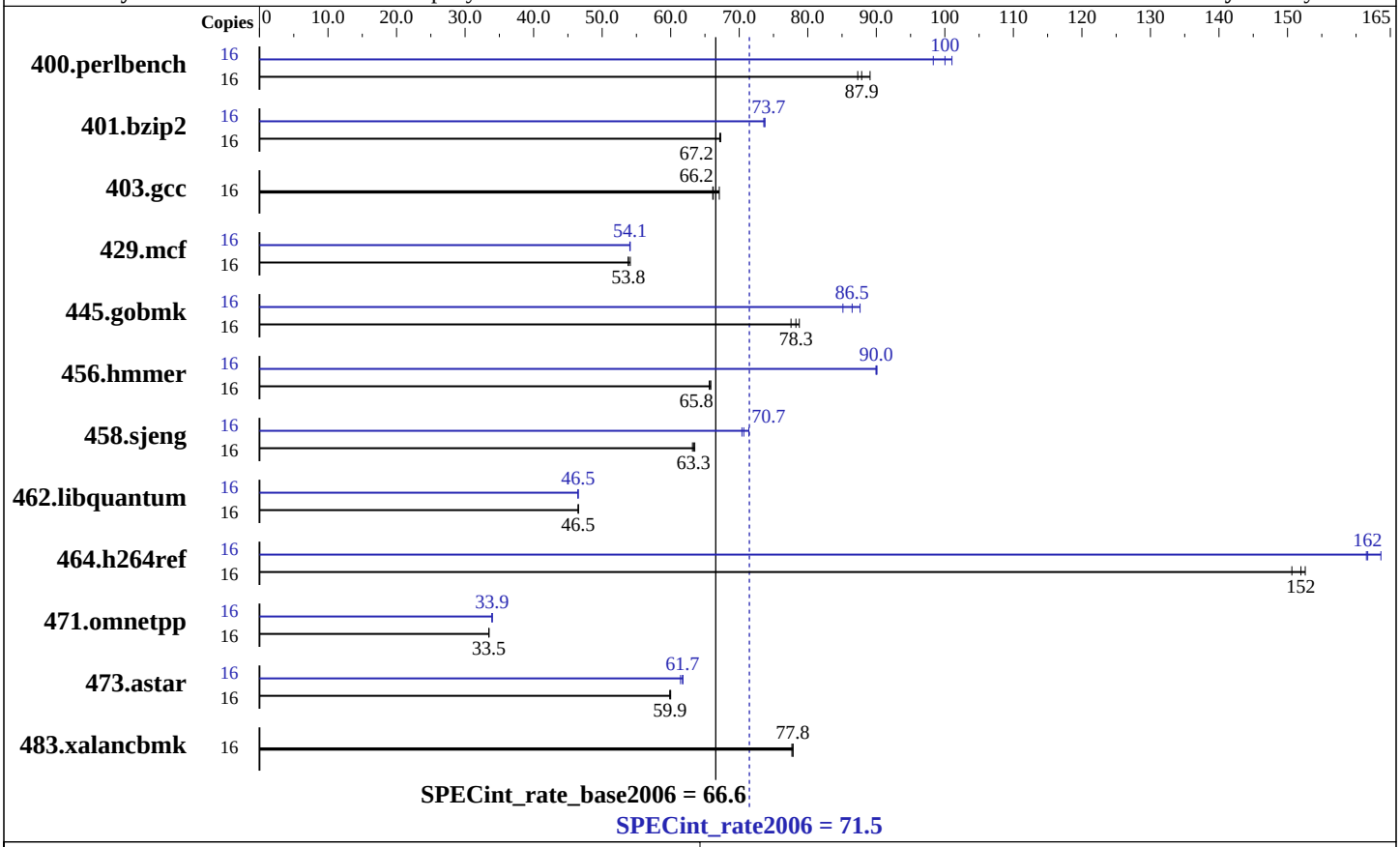
ProLiant DL580 G4
(3.2 GHz, Intel Xeon processor 7130M)

SPECint®_rate2006 = 71.5

SPECint_rate_base2006 = 66.6

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jul-2007
Hardware Availability: Aug-2006
Software Availability: May-2007



Hardware

CPU Name:	Intel Xeon 7130M
CPU Characteristics:	3.2 GHz, 800 MHz system bus
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16x2 GB PC2-3200R)
Disk Subsystem:	1x72 GB 15 K SAS
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for IA32/EM64T applications, Version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multi-user run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap 8.1 libraries

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	**1779**	**87.9**	1791	87.3	1755	89.1	16	**1563**	**100**	1590	98.3	1547	101
401.bzip2	16	**2296**	**67.2**	2299	67.2	2295	67.3	16	**2095**	**73.7**	2098	73.6	2093	73.8
403.gcc	16	1948	66.1	1920	67.1	**1945**	**66.2**	16	1948	66.1	1920	67.1	**1945**	**66.2**
429.mcf	16	**2711**	**53.8**	2712	53.8	2697	54.1	16	**2699**	**54.1**	2698	54.1	2700	54.0
445.gobmk	16	2164	77.6	**2144**	**78.3**	2131	78.8	16	**1941**	**86.5**	1972	85.1	1915	87.6
456.hmmer	16	2267	65.8	2275	65.6	**2269**	**65.8**	16	1657	90.1	**1658**	**90.0**	1660	90.0
458.sjeng	16	3065	63.2	3048	63.5	**3057**	**63.3**	16	2712	71.4	**2739**	**70.7**	2750	70.4
462.libquantum	16	7126	46.5	**7126**	**46.5**	7128	46.5	16	7129	46.5	7134	46.5	**7133**	**46.5**
464.h264ref	16	2351	151	**2330**	**152**	2320	153	16	2164	164	2192	162	**2190**	**162**
471.omnetpp	16	2989	33.5	**2988**	**33.5**	2986	33.5	16	2944	34.0	**2946**	**33.9**	2948	33.9
473.astar	16	**1875**	**59.9**	1876	59.9	1872	60.0	16	1818	61.8	1828	61.4	**1820**	**61.7**
483.xalancbmk	16	**1418**	**77.8**	1418	77.8	1421	77.7	16	**1418**	**77.8**	1418	77.8	1421	77.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

```
BIOS configuration:
 Power Regulator Disabled
 Adjacent Sector Prefetch Enabled
 Hardware Prefetcher Enabled
 Hyper-Threading Technology Enabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xP -ipo -O3 -static -no-prec-div

C++ benchmarks:
-xP -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -no-prec-div -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -no-prec-div`

403.gcc: `basepeak = yes`

429.mcf: `-xP -ipo -O3 -static -no-prec-div -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -O2 -ipo -no-prec_div -ansi-alias`

456.hmmer: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -static -no-prec-div -unroll2 -ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -static -no-prec-div -unroll4`

462.libquantum: `Same as 458.sjeng`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.03.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.03.xml`

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 12:27:29 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 August 2007.