SPEC® CINT2006 Result

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Fujitsu Siemens Computers

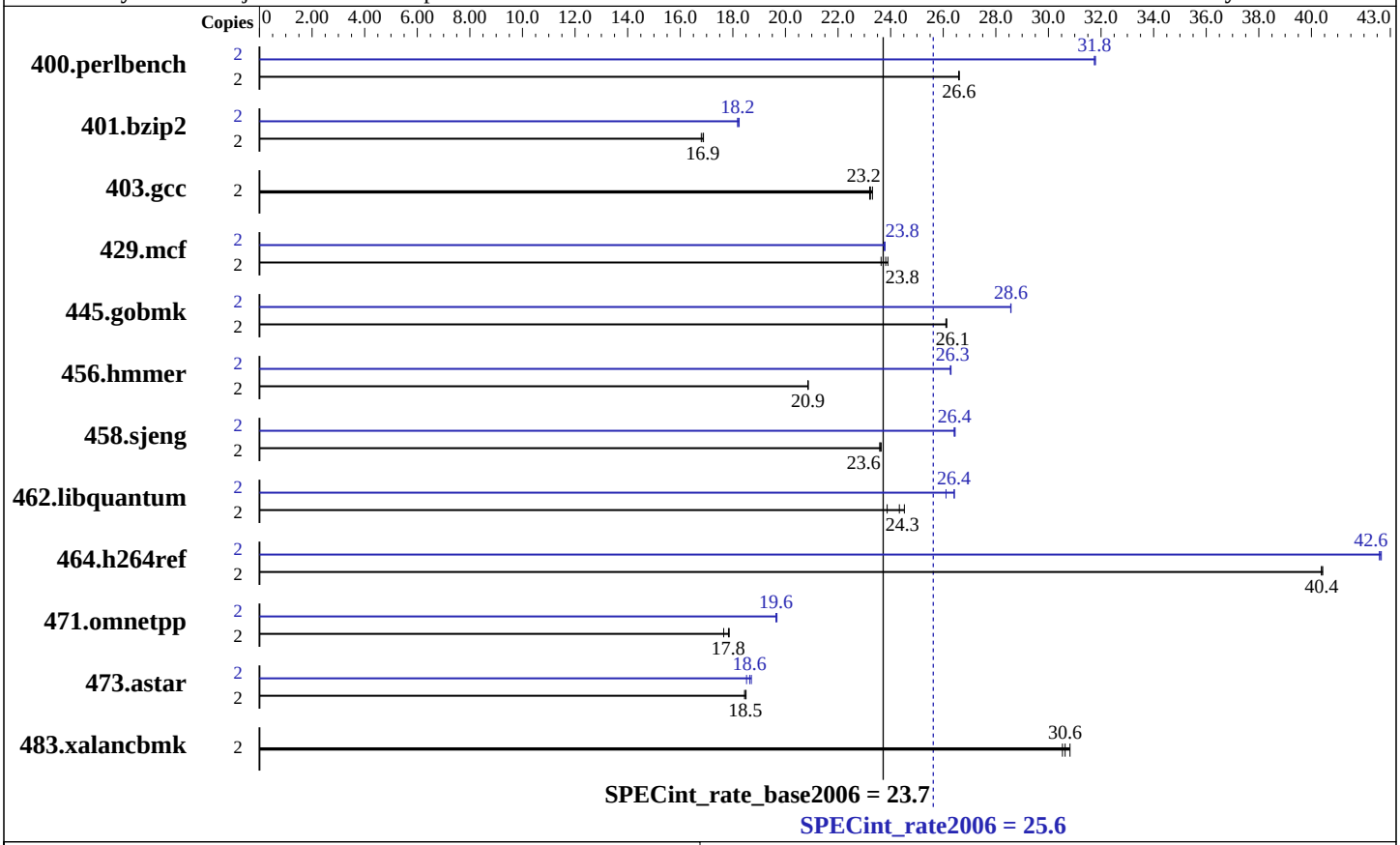
PRIMERGY TX200 S3, Intel Xeon processor 5130, 2.0 GHz

SPECint®_rate2006 = 25.6

SPECint_rate_base2006 = 23.7

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jul-2007
Hardware Availability: Jul-2006
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon 5130
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	Seagate ST336754SS (SAS, 36GB, 15000rpm)
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	735	26.6	**734**	**26.6**	734	26.6	2	615	31.8	615	31.8	**615**	**31.8**
401.bzip2	2	1143	16.9	**1144**	**16.9**	1149	16.8	2	1058	18.2	1061	18.2	**1061**	**18.2**
403.gcc	2	694	23.2	691	23.3	**693**	**23.2**	2	694	23.2	691	23.3	**693**	**23.2**
429.mcf	2	772	23.6	**766**	**23.8**	763	23.9	2	**767**	**23.8**	767	23.8	768	23.8
445.gobmk	2	804	26.1	**803**	**26.1**	803	26.1	2	734	28.6	**734**	**28.6**	734	28.6
456.hmmer	2	**895**	**20.9**	895	20.9	894	20.9	2	**710**	**26.3**	710	26.3	710	26.3
458.sjeng	2	1026	23.6	1024	23.6	**1026**	**23.6**	2	**916**	**26.4**	916	26.4	915	26.5
462.libquantum	2	1736	23.9	1690	24.5	**1703**	**24.3**	2	1568	26.4	1587	26.1	**1569**	**26.4**
464.h264ref	2	**1096**	**40.4**	1096	40.4	1095	40.4	2	1039	42.6	1038	42.7	**1038**	**42.6**
471.omnetpp	2	708	17.7	**701**	**17.8**	700	17.9	2	635	19.7	636	19.6	**636**	**19.6**
473.astar	2	761	18.4	**760**	**18.5**	759	18.5	2	758	18.5	**753**	**18.6**	751	18.7
483.xalancbmk	2	**450**	**30.6**	452	30.5	448	30.8	2	**450**	**30.6**	452	30.5	448	30.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xT -O3 -ipo -no-prec-div -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -prefetch -L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 400.perlbench`

456.hmmer: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll2`

458.sjeng: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll4`

462.libquantum: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -prefetch -opt-streaming-stores always`

464.h264ref: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -ansi-alias -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml`

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