SPEC® CINT2006 Result

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IBM Corporation

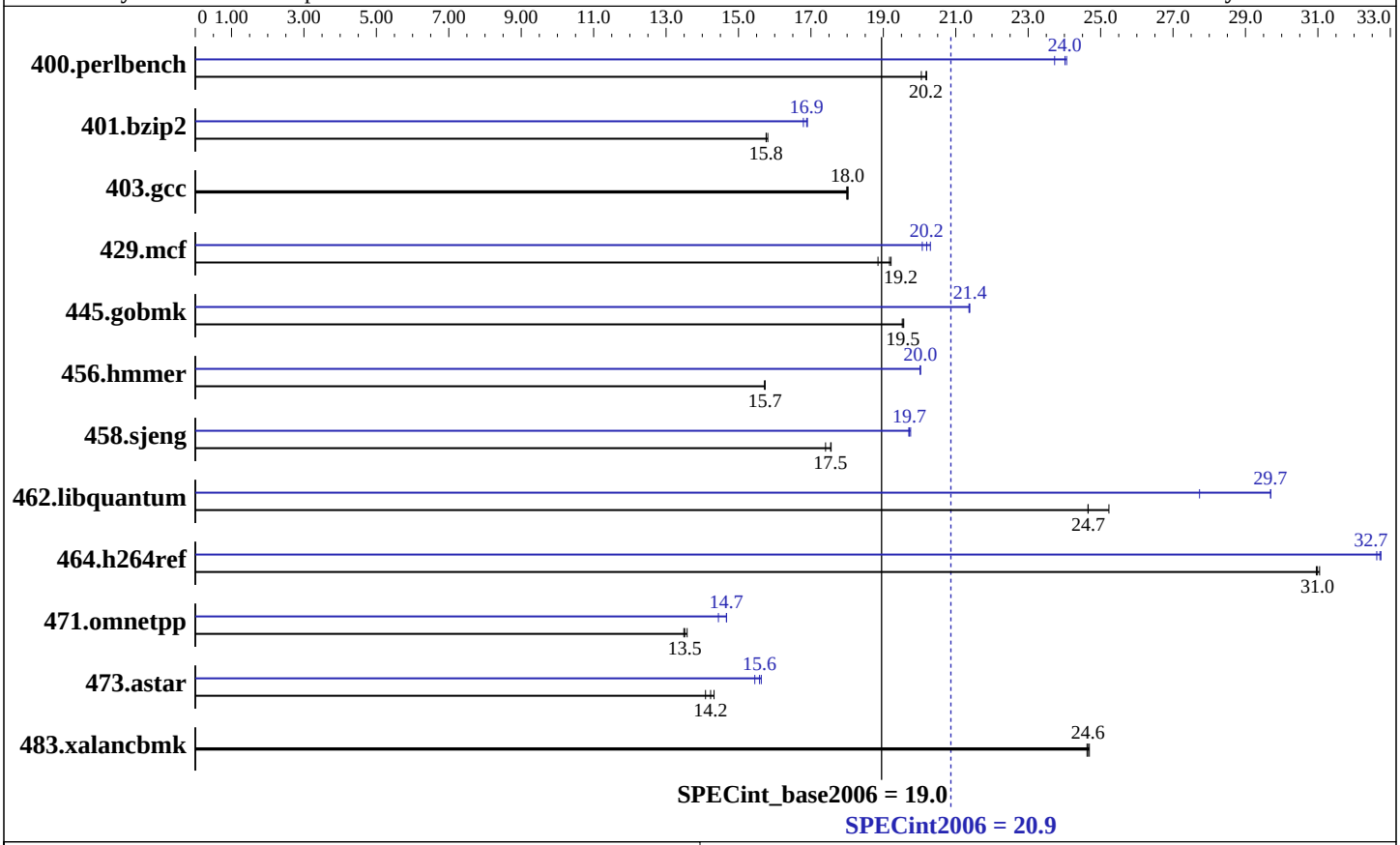
IBM System x3550 (Intel Xeon 5160)

SPECint®2006 = 20.9

SPECint_base2006 = 19.0

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jul-2007
Hardware Availability: Jul-2006
Software Availability: Jul-2007



Hardware

CPU Name:	Intel Xeon 5160
CPU Characteristics:	1333MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2GB DDR2-5300F ECC)
Disk Subsystem:	1 x 36GB SAS, 15000RPM
Other Hardware:	None

Software

Operating System:	SLES 10 (x86_64), 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Linux version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap 8.1

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	484	20.2	487	20.0	**484**	**20.2**	406	24.1	412	23.7	**407**	**24.0**
401.bzip2	610	15.8	**612**	**15.8**	612	15.8	571	16.9	575	16.8	**572**	**16.9**
403.gcc	**447**	**18.0**	447	18.0	447	18.0	**447**	**18.0**	447	18.0	447	18.0
429.mcf	484	18.9	**476**	**19.2**	475	19.2	454	20.1	449	20.3	**452**	**20.2**
445.gobmk	536	19.6	537	19.5	**537**	**19.5**	**491**	**21.4**	490	21.4	491	21.4
456.hmmer	**593**	**15.7**	593	15.7	594	15.7	**466**	**20.0**	466	20.0	466	20.0
458.sjeng	689	17.6	**690**	**17.5**	695	17.4	613	19.8	**614**	**19.7**	614	19.7
462.libquantum	821	25.2	841	24.7	**840**	**24.7**	747	27.7	698	29.7	**698**	**29.7**
464.h264ref	**714**	**31.0**	713	31.0	715	31.0	676	32.7	678	32.6	**676**	**32.7**
471.omnetpp	**462**	**13.5**	463	13.5	460	13.6	426	14.7	**426**	**14.7**	433	14.4
473.astar	498	14.1	**493**	**14.2**	490	14.3	449	15.6	454	15.5	**451**	**15.6**
483.xalancbmk	**280**	**24.6**	279	24.7	280	24.6	**280**	**24.6**	279	24.7	280	24.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/spec/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/10.0.023/bin/icc -L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include`

456.hmmer: `/opt/intel/cce/10.0.023/bin/icc -L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec_div -ansi-alias`

Continued on next page

Peak Optimization Flags (Continued)

456.hmmer: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/spec/cpu2006.1.0/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.44.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.44.xml`

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