SPEC® CINT2006 Result

HITACHI

BladeSymphony BS320 (Intel Xeon E5345)

SPECint®_rate2006 = 81.2
SPECint_rate_base2006 = 78.3

CPU2006 license: 872
Test date: Jun-2007
Test sponsor: HITACHI
Hardware Availability: Jan-2007
Tested by: HITACHI
Software Availability: Jun-2007

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<table>
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<tr>
<th>Test</th>
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<tbody>
<tr>
<td>400.perlbench</td>
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</tr>
<tr>
<td>401.bzip2</td>
<td>66.7</td>
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<td>403.gcc</td>
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<td>473.astar</td>
<td>62.3</td>
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<tr>
<td>483.xalancbmk</td>
<td>94.6</td>
</tr>
</tbody>
</table>

Hardware

CPU Name: Intel Xeon E5345
CPU Characteristics: 1333 MHz system bus
CPU MHz: 2333
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1, 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
L2 Cache: None
Other Cache: None
Memory: 8 GB(4 x 2 GB PC2-5300F CAS 5-5-5)
Disk Subsystem: 2 x 73 GB 10000rpm SAS
Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 R2, Enterprise x64 Edition
Compiler: Intel C++ Compiler for IA32 version 10.0 Build 20070426
Microsoft Visual Studio.Net 2003 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library, Version 8.0
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### Results Table

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<th>Copies</th>
<th>Base</th>
<th>Cycles</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Seconds</th>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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### Base Compiler Invocation

**C benchmarks:**

```
icl -Qvc7.1 -Qc99
```

**C++ benchmarks:**

```
icl -Qc7.1
```

---

### Base Portability Flags

- **403.gcc:** -DSPEC_CPU_WIN32
- **464.h264ref:** -DSPEC_CPU_NO_INTTYPES -DWIN32

---

### Base Optimization Flags

**C benchmarks:**

```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

**C++ benchmarks:**

```
-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```
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Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
400.perlbench: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
401.bzip2: Same as 400.perlbench
403.gcc: Same as 400.perlbench
429.mcf: basepeak = yes
445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
456.hmmer: Same as 400.perlbench
458.sjeng: Same as 400.perlbench
462.libquantum: basepeak = yes
464.h264ref: Same as 400.perlbench

C++ benchmarks:

Continued on next page
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Peak Optimization Flags (Continued)

471.omnetpp: ONESTEP, -Qprof_gen(pass 1), -Qprof_use(pass 2), -fast
-Qcxx_features /F512000000.shlw32m.lib
-link /FORCE:MULTIPLE

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/ic100.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/ic100.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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