SPEC® CINT2006 Result

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HITACHI

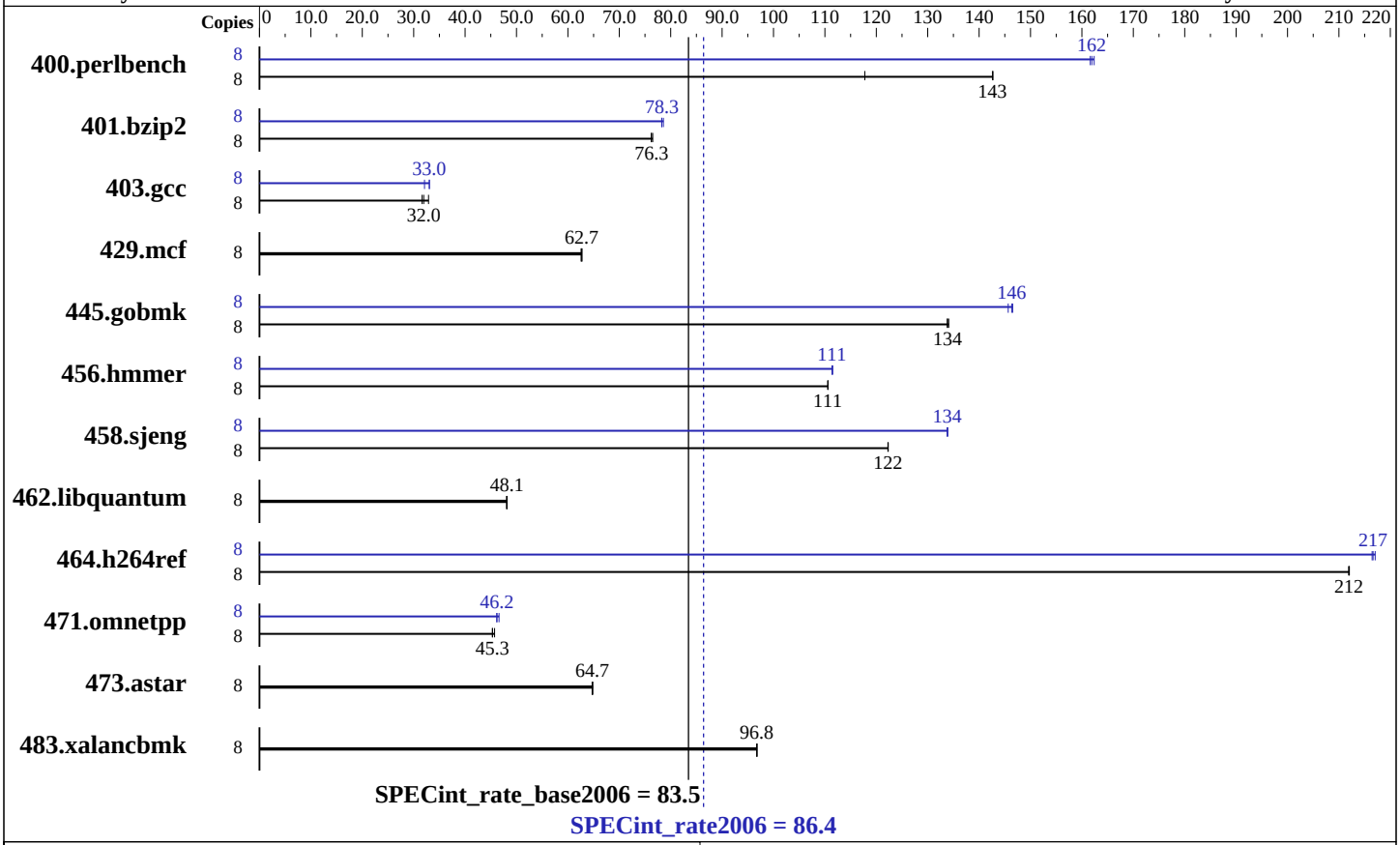
BladeSymphony BS1000 (Intel Xeon X5355)

SPECint®_rate2006 = 86.4

SPECint_rate_base2006 = 83.5

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Jul-2007
Hardware Availability: Jan-2007
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB(8 x 2 GB PC2-5300F CAS 5-5-5)
Disk Subsystem:	2 x 73 GB 10000rpm SAS
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 R2, Enterprise x64 Edition
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library, Version 8.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	664	118	548	143	**548**	**143**	8	**483**	**162**	484	162	481	162
401.bzip2	8	1009	76.5	**1012**	**76.3**	1013	76.2	8	982	78.6	**986**	**78.3**	987	78.2
403.gcc	8	**2015**	**32.0**	2036	31.6	1958	32.9	8	**1949**	**33.0**	1948	33.1	2006	32.1
429.mcf	8	1166	62.6	**1164**	**62.7**	1163	62.7	8	1166	62.6	**1164**	**62.7**	1163	62.7
445.gobmk	8	**627**	**134**	627	134	626	134	8	573	147	**573**	**146**	576	146
456.hmmer	8	675	111	675	111	**675**	**111**	8	669	112	**670**	**111**	670	111
458.sjeng	8	**792**	**122**	792	122	791	122	8	723	134	724	134	**723**	**134**
462.libquantum	8	3442	48.2	3448	48.1	**3445**	**48.1**	8	3442	48.2	3448	48.1	**3445**	**48.1**
464.h264ref	8	835	212	836	212	**835**	**212**	8	818	216	816	217	**817**	**217**
471.omnetpp	8	1093	45.7	1103	45.3	**1103**	**45.3**	8	**1082**	**46.2**	1073	46.6	1083	46.2
473.astar	8	**868**	**64.7**	866	64.9	868	64.7	8	**868**	**64.7**	866	64.9	868	64.7
483.xalancbmk	8	571	96.7	**571**	**96.8**	570	96.8	8	571	96.7	**571**	**96.8**	570	96.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

400.perlbench: `ONESTEP -Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: `basepeak = yes`

445.gobmk: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: `basepeak = yes`

464.h264ref: Same as 400.perlbench

C++ benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

471.omnetpp: `ONESTEP -Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/ic100.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/ic100.xml`

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