SPEC® CFP2006 Result

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Fujitsu Siemens Computers

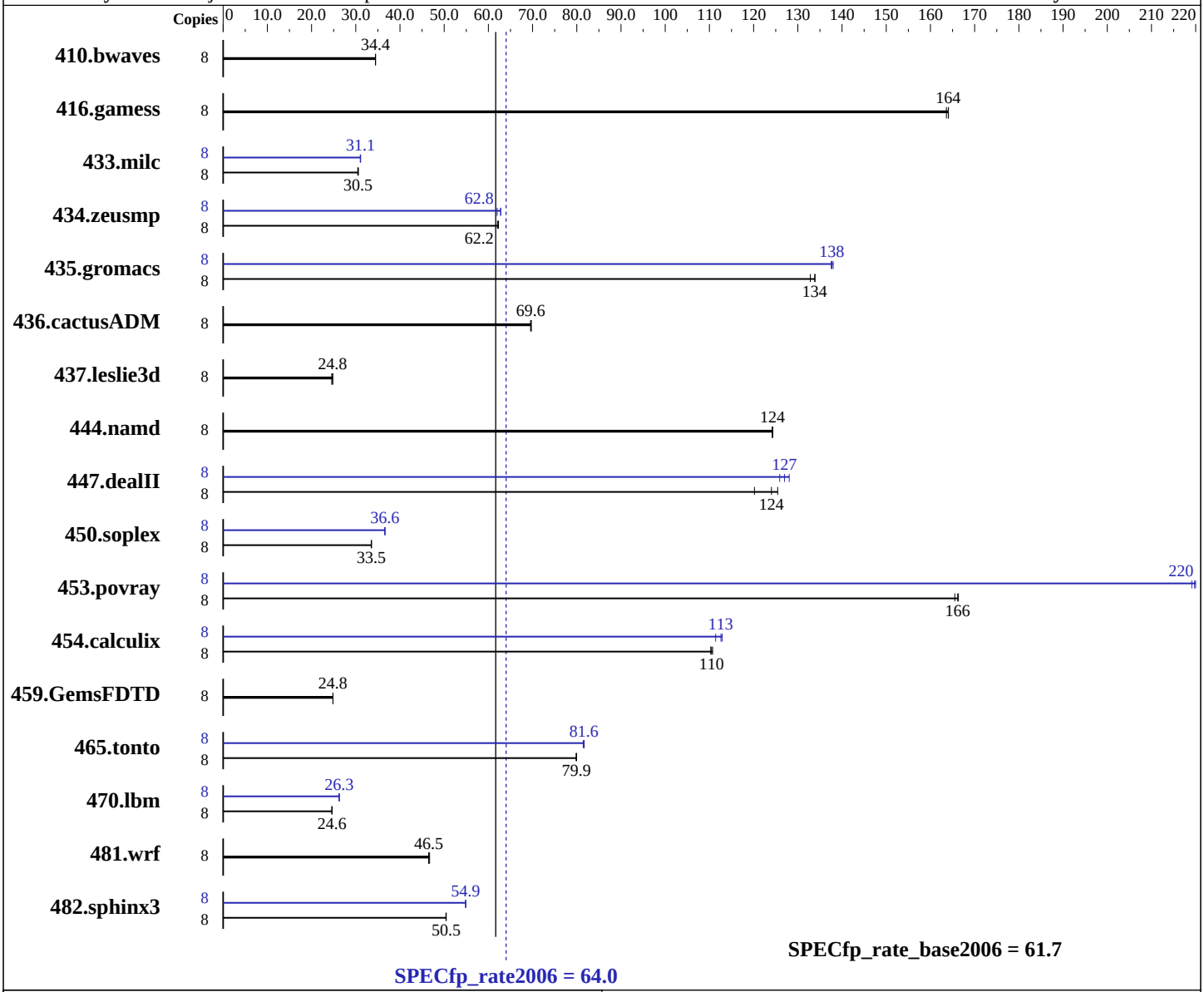
PRIMERGY TX300 S3, Intel Xeon processor X5365, 3.0 GHz

SPECfp®_rate2006 = 64.0

SPECfp_rate_base2006 = 61.7

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jul-2007
Hardware Availability: Aug-2007
Software Availability: Mar-2007



Hardware

CPU Name:	Intel Xeon X5365
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047 Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package ID: l_fc_p_9.1.043
Auto Parallel:	No
File System:	ext2

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: Seagate ST3146854SS (SAS, 146GB, 15000rpm)
Other Hardware: None

System State: Multiuser, Runlevel 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	**3156**	**34.4**	3157	34.4	3152	34.5	8	**3156**	**34.4**	3157	34.4	3152	34.5
416.gamess	8	**955**	**164**	955	164	957	164	8	**955**	**164**	955	164	957	164
433.milc	8	**2404**	**30.5**	2412	30.5	2402	30.6	8	2368	31.0	**2363**	**31.1**	2362	31.1
434.zeusmp	8	1169	62.3	**1170**	**62.2**	1173	62.1	8	1160	62.8	**1160**	**62.8**	1174	62.0
435.gromacs	8	**427**	**134**	427	134	430	133	8	**415**	**138**	415	138	414	138
436.cactusADM	8	**1374**	**69.6**	1371	69.7	1374	69.6	8	**1374**	**69.6**	1371	69.7	1374	69.6
437.leslie3d	8	3034	24.8	**3037**	**24.8**	3062	24.6	8	3034	24.8	**3037**	**24.8**	3062	24.6
444.namd	8	517	124	**516**	**124**	516	124	8	517	124	**516**	**124**	516	124
447.dealII	8	762	120	730	125	**738**	**124**	8	**721**	**127**	727	126	715	128
450.soplex	8	1987	33.6	**1991**	**33.5**	1992	33.5	8	1825	36.6	**1822**	**36.6**	1822	36.6
453.povray	8	**256**	**166**	256	166	257	166	8	**194**	**220**	194	219	193	220
454.calculix	8	596	111	**597**	**110**	598	110	8	585	113	592	111	**586**	**113**
459.GemsFDTD	8	**3417**	**24.8**	3417	24.8	3416	24.8	8	**3417**	**24.8**	3417	24.8	3416	24.8
465.tonto	8	**985**	**79.9**	986	79.8	985	79.9	8	964	81.7	966	81.5	**965**	**81.6**
470.lbm	8	**4466**	**24.6**	4466	24.6	4466	24.6	8	4187	26.3	4187	26.3	**4187**	**26.3**
481.wrf	8	1922	46.5	1913	46.7	**1922**	**46.5**	8	1922	46.5	1913	46.7	**1922**	**46.5**
482.sphinx3	8	3090	50.5	**3090**	**50.5**	3094	50.4	8	2843	54.8	2842	54.9	**2842**	**54.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

All binaries were built with 64-bit Intel compiler except:
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with 32-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

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General Notes (Continued)

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This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and
the PRIMERGY TX300 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com
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Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icc ifort`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
450.soplex: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
482.sphinx3: `-DSPEC_CPU_LP64`

Base Optimization Flags

C benchmarks:
`-fast`

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Base Optimization Flags (Continued)

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
/opt/intel/cc/9.1.047/bin/icc -I/opt/intel/cc/9.1.047/include
-L/opt/intel/cc/9.1.047/lib

C++ benchmarks (except as noted below):
icpc

450.soplex: /opt/intel/cc/9.1.047/bin/icpc
-I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib

Fortran benchmarks (except as noted below):
ifort

434.zeusmp: /opt/intel/fc/9.1.043/bin/ifort
-I/opt/intel/fc/9.1.043/include -L/opt/intel/fc/9.1.043/lib

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

470.lbm: `Same as 433.milc`

482.sphinx3: `-fast`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

450.soplex: `Same as 447.dealII`

453.povray: `Same as 447.dealII`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `basepeak = yes`

434.zeusmp: `-fast`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

Benchmarks using both Fortran and C:

435.gromacs: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

436.cactusADM: `basepeak = yes`

454.calculix: `Same as 435.gromacs`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

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