SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

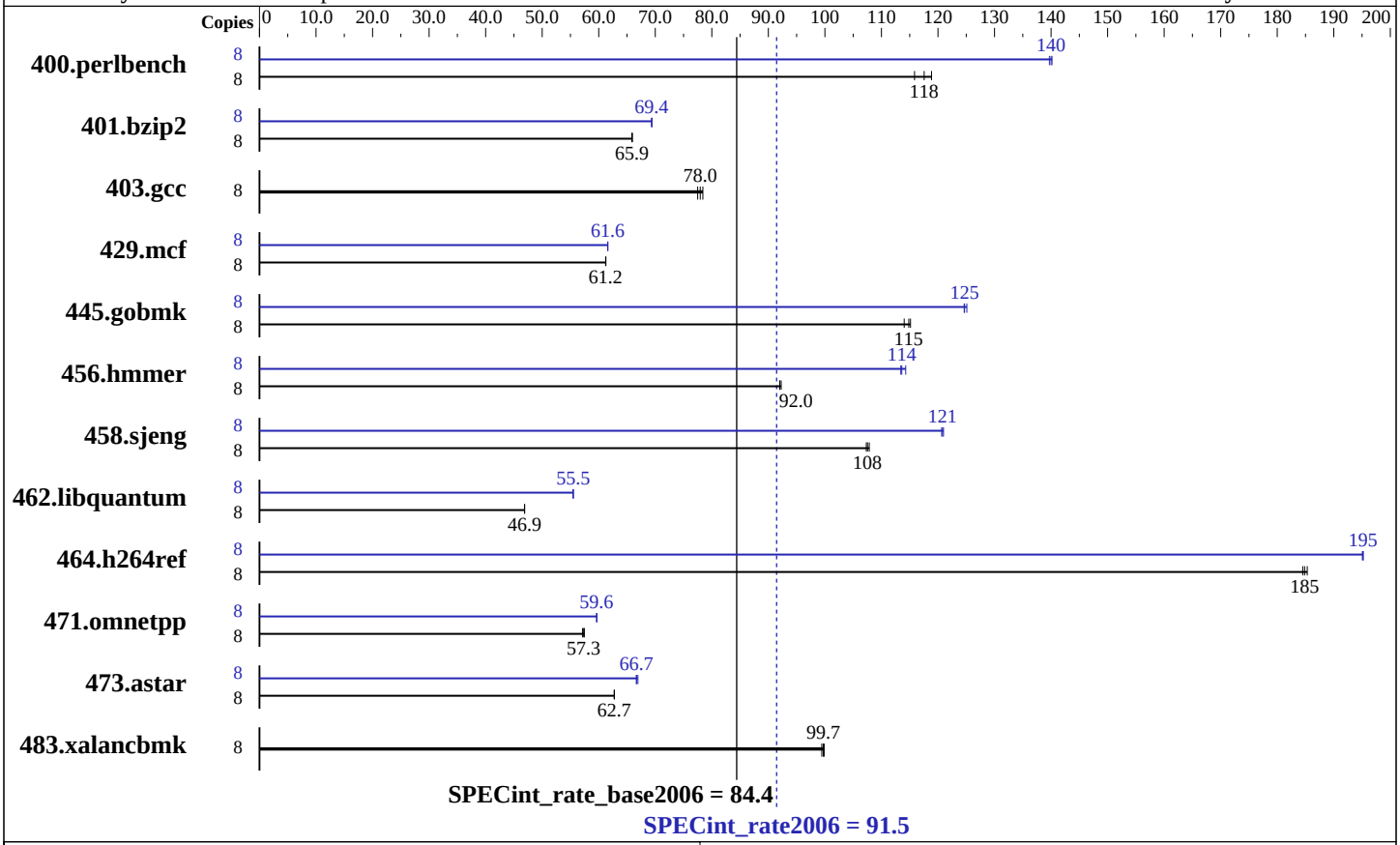
Acer Altos R720 (Intel Xeon E5345)

SPECint®_rate2006 = 91.5

SPECint_rate_base2006 = 84.4

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jun-2007
Hardware Availability: Nov-2006
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon E5345
CPU Characteristics:	1333MHz system bus
CPU MHz:	2330
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem:	1 x 73GB 10000RPM SAS HDD
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 EM64T (kernel 2.6.16.21-0.8-smp)
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ReiserFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Acer Altos R720 (Intel Xeon E5345)

SPECint_rate2006 = 91.5

SPECint_rate_base2006 = 84.4

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jun-2007
Hardware Availability: Nov-2006
Software Availability: Jun-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	675	116	**665**	**118**	658	119	8	559	140	558	140	**558**	**140**
401.bzip2	8	**1172**	**65.9**	1172	65.9	1171	65.9	8	1112	69.4	**1113**	**69.4**	1113	69.3
403.gcc	8	831	77.5	821	78.4	**826**	**78.0**	8	831	77.5	821	78.4	**826**	**78.0**
429.mcf	8	1191	61.3	**1192**	**61.2**	1192	61.2	8	1185	61.6	1184	61.6	**1184**	**61.6**
445.gobmk	8	**731**	**115**	729	115	736	114	8	**673**	**125**	673	125	671	125
456.hmmer	8	**811**	**92.0**	809	92.3	811	92.0	8	653	114	658	113	**657**	**114**
458.sjeng	8	898	108	**900**	**108**	902	107	8	800	121	802	121	**801**	**121**
462.libquantum	8	3535	46.9	**3536**	**46.9**	3537	46.9	8	2986	55.5	2989	55.5	**2988**	**55.5**
464.h264ref	8	955	185	**958**	**185**	960	185	8	**907**	**195**	907	195	908	195
471.omnetpp	8	**872**	**57.3**	875	57.1	870	57.5	8	**838**	**59.6**	838	59.6	838	59.7
473.astar	8	895	62.7	895	62.8	**895**	**62.7**	8	843	66.7	**842**	**66.7**	839	66.9
483.xalancbmk	8	553	99.9	**554**	**99.7**	555	99.4	8	553	99.9	**554**	**99.7**	555	99.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
This result was measured on the Acer Altos R520
The Altos R720 and Altos R520 are electronically equivalent.

BIOS settings
Hardware Prefetch is set to disabled
Adjacent Cache Line Prefetch is set to disabled

taskset was used to bind copies to cores
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/spec/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/10.0.023/bin/icc`
`-L/opt/intel/cce/10.0.023/lib`
`-I/opt/intel/cce/10.0.023/include`

456.hmmer: `/opt/intel/cce/10.0.023/bin/icc`
`-L/opt/intel/cce/10.0.023/lib`
`-I/opt/intel/cce/10.0.023/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec_div -ansi-alias`

456.hmmer: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/spec/cpu2006.1.0/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.41.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.41.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 13:29:56 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 September 2007.