SPEC® CINT2006 Result

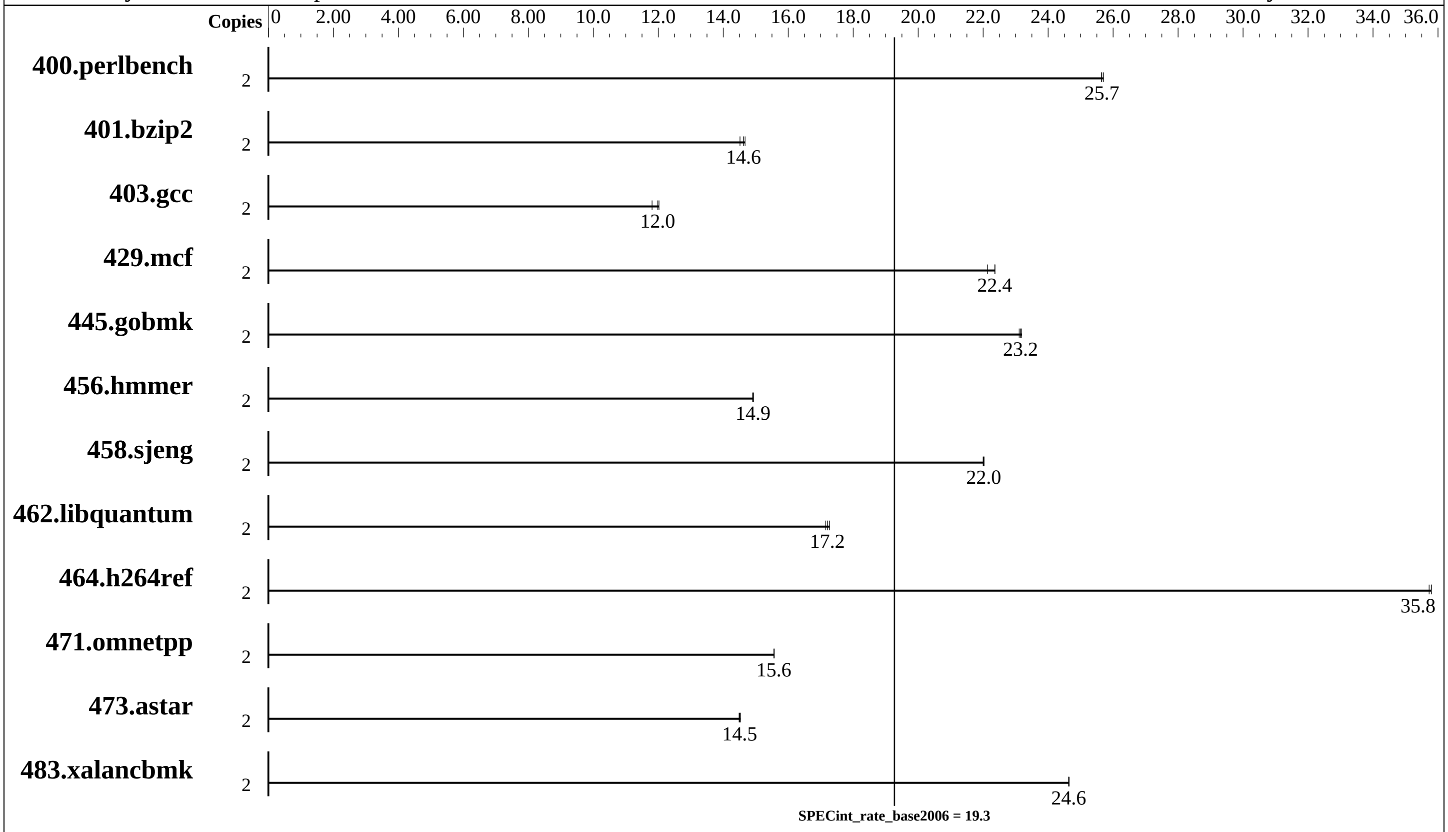
Hewlett-Packard Company
(Test Sponsor: Intel Corporation)

HP dv2000 (Intel Core 2 Duo T7100)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 19.3

CPU2006 license: 13		**Test date:**	Jun-2007
Test sponsor:	Intel Corporation	**Hardware Availability:**	Jun-2007
Tested by:	Intel Corporation	**Software Availability:**	Feb-2007



Hardware

CPU Name:	Intel Core 2 Duo T7100
CPU Characteristics:	1.8 GHz 2MB L2
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Micron MT16HTF12864HY-667B3 DDR2-667 CL5)
Disk Subsystem:	Fujitsu 120GB 5400rpm SATA HDD
Other Hardware:	None

Software

Operating System:	Windows Vista32 Business
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	SmartHeap Library Version 8.0 from http://www.microquill.com/

Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	760	25.7	762	25.6	**762**	**25.7**							
401.bzip2	2	1315	14.7	**1319**	**14.6**	1330	14.5							
403.gcc	2	1339	12.0	**1343**	**12.0**	1363	11.8							
429.mcf	2	824	22.1	816	22.4	**816**	**22.4**							
445.gobmk	2	905	23.2	908	23.1	**906**	**23.2**							
456.hmmer	2	1252	14.9	1250	14.9	**1251**	**14.9**							
458.sjeng	2	1100	22.0	**1099**	**22.0**	1099	22.0							
462.libquantum	2	2416	17.2	**2408**	**17.2**	2399	17.3							
464.h264ref	2	1237	35.8	**1237**	**35.8**	1239	35.7							
471.omnetpp	2	803	15.6	803	15.6	**803**	**15.6**							
473.astar	2	966	14.5	**968**	**14.5**	969	14.5							
483.xalancbmk	2	560	24.6	560	24.6	**560**	**24.6**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance
```

Base Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
`-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
      -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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