SPEC® CINT2006 Result
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Acer Incorporated

Acer Altos R720 (Intel Xeon X5355)

SPECint®_rate2006 =   53.4

SPECint_rate_base2006 = 49.1

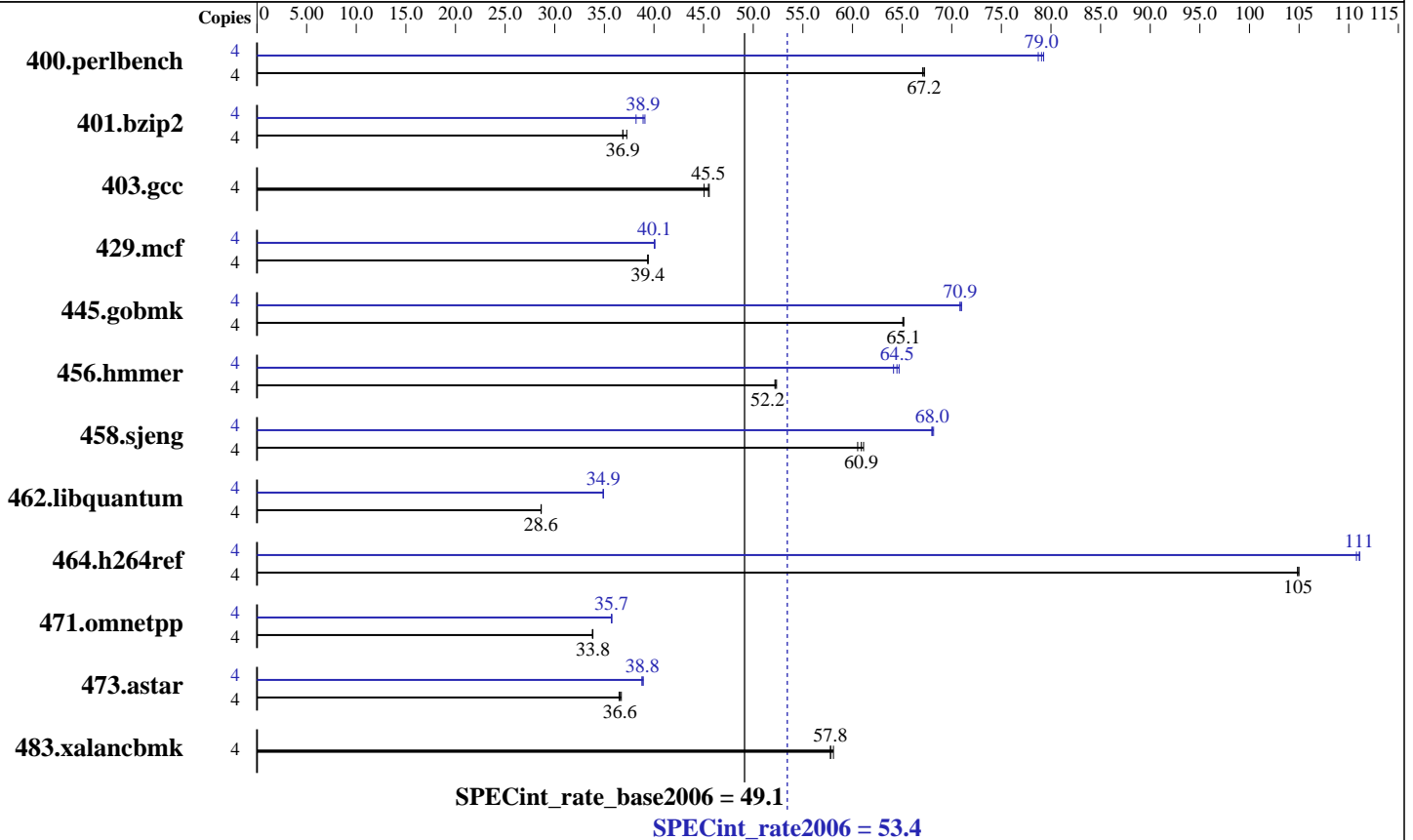**CPU2006 license:** 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jun-2007

Hardware Availability: Nov-2006

Software Availability: Jun-2007

Copies

Benchmark	Peak	Base
400.perlbench	79.0	67.2
401.bzip2	38.9	36.9
403.gcc		45.5
429.mcf	40.1	39.4
445.gobmk	70.9	65.1
456.hmmer	64.5	52.2
458.sjeng	68.0	60.9
462.libquantum	34.9	28.6
464.h264ref	111	105
471.omnetpp	35.7	33.8
473.astar	38.8	36.6
483.xalancbmk		57.8

SPECint_rate_base2006 = 49.1

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Hardware

CPU Name: Intel Xeon X5355

CPU Characteristics: 1333MHz system bus

CPU MHz: 2660

FPU: Integrated

CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip

CPU(s) orderable: 1, 2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

L3 Cache: None

Other Cache: None

Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)

Disk Subsystem: 1 x 73GB 10000RPM SAS HDD

Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 EM64T (kernel 2.6.16.21-0.8-smp)

Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023

Auto Parallel: No

File System: ReiserFS

System State: Default

Base Pointers: 32-bit

Peak Pointers: 32-bit

Other Software: None

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**581**	**67.2**	583	67.1	581	67.3	4	493	79.3	**494**	**79.0**	496	78.7
401.bzip2	4	1036	37.3	**1047**	**36.9**	1047	36.9	4	1011	38.2	**992**	**38.9**	988	39.1
403.gcc	4	707	45.6	715	45.0	**708**	**45.5**	4	707	45.6	715	45.0	**708**	**45.5**
429.mcf	4	926	39.4	**926**	**39.4**	927	39.4	4	**911**	**40.1**	911	40.0	910	40.1
445.gobmk	4	645	65.1	644	65.2	**644**	**65.1**	4	**592**	**70.9**	592	70.8	591	71.0
456.hmmer	4	713	52.4	**714**	**52.2**	715	52.2	4	577	64.7	582	64.1	**579**	**64.5**
458.sjeng	4	**795**	**60.9**	792	61.1	800	60.5	4	710	68.2	**711**	**68.0**	712	68.0
462.libquantum	4	**2896**	**28.6**	2896	28.6	2894	28.6	4	**2375**	**34.9**	2376	34.9	2375	34.9
464.h264ref	4	**844**	**105**	844	105	843	105	4	**797**	**111**	799	111	797	111
471.omnetpp	4	739	33.8	740	33.8	**739**	**33.8**	4	700	35.7	699	35.7	**700**	**35.7**
473.astar	4	**768**	**36.6**	765	36.7	770	36.5	4	721	38.9	**723**	**38.8**	724	38.8
483.xalancbmk	4	475	58.1	478	57.8	**478**	**57.8**	4	475	58.1	478	57.8	**478**	**57.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
  This result was measured on the Acer Altos R520
  The Altos R720 and Altos R520 are electronically equivalent.

  taskset was used to bind copies to cores
```

Base Compiler Invocation

C benchmarks:
    `icc`

C++ benchmarks:
    `icpc`

Base Portability Flags

    400.perlbench: `-DSPEC_CPU_LINUX_IA32`
    462.libquantum: `-DSPEC_CPU_LINUX`
    483.xalancbmk: `-DSPEC_CPU_LINUX`

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Base Optimization Flags

C benchmarks:
```
-fast
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/10.0.023/bin/icc
           -L/opt/intel/cce/10.0.023/lib
           -I/opt/intel/cce/10.0.023/include
```

```
456.hmmer: /opt/intel/cce/10.0.023/bin/icc
           -L/opt/intel/cce/10.0.023/lib
           -I/opt/intel/cce/10.0.023/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

```
   400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
                  -prefetch

       401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

         403.gcc: basepeak = yes

         429.mcf: -fast -prefetch

       445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
                  -no-prec_div -ansi-alias

       456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
                  -ansi-alias

       458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

  462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0
                  -prefetch -opt-streaming-stores always

      464.h264ref: Same as 456.hmmer
```

C++ benchmarks:

```
     471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
                  -no-prec_div -ansi-alias -Wl,-z,muldefs
                  -L/spec/cpu2006.1.0/lib -lsmartheap

        473.astar: Same as 471.omnetpp

    483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
         403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.41.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.41.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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