SPEC® CINT2006 Result

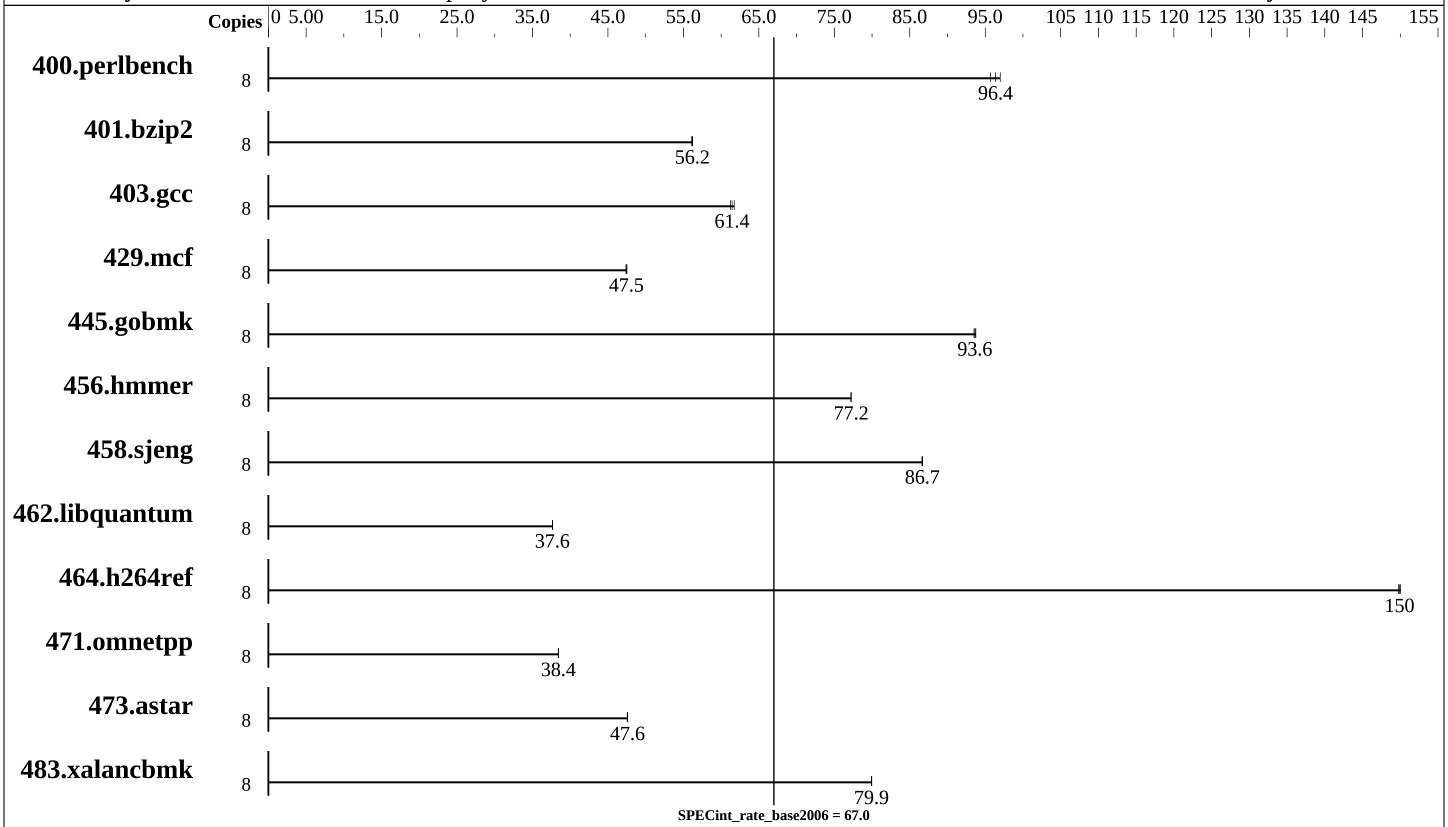
Hewlett-Packard Company

ProLiant DL380 G5
(1.86 GHz, Intel Xeon processor E5320)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 67.0

CPU2006 license: 3		**Test date:**	Jun-2007
Test sponsor:	Hewlett-Packard Company	**Hardware Availability:**	Nov-2006
Tested by:	Hewlett-Packard Company	**Software Availability:**	Jun-2007



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB PC2-5300F CL5)
Disk Subsystem:	1x72 GB 10 K SAS
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C/C++ Compiler for applications running on IA-32; Version 10.0, Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multi-user run level 3
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**811**	**96.4**	817	95.7	806	97.0							
401.bzip2	8	1376	56.1	**1374**	**56.2**	1373	56.2							
403.gcc	8	**1048**	**61.4**	1051	61.3	1043	61.7							
429.mcf	8	1535	47.5	**1537**	**47.5**	1540	47.4							
445.gobmk	8	895	93.8	897	93.5	**896**	**93.6**							
456.hmmer	8	967	77.2	966	77.2	**966**	**77.2**							
458.sjeng	8	1116	86.7	1118	86.6	**1117**	**86.7**							
462.libquantum	8	4402	37.7	4404	37.6	**4403**	**37.6**							
464.h264ref	8	1182	150	1180	150	**1181**	**150**							
471.omnetpp	8	1302	38.4	1300	38.4	**1301**	**38.4**							
473.astar	8	1179	47.6	1181	47.5	**1180**	**47.6**							
483.xalancbmk	8	**691**	**79.9**	691	79.9	690	80.0							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
Hardware Prefetch Disabled in BIOS.
taskset utility used to bind CPU(s) to processes
"ulimit -s unlimited" set
binutils updated to version 2.17
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

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Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/compiler/SmartHeap_8_1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.02.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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