SPEC® CFP2006 Result

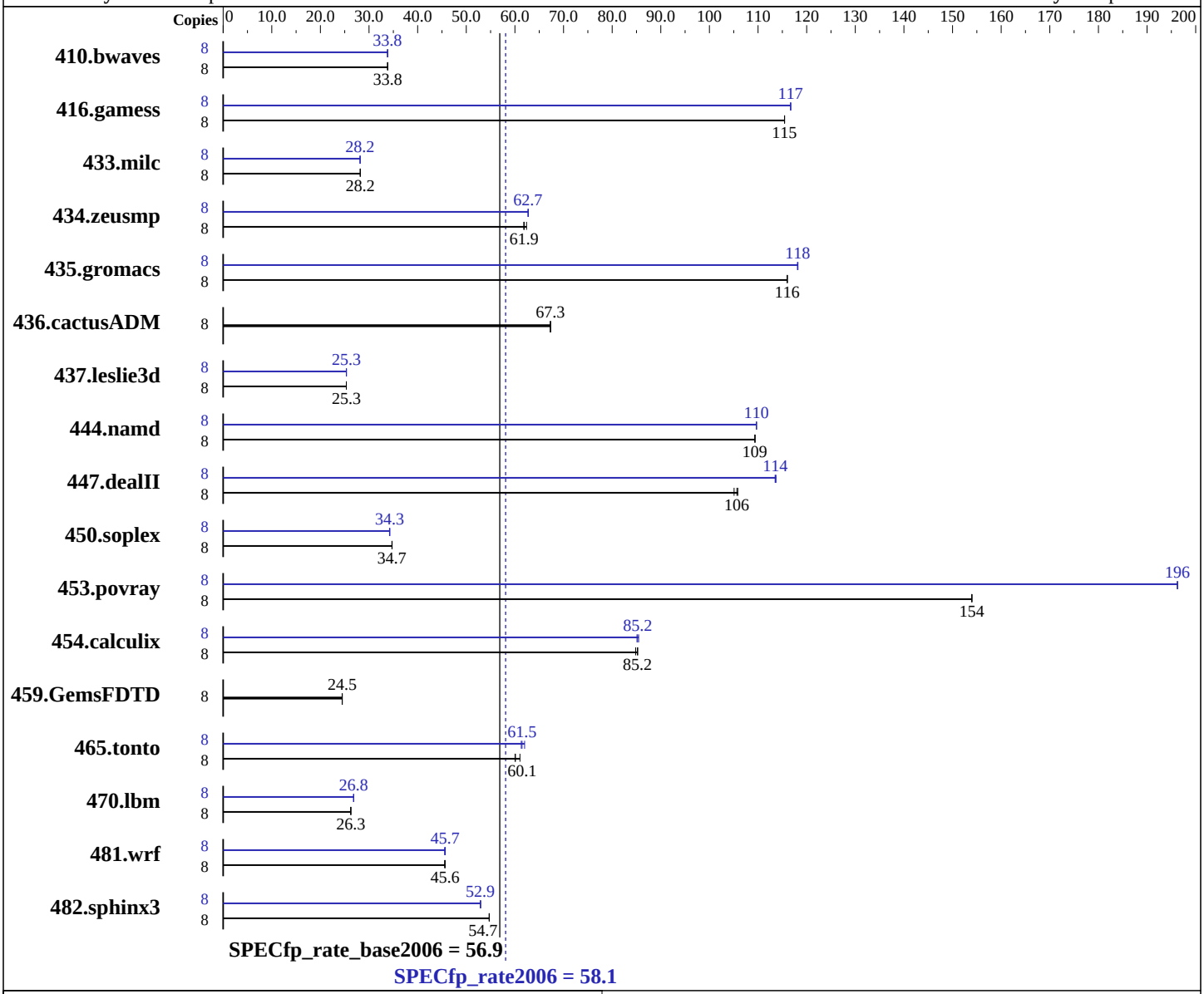
Supermicro
Motherboard X7DBT-INF

SPECfp®_rate2006 = 58.1

SPECfp_rate_base2006 = 56.9

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2007
Hardware Availability: May-2007
Software Availability: Apr-2007



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66GHz 1333MHz System Bus
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	Windows Server 2003 Enterprise Edition W/ SP1
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Intel Fortran Compiler for IA32 version 10.0 Build 20070426 Package ID: W_FC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 X 2GB ECC PC2-5300, CL5, FBDIMM)
Disk Subsystem: 250GB SATA, 7200RPM
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0 from http://www.microquill.com/

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3209	33.9	**3212**	**33.8**	3218	33.8	8	**3217**	**33.8**	3210	33.9	3218	33.8
416.gamess	8	**1357**	**115**	1357	115	1357	115	8	**1342**	**117**	1342	117	1342	117
433.milc	8	2604	28.2	2604	28.2	**2604**	**28.2**	8	2608	28.2	2607	28.2	**2607**	**28.2**
434.zeusmp	8	1167	62.4	1178	61.8	**1176**	**61.9**	8	**1161**	**62.7**	1161	62.7	1160	62.8
435.gromacs	8	492	116	**492**	**116**	492	116	8	484	118	**483**	**118**	483	118
436.cactusADM	8	1420	67.3	**1420**	**67.3**	1420	67.3	8	1420	67.3	**1420**	**67.3**	1420	67.3
437.leslie3d	8	2968	25.3	**2969**	**25.3**	2969	25.3	8	**2967**	**25.3**	2968	25.3	2967	25.3
444.namd	8	587	109	**587**	**109**	587	109	8	**585**	**110**	585	110	585	110
447.dealII	8	865	106	**866**	**106**	871	105	8	**806**	**114**	806	114	805	114
450.soplex	8	1920	34.7	**1922**	**34.7**	1922	34.7	8	1950	34.2	**1948**	**34.3**	1947	34.3
453.povray	8	276	154	**276**	**154**	276	154	8	217	196	217	196	**217**	**196**
454.calculix	8	**775**	**85.2**	774	85.3	778	84.8	8	776	85.1	772	85.4	**774**	**85.2**
459.GemsFDTD	8	3464	24.5	3469	24.5	**3467**	**24.5**	8	3464	24.5	3469	24.5	**3467**	**24.5**
465.tonto	8	1311	60.0	1290	61.0	**1310**	**60.1**	8	1284	61.3	1270	62.0	**1281**	**61.5**
470.lbm	8	4187	26.3	**4187**	**26.3**	4187	26.3	8	**4096**	**26.8**	4096	26.8	4096	26.8
481.wrf	8	1961	45.6	**1959**	**45.6**	1958	45.6	8	1956	45.7	1962	45.5	**1957**	**45.7**
482.sphinx3	8	2848	54.7	2851	54.7	**2849**	**54.7**	8	2944	53.0	2948	52.9	**2947**	**52.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with CSE-808TQ-980 case,
Product description located as of http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DBT-INF.cfm
The system bus runs at 1333 MHz
"start /b /wait /affinity" used to bind processes to CPUs.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000

Benchmarks using both Fortran and C:
-fast /F950000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Oa
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

470.lbm: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qscalar-rep- -Qprefetch /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Oa
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

450.soplex: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

453.povray: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: -fast /F950000000

416.gamess: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Ob0
-Qansi-alias -Qscalar-rep- /F950000000

434.zeusmp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qprec_div-
-Qunroll0 -Qscalar-rep- /F950000000

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Peak Optimization Flags (Continued)

437.leslie3d: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F950000000`

459.GemsFDTD: `basepeak = yes`

465.tonto: `Same as 437.leslie3d`

Benchmarks using both Fortran and C:

435.gromacs: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Oa /F950000000`

436.cactusADM: `basepeak = yes`

454.calculix: `-fast /F950000000`

481.wrf: `Same as 454.calculix`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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