SPEC® CINT2006 Result

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Fujitsu Siemens Computers

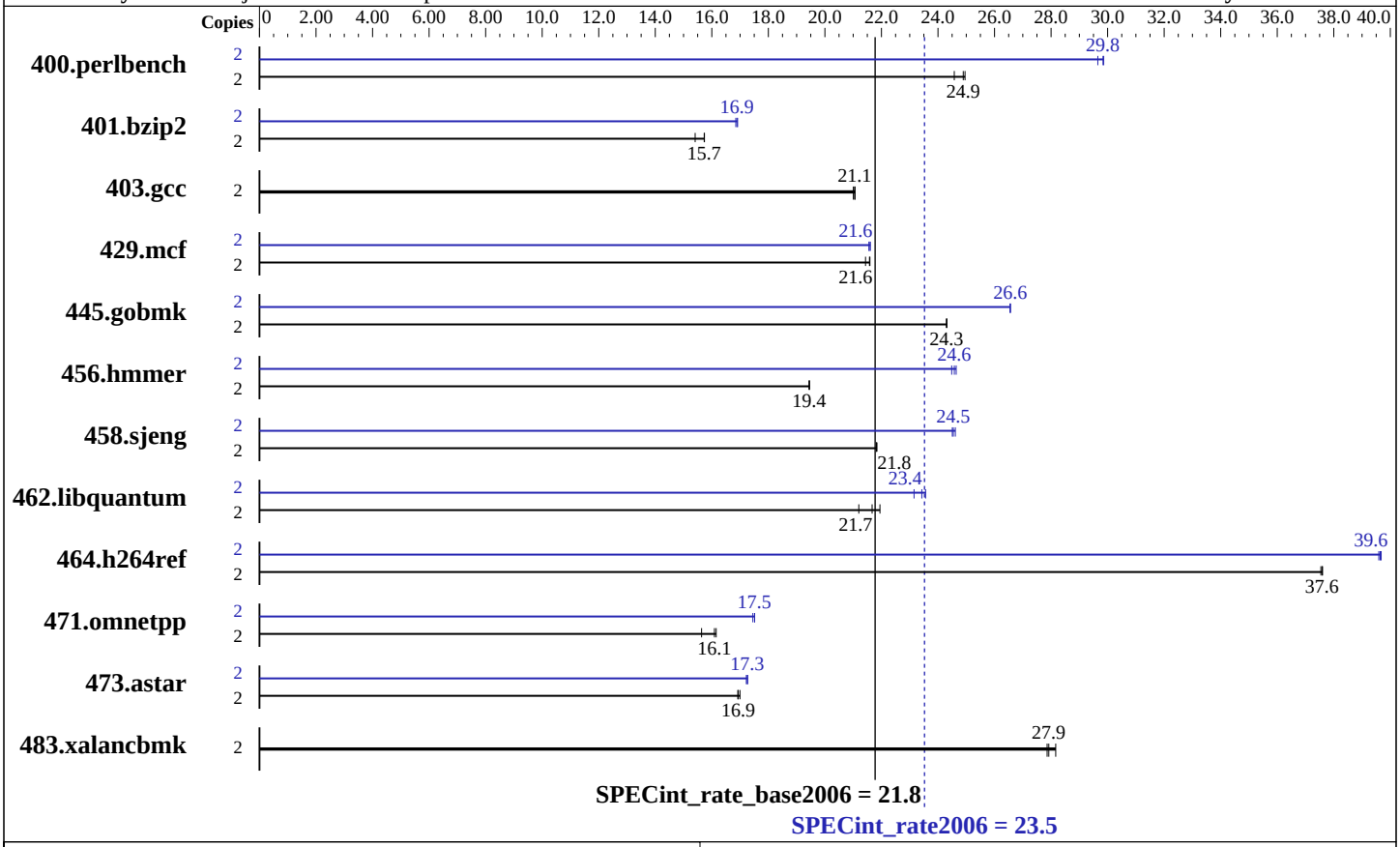
PRIMERGY TX200 S3, Intel Xeon processor 5120, 1.86 GHz

SPECint®_rate2006 = 23.5

SPECint_rate_base2006 = 21.8

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jul-2007
Hardware Availability: Jul-2006
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon 5120
CPU Characteristics:	1067 MHz system bus
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	Seagate ST336754SS (SAS, 36GB, 15000rpm)
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**785**	**24.9**	795	24.6	783	25.0	2	654	29.9	**655**	**29.8**	659	29.7
401.bzip2	2	1226	15.7	**1227**	**15.7**	1252	15.4	2	1141	16.9	**1143**	**16.9**	1146	16.8
403.gcc	2	766	21.0	**765**	**21.1**	764	21.1	2	766	21.0	**765**	**21.1**	764	21.1
429.mcf	2	851	21.4	845	21.6	**845**	**21.6**	2	846	21.6	844	21.6	**846**	**21.6**
445.gobmk	2	**863**	**24.3**	863	24.3	863	24.3	2	790	26.6	**790**	**26.6**	790	26.6
456.hmmer	2	959	19.5	**960**	**19.4**	960	19.4	2	762	24.5	757	24.6	**759**	**24.6**
458.sjeng	2	1108	21.8	1109	21.8	**1109**	**21.8**	2	**986**	**24.5**	983	24.6	987	24.5
462.libquantum	2	1955	21.2	1888	21.9	**1912**	**21.7**	2	**1769**	**23.4**	1759	23.6	1790	23.2
464.h264ref	2	1179	37.5	**1178**	**37.6**	1177	37.6	2	1116	39.7	**1117**	**39.6**	1118	39.6
471.omnetpp	2	799	15.6	**777**	**16.1**	774	16.2	2	716	17.5	**714**	**17.5**	714	17.5
473.astar	2	830	16.9	**829**	**16.9**	826	17.0	2	813	17.3	815	17.2	**814**	**17.3**
483.xalancbmk	2	**494**	**27.9**	495	27.9	490	28.2	2	**494**	**27.9**	495	27.9	490	28.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-xT -O3 -ipo -no-prec-div -ansi-alias`
`-L/opt/SmartHeap_8_1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/10.0.023/bin/icc`
`-I/opt/intel/cce/10.0.023/include`
`-L/opt/intel/cce/10.0.023/lib`

456.hmmer: `/opt/intel/cce/10.0.023/bin/icc`
`-I/opt/intel/cce/10.0.023/include`
`-L/opt/intel/cce/10.0.023/lib`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -prefetch -L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 400.perlbench`

456.hmmer: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll2`

458.sjeng: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll4`

462.libquantum: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -prefetch -opt-streaming-stores always`

464.h264ref: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -ansi-alias -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml`

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