SPEC® CINT2006 Result

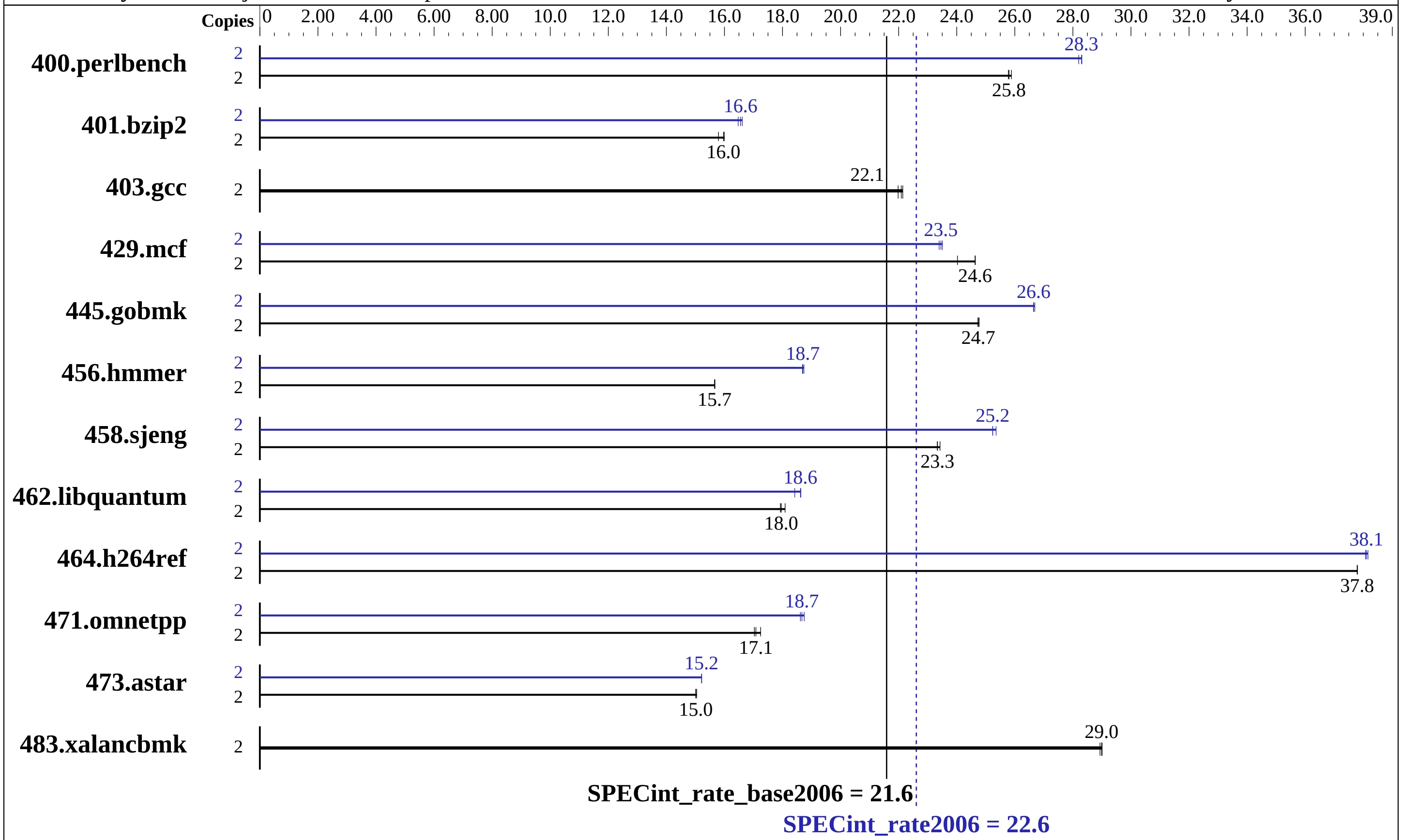
Fujitsu Siemens Computers

PRIMERGY TX120, Intel Xeon processor 3040, 1.86 GHz

SPECint®_rate2006 = 22.6

SPECint_rate_base2006 = 21.6

CPU2006 license: 22		**Test date:**	Jun-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Sep-2006
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Mar-2007



Hardware

CPU Name:	Intel Xeon 3040
CPU Characteristics:	1067 MHz system bus
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
Disk Subsystem:	Seagate ST973401SS (SAS, 73GB 10000rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ReiserFS
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	2	755	25.9	758	25.8	**757**	**25.8**	2	693	28.2	**691**	**28.3**	690	28.3
401.bzip2	2	1207	16.0	**1209**	**16.0**	1222	15.8	2	**1166**	**16.6**	1162	16.6	1172	16.5
403.gcc	2	732	22.0	727	22.1	**729**	**22.1**	2	732	22.0	727	22.1	**729**	**22.1**
429.mcf	2	759	24.0	**741**	**24.6**	740	24.6	2	780	23.4	**778**	**23.5**	776	23.5
445.gobmk	2	**848**	**24.7**	848	24.7	847	24.8	2	786	26.7	**787**	**26.6**	787	26.6
456.hmmer	2	1192	15.7	**1191**	**15.7**	1191	15.7	2	**998**	**18.7**	999	18.7	996	18.7
458.sjeng	2	1037	23.3	1033	23.4	**1037**	**23.3**	2	954	25.4	**959**	**25.2**	959	25.2
462.libquantum	2	2312	17.9	2291	18.1	**2308**	**18.0**	2	2250	18.4	2224	18.6	**2226**	**18.6**
464.h264ref	2	1171	37.8	1171	37.8	**1171**	**37.8**	2	**1161**	**38.1**	1162	38.1	1160	38.2
471.omnetpp	2	734	17.0	725	17.2	**732**	**17.1**	2	671	18.6	**670**	**18.7**	667	18.7
473.astar	2	**935**	**15.0**	933	15.0	935	15.0	2	923	15.2	923	15.2	**923**	**15.2**
483.xalancbmk	2	476	29.0	**476**	**29.0**	477	28.9	2	476	29.0	**476**	**29.0**	477	28.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

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'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.


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http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_X64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

456.hmmer: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

462.libquantum: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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