Fujitsu Siemens Computers
PRIMERGY TX120, Intel Xeon processor 3070, 2.66 GHz

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>SPECint_rate2006</th>
<th>SPECint_rate_base2006</th>
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</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>36.1</td>
<td>39.3</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>24.5</td>
<td>29.8</td>
</tr>
<tr>
<td>403.gcc</td>
<td>25.7</td>
<td>31.2</td>
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<tr>
<td>429.mcf</td>
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<tr>
<td>445.gobmk</td>
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</tr>
<tr>
<td>456.hmmer</td>
<td>18.9</td>
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<td>458.sjeng</td>
<td>22.2</td>
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<tr>
<td>462.libquantum</td>
<td>18.7</td>
<td>36.0</td>
</tr>
<tr>
<td>464.h264ref</td>
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<td>54.8</td>
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<tr>
<td>471.omnetpp</td>
<td>23.1</td>
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</tr>
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<td></td>
</tr>
<tr>
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<td></td>
</tr>
</tbody>
</table>

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Hardware
- CPU Name: Intel Xeon 3070
- CPU Characteristics: 1067 MHz system bus
- CPU MHz: 2667
- FPU: Integrated
- CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
- CPU(s) orderable: 1 chip
- Primary Cache: 32 KB I + 32 KB D on chip per core
- Secondary Cache: 4 MB I+D on chip per chip
- L3 Cache: None
- Other Cache: None
- Memory: 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
- Disk Subsystem: Seagate ST973401SS (SAS, 73GB 10000rpm)
- Other Hardware: None

Software
- Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86,64
- Auto Parallel: No
- File System: ReiserFS
- System State: Multiuser, Runlevel 3
- Base Pointers: 32-bit
- Peak Pointers: 32/64-bit
- Other Software: Smart Heap Library, Version 8.1

Test date: Jun-2007
Hardware Availability: May-2007
Software Availability: Mar-2007

Fujitsu Siemens Computers
GHz

SPECint_rate2006 = 30.7
SPECint_rate_base2006 = 29.3
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base Copies</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Peak Copies</th>
<th>Peak Seconds</th>
<th>Peak Ratio</th>
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<td>355</td>
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</tr>
</tbody>
</table>

### Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

### General Notes

The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with 64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

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http://www.fujitsu-siemens.com/countries

### Base Compiler Invocation

<table>
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<th>C benchmarks:</th>
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<td>C++ benchmarks:</td>
<td>icpc</td>
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**SPEC CINT2006 Result**

**Fujitsu Siemens Computers**  
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| SPECint_rate2006 | 30.7 |
| SPECint_rate_base2006 | 29.3 |

**CPU2006 license:** 22  
**Test sponsor:** Fujitsu Siemens Computers  
**Tested by:** Fujitsu Siemens Computers

| Test date | Jun-2007 |
| Hardware Availability | May-2007 |
| Software Availability | Mar-2007 |

**Base Portability Flags**

- 400.perlbench: -DSPEC_CPU_LINUX_X64
- 462.libquantum: -DSPEC_CPU_LINUX
- 483.xalancbmk: -DSPEC_CPU_LINUX

**Base Optimization Flags**

**C benchmarks:**  
- -fast

**C++ benchmarks:**  
- -xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

**Peak Compiler Invocation**

**C benchmarks** (except as noted below):

- icc

  - 401.bzip2: /opt/intel/cce/9.1.047/bin/icc  
    -I/opt/intel/cce/9.1.047/include  
    -L/opt/intel/cce/9.1.047/lib

  - 456.hmmer: /opt/intel/cce/9.1.047/bin/icc  
    -I/opt/intel/cce/9.1.047/include  
    -L/opt/intel/cce/9.1.047/lib

  - 462.libquantum: /opt/intel/cce/9.1.047/bin/icc  
    -I/opt/intel/cce/9.1.047/include  
    -L/opt/intel/cce/9.1.047/lib

**C++ benchmarks:**

- icpc

**Peak Portability Flags**

- 400.perlbench: -DSPEC_CPU_LINUX_X64
- 401.bzip2: -DSPEC_CPU_LP64
- 456.hmmer: -DSPEC_CPU_LP64
- 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
- 483.xalancbmk: -DSPEC_CPU_LINUX
**Fujitsu Siemens Computers**  
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**SPECint_rate2006 = 30.7**  
**SPECint_rate_base2006 = 29.3**

**Peak Optimization Flags**

C benchmarks:

- 400.perlbench: `prof_gen(pass 1) prof_use(pass 2) fast`
- 401.bzip2: `fast`
- 403.gcc: `basepeak = yes`
- 429.mcf: `prof_gen(pass 1) prof_use(pass 2) fast
  -L/opt/SmartHeap_8_1/lib -lsmartheap`
- 445.gobmk: `Same as 429.mcf`
- 456.hmmer: `Same as 400.perlbench`
- 458.sjeng: `Same as 429.mcf`
- 462.libquantum: `Same as 400.perlbench`
- 464.h264ref: `Same as 429.mcf`

C++ benchmarks:

- 471.omnetpp: `prof_gen(pass 1) prof_use(pass 2) XP -O3 -ipo
  -no-prec-div
  -L/opt/SmartHeap_8_1/lib -lsmartheap`
- 473.astar: `prof_gen(pass 1) prof_use(pass 2) fast
  -L/opt/SmartHeap_8_1/lib -lsmartheap`
- 483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at [http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

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For questions about this result, please contact the tester. For other inquiries, please contact webmaster@spec.org.