SPEC® CINT2006 Result

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NEC Corporation

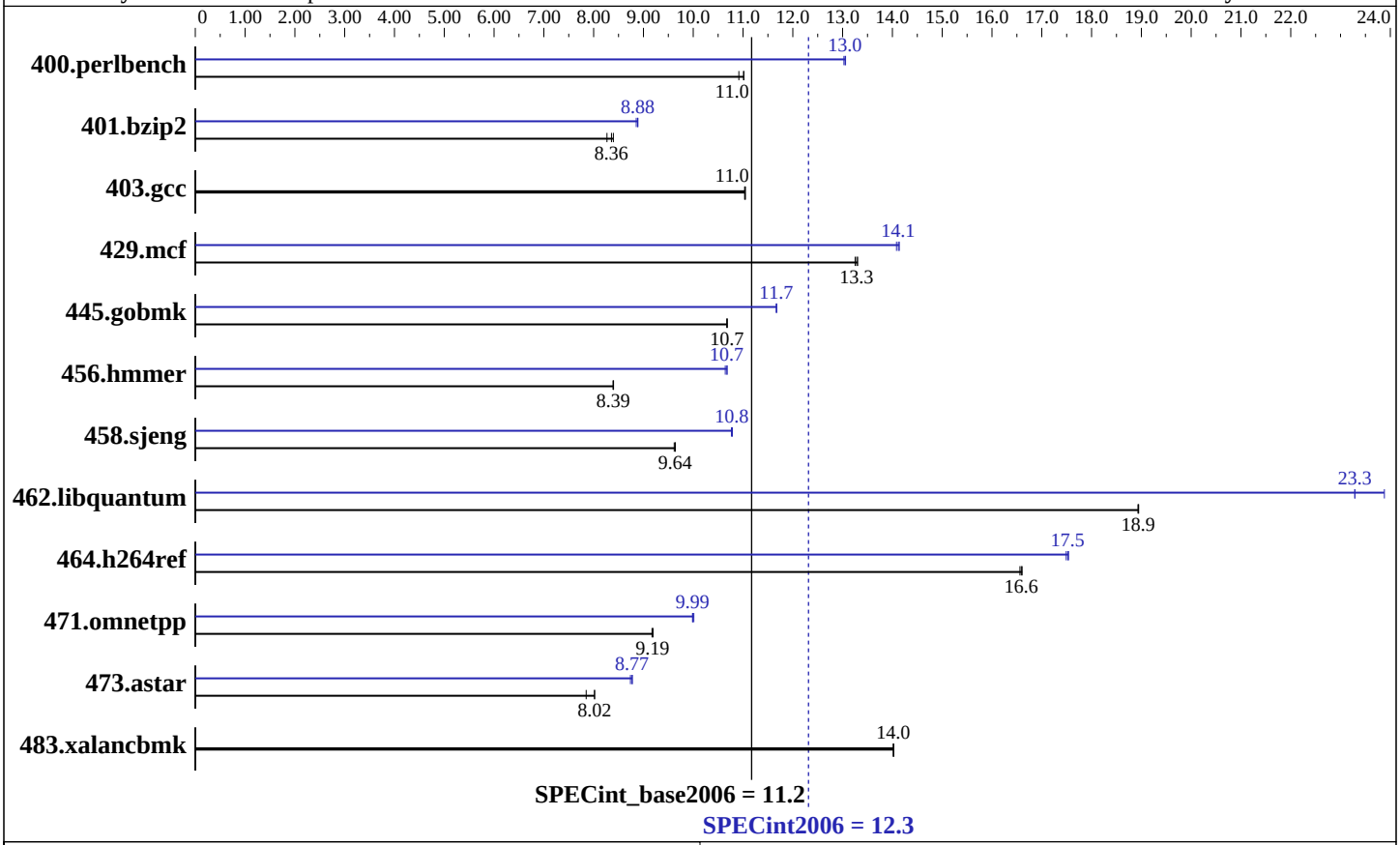
Express5800/120Ri-2
(Intel Xeon Prosessor 5110)

SPECint®2006 = 12.3

SPECint_base2006 = 11.2

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Jun-2007
Hardware Availability: May-2007
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon 5110
CPU Characteristics:	1.60 GHz, 4MB L2, 1066MHz bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	1x73.2 GB SAS, 15000RPM
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	895	10.9	887	11.0	**887**	**11.0**	750	13.0	748	13.1	**749**	**13.0**
401.bzip2	1149	8.40	1167	8.27	**1155**	**8.36**	1086	8.89	1090	8.86	**1086**	**8.88**
403.gcc	730	11.0	**729**	**11.0**	729	11.0	730	11.0	**729**	**11.0**	729	11.0
429.mcf	688	13.3	**687**	**13.3**	686	13.3	645	14.1	**646**	**14.1**	648	14.1
445.gobmk	983	10.7	**982**	**10.7**	982	10.7	899	11.7	899	11.7	**899**	**11.7**
456.hmmer	1111	8.40	**1112**	**8.39**	1112	8.39	874	10.7	**874**	**10.7**	877	10.6
458.sjeng	1255	9.64	**1256**	**9.64**	1258	9.62	**1123**	**10.8**	1122	10.8	1124	10.8
462.libquantum	1094	18.9	1094	18.9	**1094**	**18.9**	890	23.3	**890**	**23.3**	868	23.9
464.h264ref	1336	16.6	**1334**	**16.6**	1333	16.6	1262	17.5	**1263**	**17.5**	1265	17.5
471.omnetpp	**680**	**9.19**	681	9.17	680	9.19	**626**	**9.99**	624	10.0	626	9.99
473.astar	875	8.02	**876**	**8.02**	894	7.85	803	8.74	**800**	**8.77**	800	8.78
483.xalancbmk	492	14.0	492	14.0	**492**	**14.0**	492	14.0	492	14.0	**492**	**14.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode

The Express5800/120Rg-1(Intel Xeon Prosessor 5110) and
the Express5800/120Ri-2(Intel Xeon Prosessor 5110) models are electronically equivalent.
The results have been measured on a Express5800/120Ri-2(Intel Xeon Prosessor 5110) model.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/opt/SmartHeap_8.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/10.0.023/bin/icc`

456.hmmer: `/opt/intel/cce/10.0.023/bin/icc`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include -prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec_div -ansi-alias`

456.hmmer: `-L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include -prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/NEC-ic10-linux-flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/NEC-ic10-linux-flags.xml`

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