Supermicro
Motherboard X7DA8+

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<thead>
<tr>
<th>SPECint_rate2006 = 55.6</th>
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CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

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Hardware

- CPU Name: Intel Xeon L5310
- CPU Characteristics: 1.60GHz, 1066 MHz Bus
- CPU MHz: 1600
- FPU: Integrated
- CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
- CPU(s) orderable: 1, 2 chips
- Primary Cache: 32 KB I + 32 KB D on chip per core
- Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
- L3 Cache: None
- Other Cache: None
- Memory: 16 GB (8 X 2GB ECC PC2-5300, CL5, FBDIMM)
- Disk Subsystem: 750GB IDE, 7200RPM
- Other Hardware: None

Software

- Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20070322Z
- Microsoft Visual Studio .Net 2003 (for libraries)
- Auto Parallel: No
- File System: NTFS
- System State: Default
- Base Pointers: 32-bit
- Peak Pointers: 32-bit
- Other Software: SmartHeap Library Version 8.0
Supermicro Motherboard X7DA8+  

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<table>
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<tr>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### General Notes
Tested systems can be used with CSE-833S-R760 case.  
To ensure system stability, a 500W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]  
Product description located at [Supermicro](http://www.supermicro.com/products/motherboard/Xeon1333/5000X/X7DA8+.cfm)
The system bus runs at 1066 MHz

### Base Compiler Invocation
**C benchmarks:**
```
icl -Qvc7.1 -Qc99
```
**C++ benchmarks:**
```
icl -Qvc7.1
```

### Base Portability Flags
- 403.gcc: `-DSPEC_CPU_WIN32`
- 464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

### Base Optimization Flags
**C benchmarks:**
```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

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Test date: Apr-2007
Hardware Availability: May-2007
Software Availability: Apr-2007

Base Optimization Flags (Continued)

C++ benchmarks:
- fast -Qcxx_features /F5120000000 shlw32m.lib
- link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F5120000000
shlw32m.lib -link /FORCE:MULTIPLE
401.bzip2: Same as 400.perlbench
403.gcc: Same as 400.perlbench
429.mcf: basepeak = yes
445.gobmk: Same as 400.perlbench
456.hmmer: Same as 400.perlbench
458.sjeng: Same as 400.perlbench

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Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
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Motherboard X7DA8+

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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:
471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE
473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QXp -O2 -Qipo
-Qprec-div -Qunroll4 -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE
483.xalancbmk: Same as 471.omnetpp

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.