SPEC® CINT2006 Result

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Fujitsu Siemens Computers

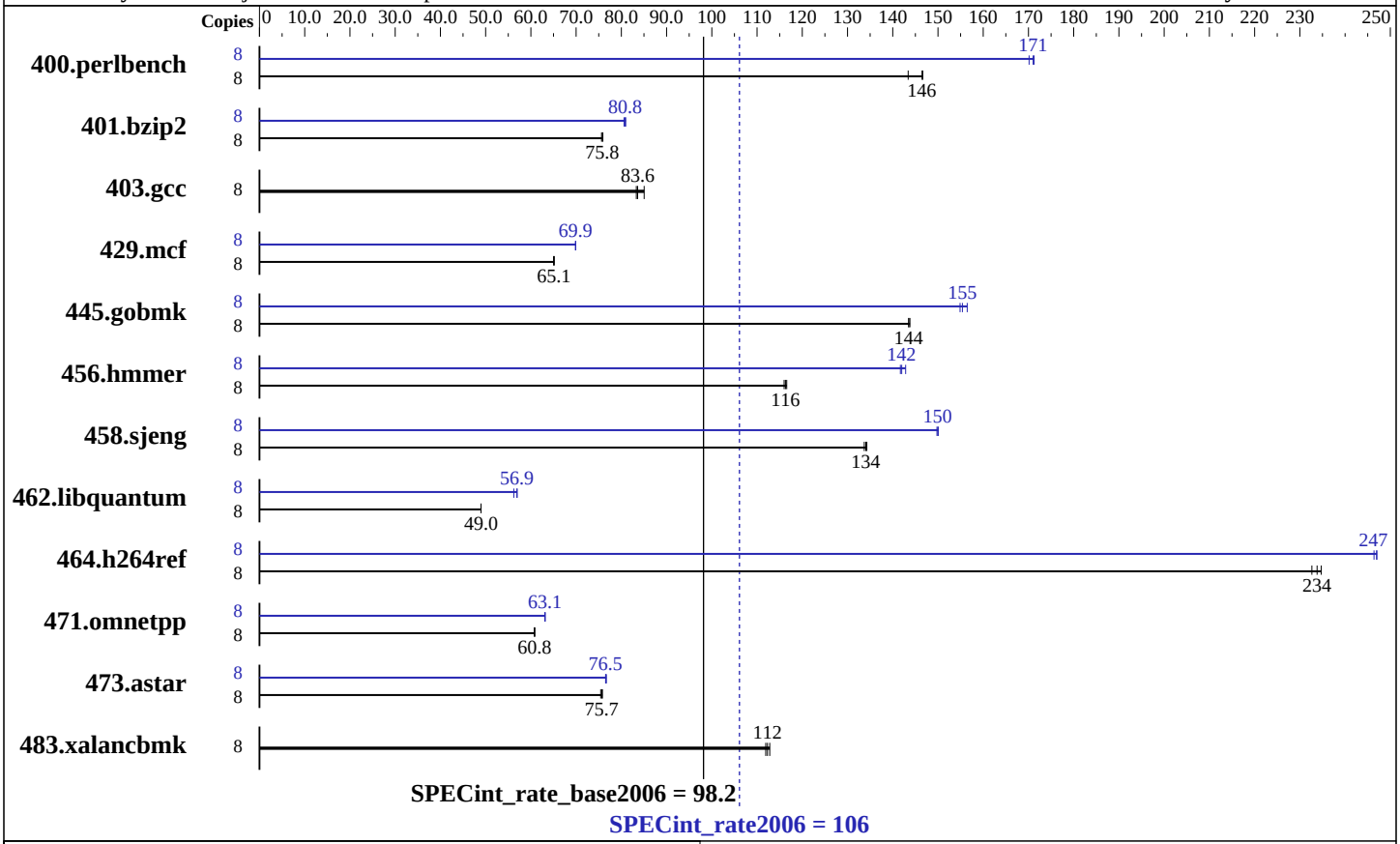
PRIMERGY RX200 S3, Intel Xeon processor X5365, 3.0 GHz

SPECint®_rate2006 = 106

SPECint_rate_base2006 = 98.2

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jun-2007
Hardware Availability: Aug-2007
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon X5365
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	Seagate ST373454SS (SAS, 73GB, 15000rpm)
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ReiserFS
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	545	143	533	147	**534**	**146**	8	459	170	**457**	**171**	456	171
401.bzip2	8	1017	75.9	1021	75.6	**1018**	**75.8**	8	958	80.6	**955**	**80.8**	953	81.0
403.gcc	8	757	85.0	**770**	**83.6**	773	83.3	8	757	85.0	**770**	**83.6**	773	83.3
429.mcf	8	1122	65.0	**1120**	**65.1**	1120	65.2	8	**1044**	**69.9**	1044	69.9	1044	69.9
445.gobmk	8	585	144	584	144	**585**	**144**	8	**540**	**155**	542	155	536	156
456.hmmer	8	644	116	**642**	**116**	640	117	8	527	142	**526**	**142**	523	143
458.sjeng	8	**722**	**134**	724	134	721	134	8	645	150	646	150	**646**	**150**
462.libquantum	8	3385	49.0	3384	49.0	**3385**	**49.0**	8	2911	57.0	**2912**	**56.9**	2948	56.2
464.h264ref	8	761	233	754	235	**757**	**234**	8	719	246	**717**	**247**	717	247
471.omnetpp	8	**822**	**60.8**	823	60.8	822	60.8	8	**792**	**63.1**	791	63.2	792	63.1
473.astar	8	**742**	**75.7**	741	75.8	744	75.4	8	**734**	**76.5**	734	76.5	732	76.7
483.xalancbmk	8	**492**	**112**	493	112	489	113	8	**492**	**112**	493	112	489	113

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xT -O3 -ipo -no-prec-div -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -prefetch -L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 400.perlbench`

456.hmmer: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll2`

458.sjeng: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll4`

462.libquantum: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -prefetch -opt-streaming-stores always`

464.h264ref: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -ansi-alias -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml`

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 13:03:26 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 July 2007.