SPEC® CINT2006 Result

Fujitsu Siemens Computers

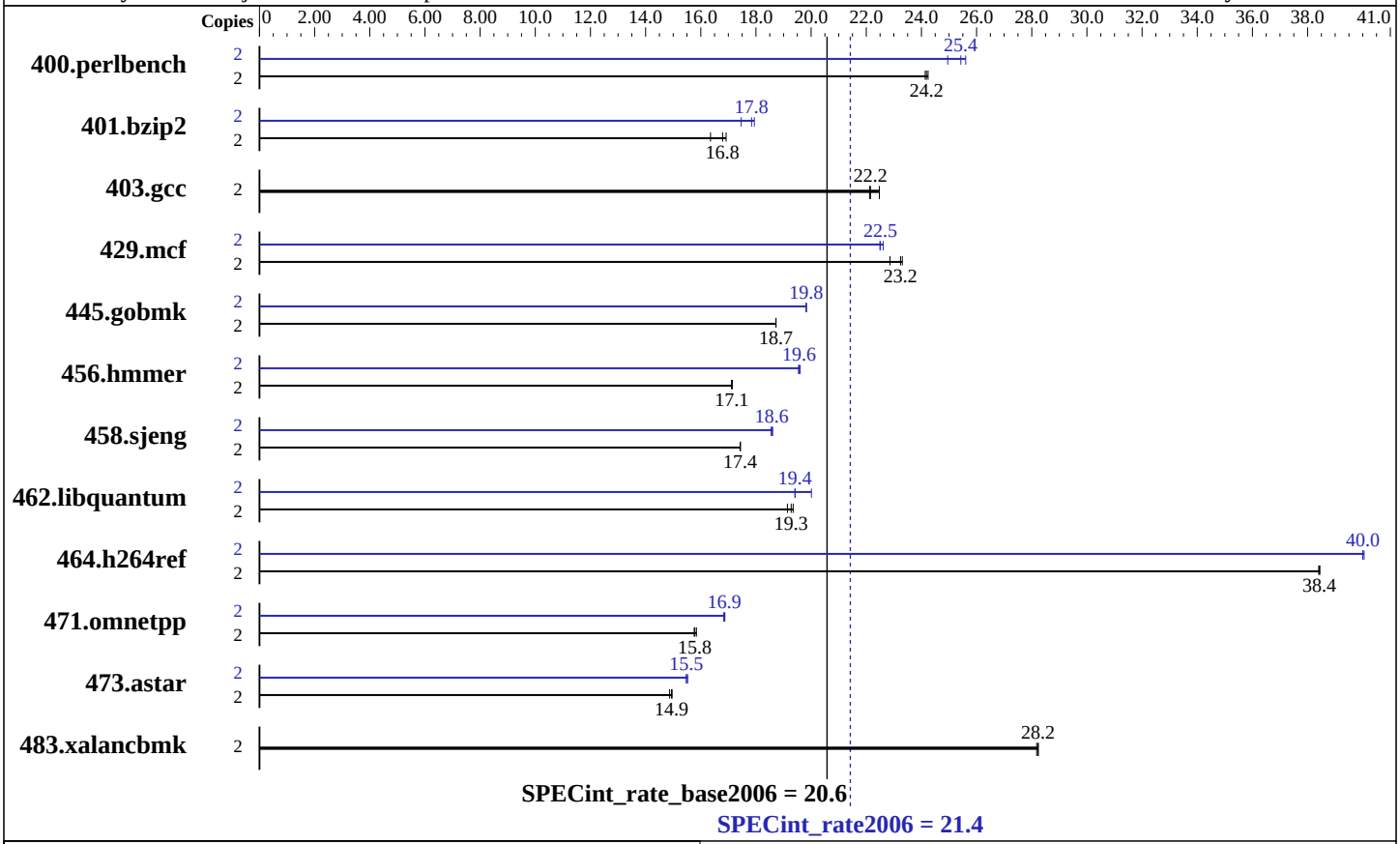
PRIMERGY RX100 S4, Intel Pentium D processor 945, 3.40 GHz

SPECint®_rate2006 = 21.4

SPECint_rate_base2006 = 20.6

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jun-2007
Hardware Availability: Sep-2006
Software Availability: Mar-2007



Hardware

CPU Name:	Intel Pentium D 945
CPU Characteristics:	800 MHz system bus
CPU MHz:	3400
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per core
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
Disk Subsystem:	Seagate ST373454SS (SAS, 73GB, 15000rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ReiserFS
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**808**	**24.2**	810	24.1	806	24.2	2	**768**	**25.4**	783	25.0	763	25.6
401.bzip2	2	**1149**	**16.8**	1180	16.4	1141	16.9	2	1076	17.9	1105	17.5	**1082**	**17.8**
403.gcc	2	728	22.1	716	22.5	**727**	**22.2**	2	728	22.1	716	22.5	**727**	**22.2**
429.mcf	2	798	22.9	**785**	**23.2**	783	23.3	2	811	22.5	**810**	**22.5**	807	22.6
445.gobmk	2	1121	18.7	**1120**	**18.7**	1120	18.7	2	1058	19.8	1058	19.8	**1058**	**19.8**
456.hmmer	2	1090	17.1	1089	17.1	**1089**	**17.1**	2	952	19.6	955	19.5	**954**	**19.6**
458.sjeng	2	1388	17.4	1389	17.4	**1388**	**17.4**	2	1305	18.6	1301	18.6	**1303**	**18.6**
462.libquantum	2	2165	19.1	2142	19.3	**2150**	**19.3**	2	2071	20.0	2134	19.4	**2134**	**19.4**
464.h264ref	2	**1152**	**38.4**	1152	38.4	1151	38.4	2	1106	40.0	1105	40.1	**1106**	**40.0**
471.omnetpp	2	793	15.8	**792**	**15.8**	789	15.8	2	741	16.9	**741**	**16.9**	743	16.8
473.astar	2	944	14.9	**940**	**14.9**	938	15.0	2	904	15.5	**907**	**15.5**	907	15.5
483.xalancbmk	2	489	28.2	490	28.2	**489**	**28.2**	2	489	28.2	490	28.2	**489**	**28.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

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http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 429.mcf`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 429.mcf`

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 429.mcf`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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