SPEC® CINT2006 Result

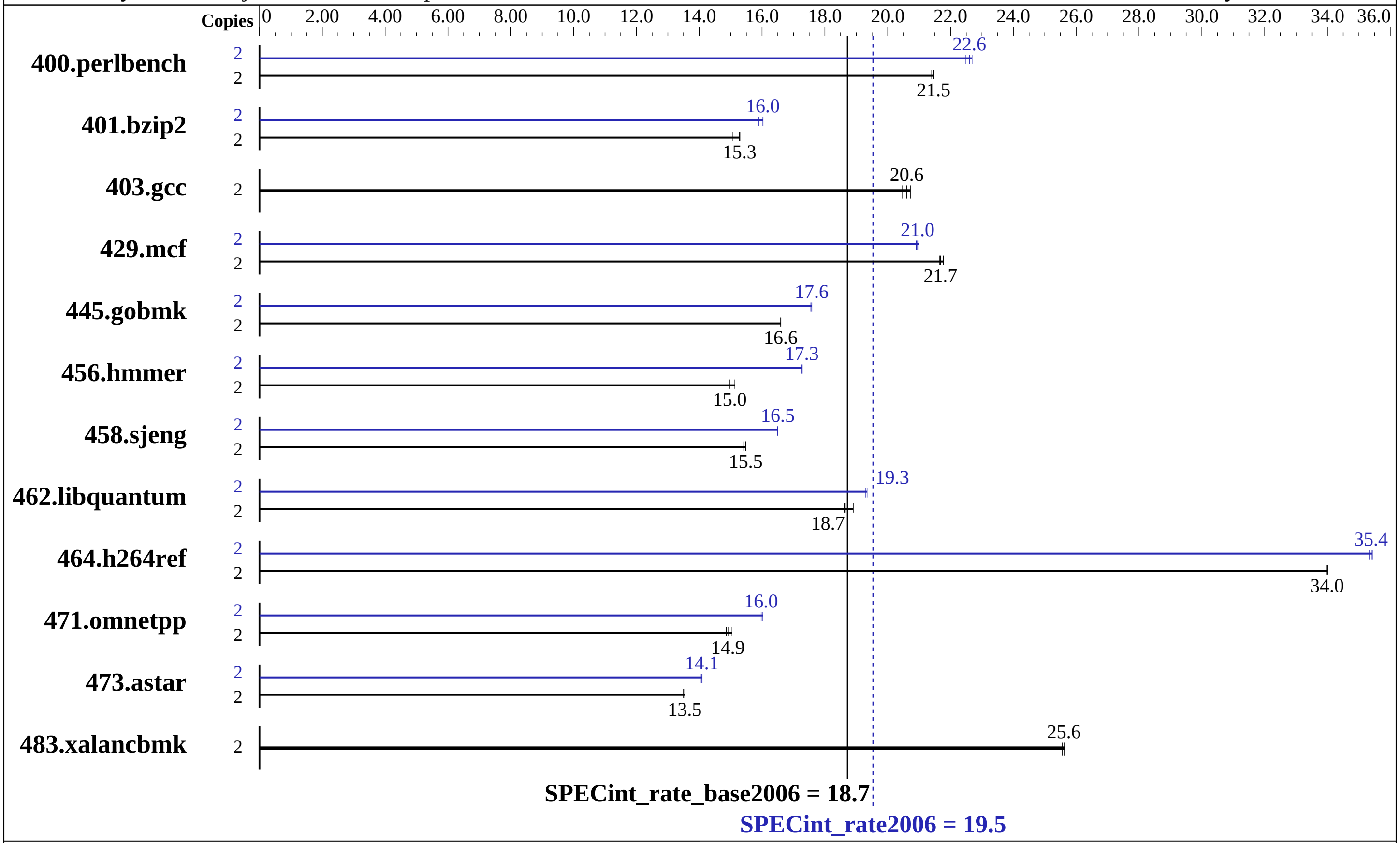
Fujitsu Siemens Computers

PRIMERGY RX100 S4, Intel Pentium D processor 925, 3.0 GHz

SPECint®_rate2006 = 19.5

SPECint_rate_base2006 = 18.7

CPU2006 license: 22		**Test date:**	Jun-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Oct-2006
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Mar-2007



Hardware	
CPU Name:	Intel Pentium D 925
CPU Characteristics:	800 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per core
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
Disk Subsystem:	Seagate ST373454SS (SAS, 73GB, 15000rpm)
Other Hardware:	None

Software	
Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ReiserFS
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	910	21.5	**910**	**21.5**	914	21.4	2	861	22.7	869	22.5	**865**	**22.6**
401.bzip2	2	1262	15.3	**1263**	**15.3**	1280	15.1	2	1204	16.0	1215	15.9	**1204**	**16.0**
403.gcc	2	**781**	**20.6**	777	20.7	786	20.5	2	**781**	**20.6**	777	20.7	786	20.5
429.mcf	2	842	21.7	**841**	**21.7**	838	21.8	2	872	20.9	**871**	**21.0**	869	21.0
445.gobmk	2	**1264**	**16.6**	1264	16.6	1264	16.6	2	1193	17.6	1197	17.5	**1193**	**17.6**
456.hmmer	2	**1246**	**15.0**	1287	14.5	1233	15.1	2	1080	17.3	1082	17.3	**1081**	**17.3**
458.sjeng	2	1570	15.4	1563	15.5	**1563**	**15.5**	2	**1466**	**16.5**	1467	16.5	1466	16.5
462.libquantum	2	**2220**	**18.7**	2192	18.9	2226	18.6	2	2142	19.3	2147	19.3	**2146**	**19.3**
464.h264ref	2	**1302**	**34.0**	1302	34.0	1303	34.0	2	1249	35.4	1253	35.3	**1250**	**35.4**
471.omnetpp	2	841	14.9	831	15.0	**838**	**14.9**	2	787	15.9	780	16.0	**783**	**16.0**
473.astar	2	**1037**	**13.5**	1041	13.5	1037	13.5	2	996	14.1	**997**	**14.1**	998	14.1
483.xalancbmk	2	**539**	**25.6**	538	25.6	540	25.6	2	**539**	**25.6**	538	25.6	540	25.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

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'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

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http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_X64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

456.hmmer: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

462.libquantum: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 429.mcf`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 429.mcf`

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 429.mcf`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo` `-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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