SPEC® CINT2006 Result

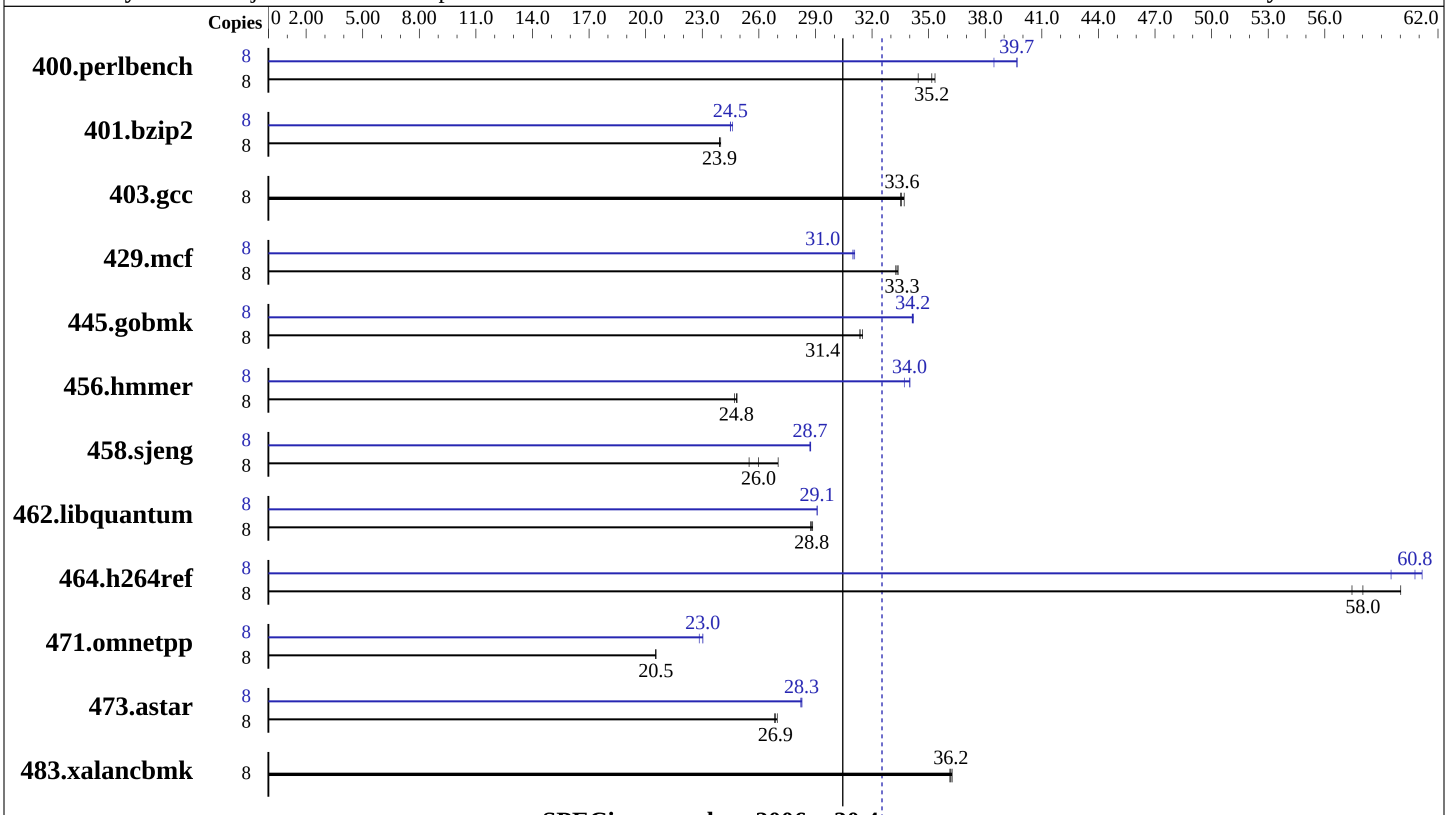
Fujitsu Siemens Computers

PRIMERGY TX600 S3, Intel Xeon processor 7110M, 2.60 GHz

SPECint®_rate2006 = 32.5

SPECint_rate_base2006 = 30.4

CPU2006 license: 22		**Test date:**	Jun-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Aug-2006
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Mar-2007



SPECint_rate_base2006 = 30.4

SPECint_rate2006 = 32.5

Hardware

CPU Name:	Intel Xeon 7110M
CPU Characteristics:	800 MHz system bus
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16x2 GB DDR2 PC2-3200R, 1 rank, CAS 3-3-3, with ECC)
Disk Subsystem:	Seagate ST973401SS (SAS, 73GB, 10000rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ReiserFS
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	2269	34.4	2212	35.3	**2222**	**35.2**	8	1969	39.7	**1970**	**39.7**	2032	38.5
401.bzip2	8	**3228**	**23.9**	3228	23.9	3220	24.0	8	3137	24.6	3153	24.5	**3152**	**24.5**
403.gcc	8	1922	33.5	1911	33.7	**1919**	**33.6**	8	1922	33.5	1911	33.7	**1919**	**33.6**
429.mcf	8	**2189**	**33.3**	2186	33.4	2194	33.3	8	2355	31.0	**2354**	**31.0**	2347	31.1
445.gobmk	8	**2675**	**31.4**	2664	31.5	2677	31.4	8	2459	34.1	2454	34.2	**2457**	**34.2**
456.hmmer	8	**3009**	**24.8**	3004	24.8	3022	24.7	8	2195	34.0	**2196**	**34.0**	2214	33.7
458.sjeng	8	**3725**	**26.0**	3799	25.5	3582	27.0	8	**3371**	**28.7**	3373	28.7	3367	28.7
462.libquantum	8	5770	28.7	5745	28.9	**5755**	**28.8**	8	**5699**	**29.1**	5699	29.1	5696	29.1
464.h264ref	8	2949	60.0	3082	57.4	**3051**	**58.0**	8	2975	59.5	2895	61.2	**2913**	**60.8**
471.omnetpp	8	**2434**	**20.5**	2433	20.6	2437	20.5	8	2189	22.8	**2171**	**23.0**	2171	23.0
473.astar	8	**2090**	**26.9**	2082	27.0	2094	26.8	8	**1987**	**28.3**	1985	28.3	1990	28.2
483.xalancbmk	8	1523	36.2	**1526**	**36.2**	1528	36.1	8	1523	36.2	**1526**	**36.2**	1528	36.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

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'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Enable

This result was measured on the PRIMERGY RX600 S3.
The PRIMERGY RX600 S3 and the PRIMERGY TX600 S3 are electronically equivalent.

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http://www.fujitsu-siemens.com/countries
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Base Compiler Invocation

C benchmarks:
 icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_X64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

456.hmmer: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

462.libquantum: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`

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Peak Portability Flags (Continued)

456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast -L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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