SPEC® CFP2006 Result

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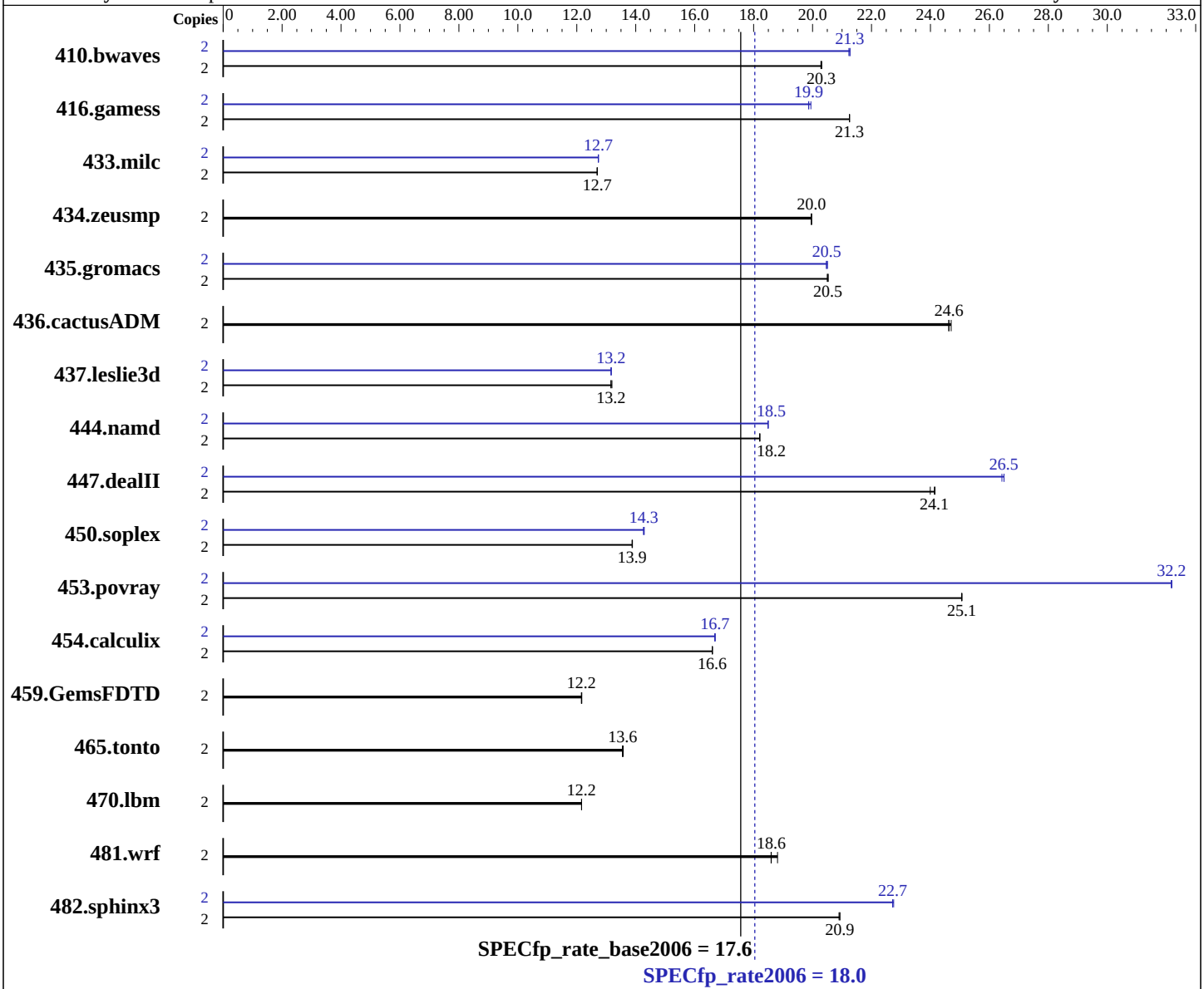
Supermicro
Motherboard PDSMU

SPECfp®_rate2006 = 18.0

SPECfp_rate_base2006 = 17.6

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Apr-2007
Hardware Availability: Dec-2006
Software Availability: Mar-2007



Hardware

CPU Name:	Intel Core 2 Duo E4300
CPU Characteristics:	1.8 GHz, 800 MHz bus
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip

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Software

Operating System:	Windows Server 2003 Enterprise Edition w/ SP1
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20070322Z Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit

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L3 Cache: None
Other Cache: None
Memory: 4 GB (2 X 2GB ECC, CL4, 533MHz, UBDIMM)
Disk Subsystem: 250GB SATA, 7200RPM
Other Hardware: None

Other Software: SmartHeap Library Version 8.0

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	1340	20.3	**1340**	**20.3**	1338	20.3	2	1278	21.3	**1278**	**21.3**	1280	21.2
416.gamess	2	1843	21.3	1842	21.3	**1843**	**21.3**	2	1964	19.9	1971	19.9	**1971**	**19.9**
433.milc	2	**1446**	**12.7**	1447	12.7	1446	12.7	2	1443	12.7	**1442**	**12.7**	1442	12.7
434.zeusmp	2	912	20.0	912	20.0	**912**	**20.0**	2	912	20.0	912	20.0	**912**	**20.0**
435.gromacs	2	697	20.5	696	20.5	**696**	**20.5**	2	**697**	**20.5**	698	20.5	696	20.5
436.cactusADM	2	**970**	**24.6**	971	24.6	967	24.7	2	**970**	**24.6**	971	24.6	967	24.7
437.leslie3d	2	1429	13.2	**1428**	**13.2**	1425	13.2	2	1428	13.2	**1428**	**13.2**	1428	13.2
444.namd	2	881	18.2	881	18.2	**881**	**18.2**	2	867	18.5	**867**	**18.5**	867	18.5
447.dealII	2	**948**	**24.1**	947	24.2	954	24.0	2	863	26.5	866	26.4	**864**	**26.5**
450.soplex	2	**1202**	**13.9**	1201	13.9	1202	13.9	2	1169	14.3	1167	14.3	**1169**	**14.3**
453.povray	2	424	25.1	425	25.1	**425**	**25.1**	2	331	32.2	331	32.2	**331**	**32.2**
454.calculix	2	994	16.6	**994**	**16.6**	994	16.6	2	989	16.7	**988**	**16.7**	988	16.7
459.GemsFDTD	2	**1745**	**12.2**	1744	12.2	1745	12.2	2	**1745**	**12.2**	1744	12.2	1745	12.2
465.tonto	2	1450	13.6	**1452**	**13.6**	1453	13.5	2	1450	13.6	**1452**	**13.6**	1453	13.5
470.lbm	2	2260	12.2	2259	12.2	**2260**	**12.2**	2	2260	12.2	2259	12.2	**2260**	**12.2**
481.wrf	2	1188	18.8	1202	18.6	**1201**	**18.6**	2	1188	18.8	1202	18.6	**1201**	**18.6**
482.sphinx3	2	1865	20.9	1863	20.9	**1863**	**20.9**	2	1716	22.7	1713	22.8	**1716**	**22.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with CSE-815TQ-R450U case.
For a general system, a 420W (minimum) ATX12V power supply [8-pin +12V AND 24-pin is recommended
to assure system stability].
Product description located as of
http://www.supermicro.com/products/motherboard/Xeon3000/3010/PDSMU.cfm
The system bus runs at 800 MHz.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F950000000 -link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
shlw32m.lib -link /FORCE:MULTIPLE

470.lbm: basepeak = yes

482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxB -Qipo -O3
-Qprec-div- /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: -QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000
libguide.lib libguide40.lib -link /FORCE:MULTIPLE

416.gamess: Same as 410.bwaves

434.zeusmp: basepeak = yes

437.leslie3d: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
-link /FORCE:MULTIPLE

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: `-QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000 shlw32m.lib libguide.lib libguide40.lib -link /FORCE:MULTIPLE`

436.cactusADM: `basepeak = yes`

454.calculix: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F950000000 -link /FORCE:MULTIPLE`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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