SPEC® CINT2006 Result

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Fujitsu Siemens Computers

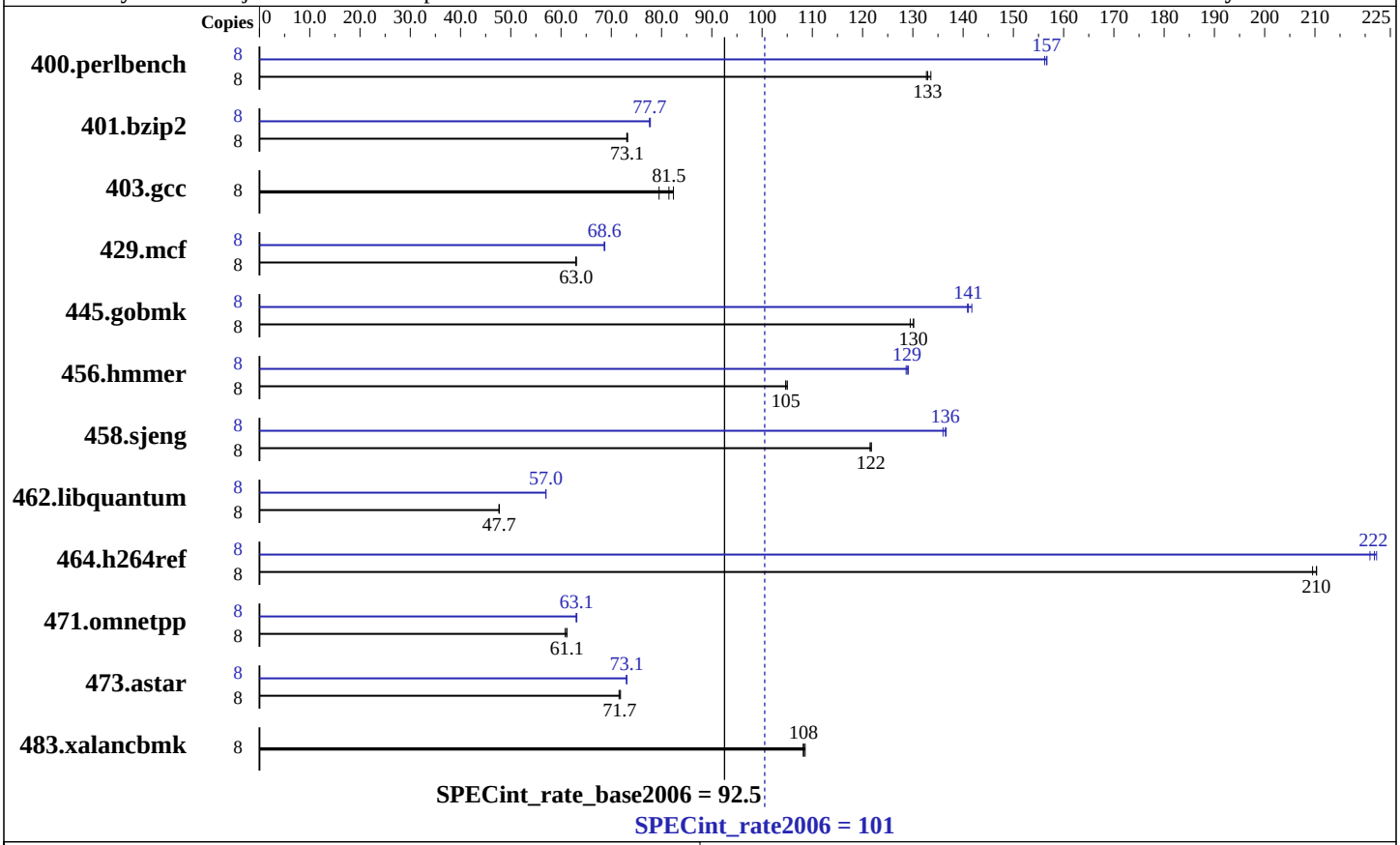
PRIMERGY BX620 S3, Intel Xeon processor X5355, 2.66 GHz

SPECint®_rate2006 = 101

SPECint_rate_base2006 = 92.5

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jun-2007
Hardware Availability: Jan-2007
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	SAS (73GB 15400 rpm)
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	589	133	585	134	**588**	**133**	8	499	157	**499**	**157**	500	156
401.bzip2	8	**1056**	**73.1**	1056	73.1	1054	73.3	8	995	77.6	**994**	**77.7**	993	77.8
403.gcc	8	**791**	**81.5**	782	82.4	810	79.5	8	**791**	**81.5**	782	82.4	810	79.5
429.mcf	8	**1158**	**63.0**	1159	62.9	1157	63.0	8	1062	68.7	**1063**	**68.6**	1063	68.6
445.gobmk	8	**645**	**130**	645	130	648	129	8	**595**	**141**	596	141	592	142
456.hmmer	8	713	105	711	105	**713**	**105**	8	578	129	580	129	**579**	**129**
458.sjeng	8	**795**	**122**	795	122	797	121	8	712	136	708	137	**709**	**136**
462.libquantum	8	3476	47.7	3475	47.7	**3475**	**47.7**	8	2910	57.0	**2909**	**57.0**	2908	57.0
464.h264ref	8	**842**	**210**	842	210	845	210	8	**798**	**222**	797	222	801	221
471.omnetpp	8	822	60.8	817	61.2	**818**	**61.1**	8	**793**	**63.1**	794	63.0	792	63.1
473.astar	8	**784**	**71.7**	785	71.5	782	71.8	8	769	73.0	**769**	**73.1**	769	73.1
483.xalancbmk	8	510	108	**510**	**108**	508	109	8	510	108	**510**	**108**	508	109

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xT -O3 -ipo -no-prec-div -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -prefetch -L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 400.perlbench`

456.hmmer: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll2`

458.sjeng: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll4`

462.libquantum: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -prefetch -opt-streaming-stores always`

464.h264ref: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -ansi-alias -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml`

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