SPEC® CINT2006 Result

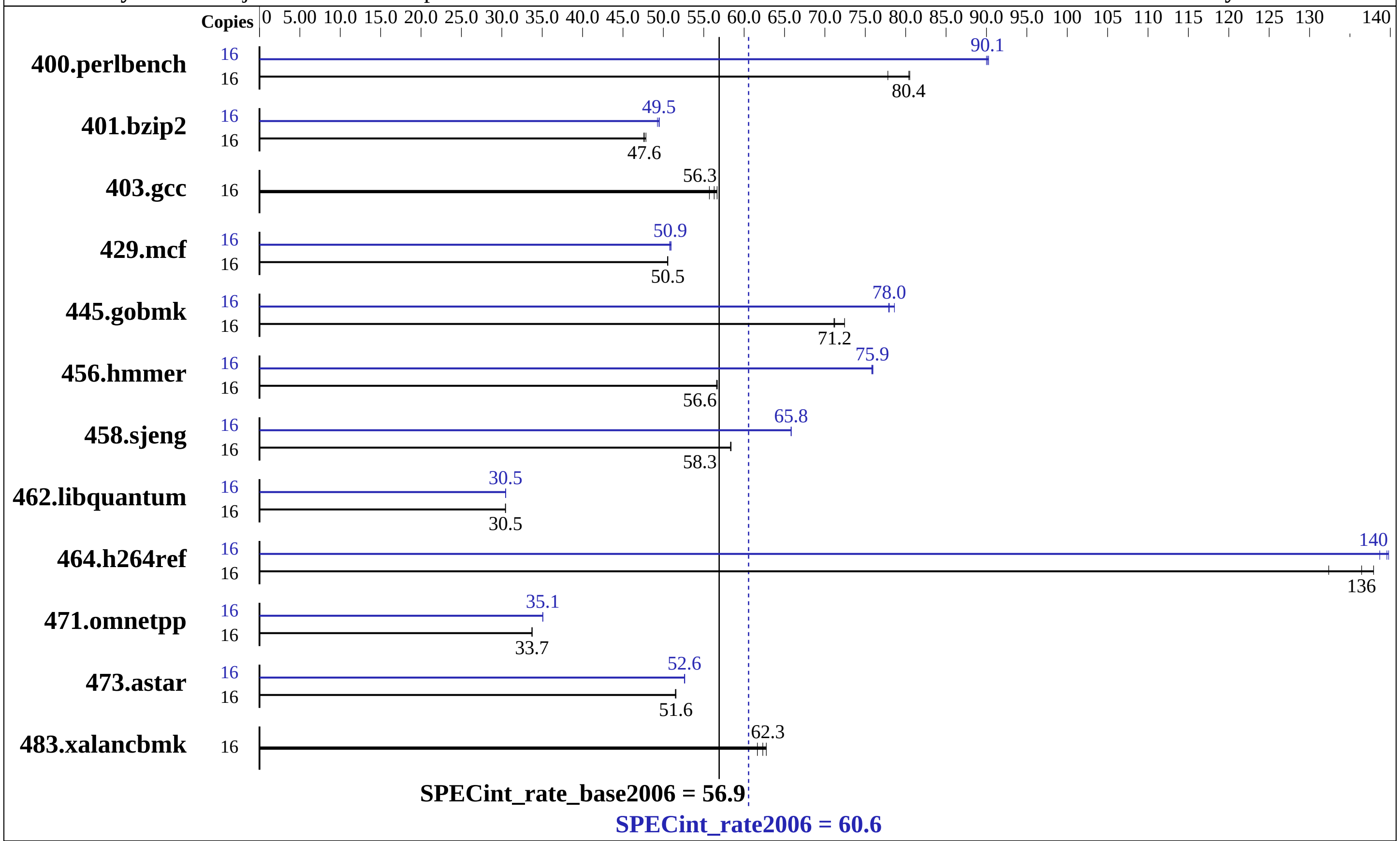
Fujitsu Siemens Computers

PRIMERGY TX600 S3, Intel Xeon processor 7120M, 3.0 GHz

SPECint®_rate2006 = 60.6

SPECint_rate_base2006 = 56.9

CPU2006 license: 22		**Test date:**	May-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Aug-2006
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Mar-2007



Hardware

CPU Name:	Intel Xeon 7120M
CPU Characteristics:	800 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16x2 GB DDR2 PC2-3200R, 2 rank, CAS 3-3-3, with ECC)
Disk Subsystem:	Seagate ST973401SS (SAS 73GB 10 krpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ReiserFS
System State:	Multiuser Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	16	2009	77.8	1942	80.5	**1945**	**80.4**	16	1737	90.0	1732	90.3	**1734**	**90.1**
401.bzip2	16	3245	47.6	**3241**	**47.6**	3227	47.8	16	**3122**	**49.5**	3134	49.3	3119	49.5
403.gcc	16	**2288**	**56.3**	2273	56.7	2312	55.7	16	**2288**	**56.3**	2273	56.7	2312	55.7
429.mcf	16	2886	50.6	2889	50.5	**2887**	**50.5**	16	2863	51.0	**2870**	**50.9**	2873	50.8
445.gobmk	16	2317	72.4	2360	71.1	**2357**	**71.2**	16	2135	78.6	**2152**	**78.0**	2155	77.9
456.hmmer	16	**2637**	**56.6**	2638	56.6	2633	56.7	16	**1968**	**75.9**	1969	75.8	1965	76.0
458.sjeng	16	3319	58.3	3316	58.4	**3319**	**58.3**	16	2942	65.8	**2941**	**65.8**	2940	65.8
462.libquantum	16	10882	30.5	10891	30.4	**10884**	**30.5**	16	10879	30.5	10878	30.5	**10878**	**30.5**
464.h264ref	16	2567	138	2675	132	**2595**	**136**	16	**2537**	**140**	2553	139	2533	140
471.omnetpp	16	2966	33.7	**2963**	**33.7**	2961	33.8	16	**2852**	**35.1**	2850	35.1	2852	35.1
473.astar	16	2182	51.5	**2179**	**51.6**	2178	51.6	16	2133	52.7	**2135**	**52.6**	2135	52.6
483.xalancbmk	16	1791	61.6	1759	62.7	**1772**	**62.3**	16	1791	61.6	1759	62.7	**1772**	**62.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

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'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
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General Notes

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The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Enable

This result was measured on the PRIMERGY RX600 S3.
The PRIMERGY RX600 S3 and the PRIMERGY TX600 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries
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Base Compiler Invocation

C benchmarks:
    icc

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Test date: May-2007

Hardware Availability: Aug-2006

Software Availability: Mar-2007

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast -L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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