



SPEC[®] CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY TX600 S3, Intel Xeon processor 7120M, 3.0 GHz

SPECint[®]_rate2006 = 35.9

SPECint_rate_base2006 = 33.7

CPU2006 license: 22

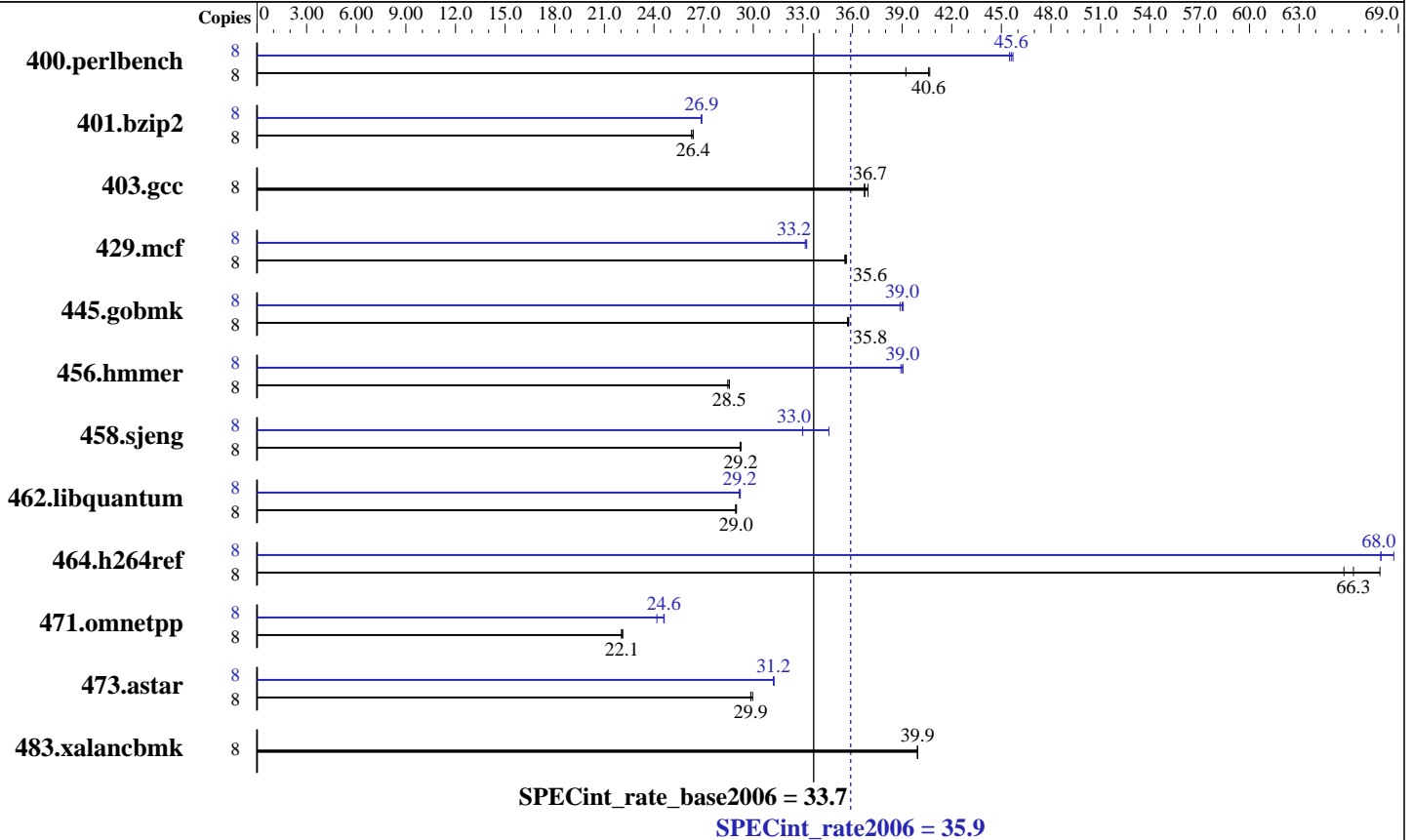
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jun-2007

Hardware Availability: Aug-2006

Software Availability: Mar-2007



Hardware

CPU Name: Intel Xeon 7120M
 CPU Characteristics: 800 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (16x2 GB DDR2 PC2-3200R, 2 rank, CAS 3-3-3, with ECC)
 Disk Subsystem: Seagate ST973401SS (SAS 73GB 10 krpm)
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
 Auto Parallel: No
 File System: ReiserFS
 System State: Multiuser Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<u>1925</u>	<u>40.6</u>	1992	39.2	1922	40.7	8	<u>1714</u>	<u>45.6</u>	1718	45.5	1710	45.7
401.bzip2	8	2928	26.4	2939	26.3	<u>2928</u>	<u>26.4</u>	8	2874	26.9	2872	26.9	<u>2873</u>	<u>26.9</u>
403.gcc	8	1743	36.9	1754	36.7	<u>1753</u>	<u>36.7</u>	8	1743	36.9	1754	36.7	<u>1753</u>	<u>36.7</u>
429.mcf	8	<u>2051</u>	<u>35.6</u>	2053	35.5	2048	35.6	8	2201	33.1	<u>2197</u>	<u>33.2</u>	2195	33.2
445.gobmk	8	<u>2346</u>	<u>35.8</u>	2351	35.7	2346	35.8	8	2159	38.9	2148	39.1	<u>2151</u>	<u>39.0</u>
456.hmmmer	8	2623	28.5	2614	28.6	<u>2615</u>	<u>28.5</u>	8	1917	38.9	1912	39.0	<u>1912</u>	<u>39.0</u>
458.sjeng	8	<u>3311</u>	<u>29.2</u>	3314	29.2	3309	29.3	8	<u>2934</u>	<u>33.0</u>	2799	34.6	2935	33.0
462.libquantum	8	<u>5723</u>	<u>29.0</u>	5730	28.9	5718	29.0	8	<u>5680</u>	<u>29.2</u>	5684	29.2	5673	29.2
464.h264ref	8	2694	65.7	<u>2671</u>	<u>66.3</u>	2608	67.9	8	2576	68.7	<u>2605</u>	<u>68.0</u>	2606	67.9
471.omnetpp	8	2261	22.1	2269	22.0	<u>2267</u>	<u>22.1</u>	8	<u>2033</u>	<u>24.6</u>	2031	24.6	2068	24.2
473.astar	8	1874	30.0	1881	29.9	<u>1881</u>	<u>29.9</u>	8	1797	31.3	<u>1798</u>	<u>31.2</u>	1799	31.2
483.xalancbmk	8	1382	39.9	<u>1382</u>	<u>39.9</u>	1382	39.9	8	1382	39.9	<u>1382</u>	<u>39.9</u>	1382	39.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Enable

This result was measured on the PRIMERGY RX600 S3.
The PRIMERGY RX600 S3 and the PRIMERGY TX600 S3 are electronically equivalent.

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Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml



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For other inquiries, please contact webmaster@spec.org.

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