SPEC® CINT2006 Result

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Bull SAS

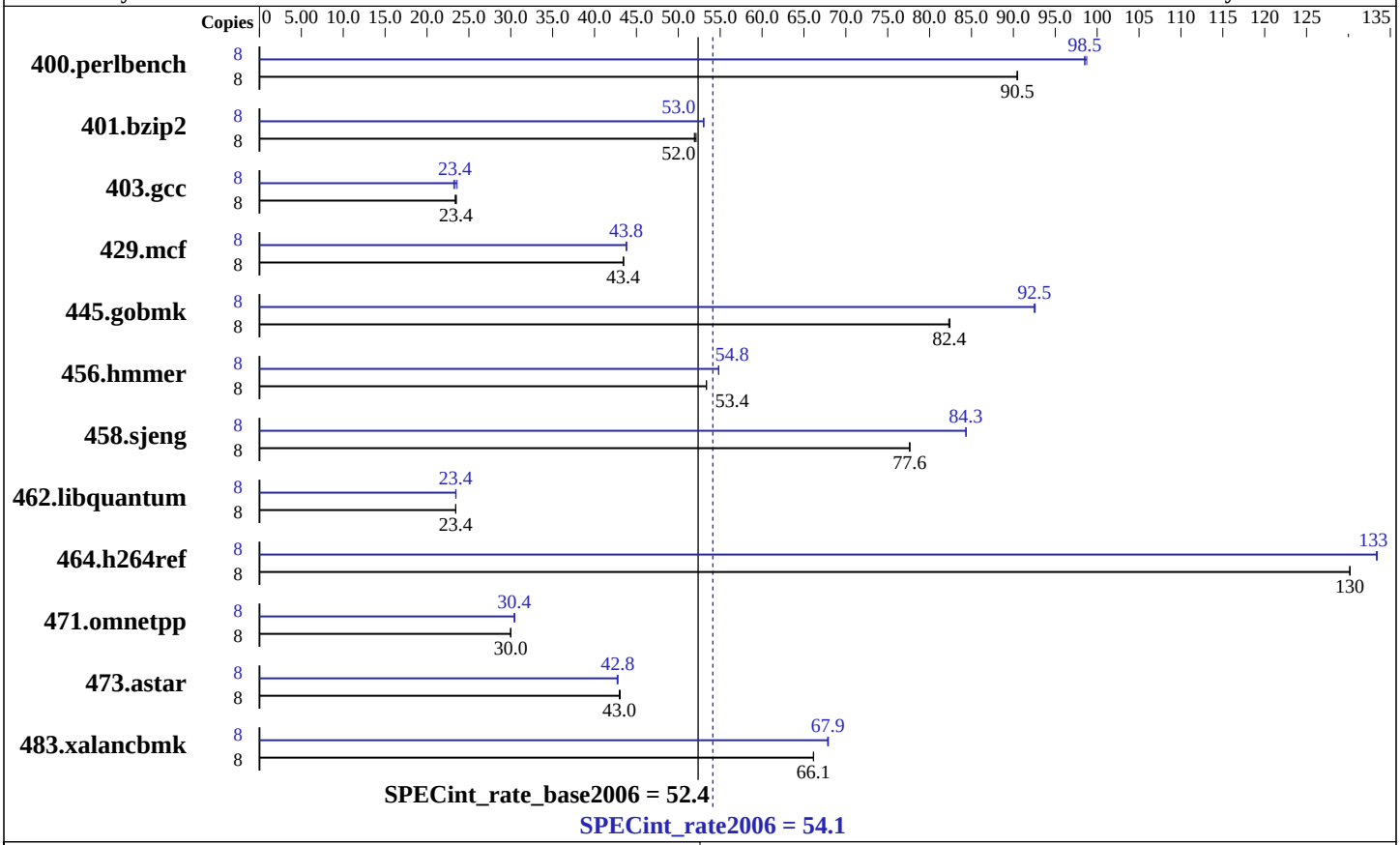
NovaScale T860
(Intel Xeon processor E5310,1.60GHz)

SPECint®_rate2006 = 54.1

SPECint_rate_base2006 = 52.4

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: May-2007
Hardware Availability: Feb-2007
Software Availability: Dec-2006



Hardware

CPU Name:	Intel Xeon E5310
CPU Characteristics:	1.60 GHz, 8 MB L2, 1066 MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1 to 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	24 GB (12x2 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem:	1x147 GB SAS, 15000 RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition X64 Edition Service Pack1
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.033 Build no 20061103Z Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	864	90.5	864	90.4	**864**	**90.5**	8	791	98.8	793	98.5	**793**	**98.5**
401.bzip2	8	**1485**	**52.0**	1481	52.1	1487	51.9	8	1456	53.0	1456	53.0	**1456**	**53.0**
403.gcc	8	**2747**	**23.4**	2757	23.4	2740	23.5	8	2730	23.6	2776	23.2	**2756**	**23.4**
429.mcf	8	**1679**	**43.4**	1679	43.5	1679	43.4	8	1664	43.8	**1666**	**43.8**	1666	43.8
445.gobmk	8	**1019**	**82.4**	1018	82.4	1020	82.3	8	907	92.5	**907**	**92.5**	906	92.6
456.hmmer	8	**1399**	**53.4**	1399	53.4	1399	53.4	8	1362	54.8	**1362**	**54.8**	1361	54.8
458.sjeng	8	**1247**	**77.6**	1247	77.6	1247	77.6	8	1148	84.4	**1148**	**84.3**	1148	84.3
462.libquantum	8	7074	23.4	7075	23.4	**7074**	**23.4**	8	**7073**	**23.4**	7064	23.5	7073	23.4
464.h264ref	8	1360	130	**1360**	**130**	1360	130	8	1327	133	1328	133	**1327**	**133**
471.omnetpp	8	1667	30.0	1667	30.0	**1667**	**30.0**	8	1640	30.5	1644	30.4	**1643**	**30.4**
473.astar	8	1307	43.0	1304	43.1	**1307**	**43.0**	8	1315	42.7	1313	42.8	**1313**	**42.8**
483.xalancbmk	8	835	66.1	**835**	**66.1**	834	66.2	8	**813**	**67.9**	813	67.9	814	67.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/flags.20090714.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/flags.20090714.00.xml`

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 11:19:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 26 June 2007.