SPEC® CINT2006 Result

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Bull SAS

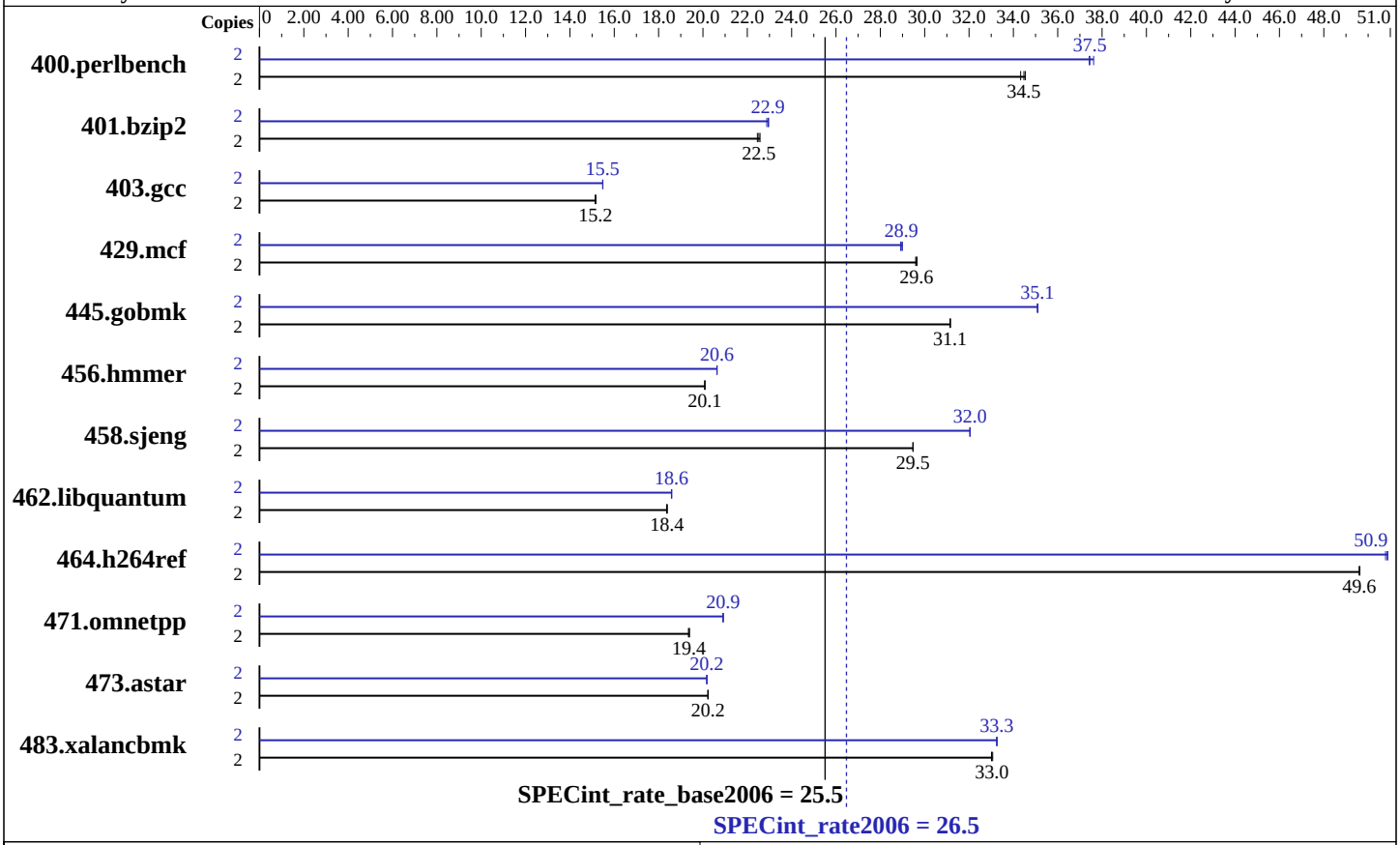
NovaScale T820
(Intel Xeon processor 3060, 2.40GHz)

SPECint®_rate2006 = 26.5

SPECint_rate_base2006 = 25.5

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: May-2007
Hardware Availability: Feb-2007
Software Availability: Dec-2006



Hardware

CPU Name:	Intel Xeon 3060
CPU Characteristics:	2.40 GHz, 4 MB L2, 1066 MHz system bus
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4x1 GB) PC2-4200E ECC CL4
Disk Subsystem:	1x80 GB SATA2, 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition X64 Edition Service Pack1
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.033 Build no 20061103Z Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**567**	**34.5**	569	34.3	566	34.5	2	**522**	**37.5**	519	37.6	522	37.4
401.bzip2	2	860	22.5	855	22.6	**857**	**22.5**	2	844	22.9	**841**	**22.9**	840	23.0
403.gcc	2	1063	15.1	1061	15.2	**1063**	**15.2**	2	1040	15.5	1040	15.5	**1040**	**15.5**
429.mcf	2	616	29.6	615	29.7	**616**	**29.6**	2	631	28.9	**630**	**28.9**	629	29.0
445.gobmk	2	673	31.2	674	31.1	**674**	**31.1**	2	**598**	**35.1**	598	35.1	598	35.1
456.hmmer	2	929	20.1	**929**	**20.1**	929	20.1	2	**904**	**20.6**	904	20.6	904	20.6
458.sjeng	2	821	29.5	821	29.5	**821**	**29.5**	2	**755**	**32.0**	755	32.0	755	32.0
462.libquantum	2	2253	18.4	**2255**	**18.4**	2257	18.4	2	2228	18.6	**2230**	**18.6**	2231	18.6
464.h264ref	2	892	49.6	892	49.6	**892**	**49.6**	2	872	50.8	**870**	**50.9**	870	50.9
471.omnetpp	2	646	19.3	645	19.4	**645**	**19.4**	2	597	20.9	598	20.9	**598**	**20.9**
473.astar	2	694	20.2	694	20.2	**694**	**20.2**	2	**696**	**20.2**	696	20.2	696	20.2
483.xalancbmk	2	417	33.1	**418**	**33.0**	418	33.0	2	415	33.3	**415**	**33.3**	415	33.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/flags.20090714.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/flags.20090714.00.xml`

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 11:24:34 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 26 June 2007.