



# SPEC® CFP2006 Result

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## Fujitsu Siemens Computers

PRIMERGY RX300 S3, Intel Xeon processor X5355,  
2.66 GHz

SPECfp®\_rate2006 = 37.5

SPECfp\_rate\_base2006 = 36.2

CPU2006 license: 22

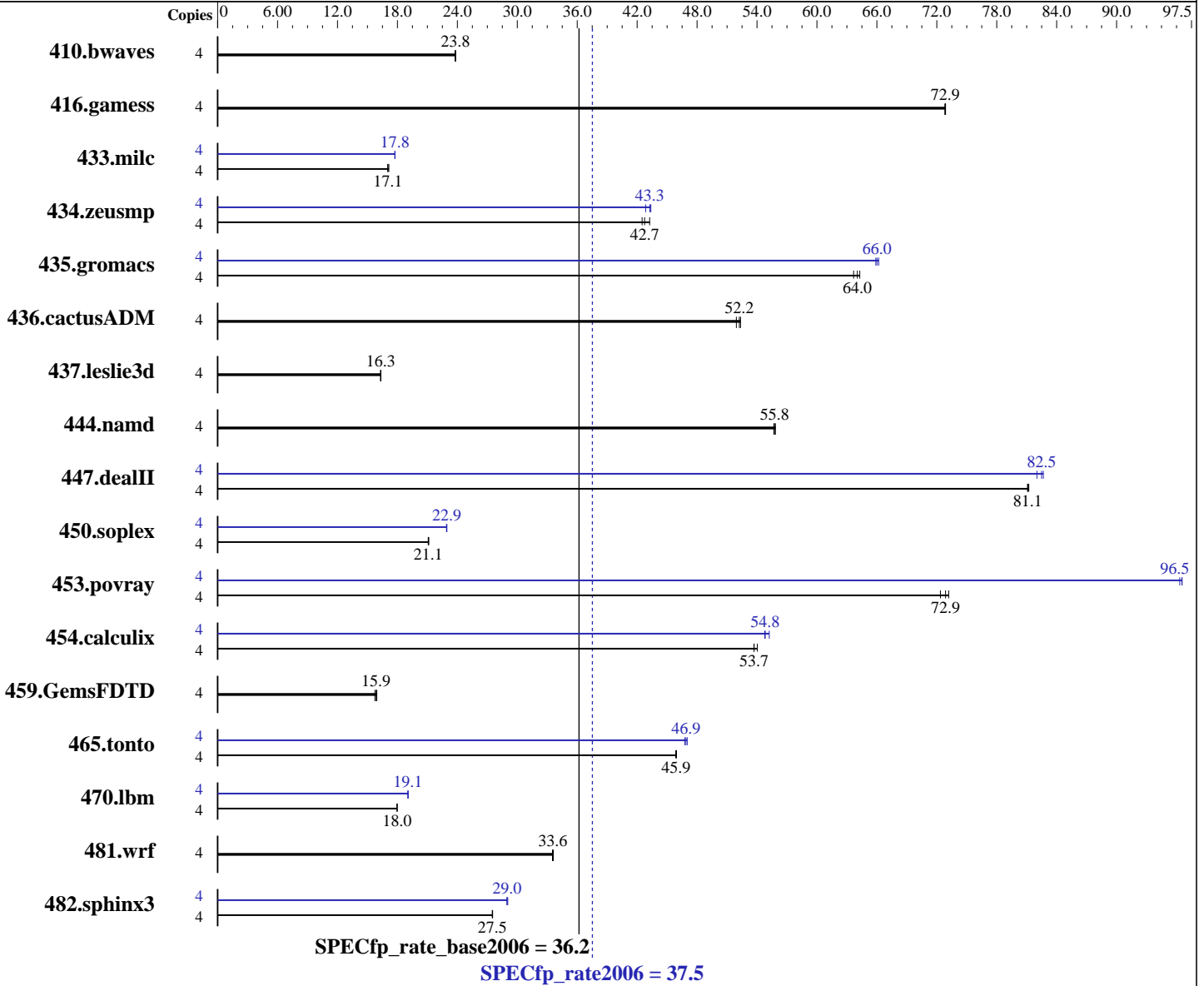
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: Jan-2007

Software Availability: Mar-2007



### Hardware

CPU Name: Intel Xeon X5355  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_cc\_p\_9.1.047  
 Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_fc\_p\_9.1.043  
 Auto Parallel: No  
 File System: ext2

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L3 Cache: None  
Other Cache: None  
Memory: 8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
Disk Subsystem: SAS (73GB 15400 rpm)  
Other Hardware: None

System State: Multiuser, Runlevel 3  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	2282	23.8	2287	23.8	<b>2283</b>	<b>23.8</b>	4	2282	23.8	2287	23.8	<b>2283</b>	<b>23.8</b>		
416.gamess	4	1076	72.8	1075	72.9	<b>1075</b>	<b>72.9</b>	4	1076	72.8	1075	72.9	<b>1075</b>	<b>72.9</b>		
433.milc	4	2142	17.1	2156	17.0	<b>2153</b>	<b>17.1</b>	4	2068	17.8	2069	17.8	<b>2068</b>	<b>17.8</b>		
434.zeusmp	4	842	43.3	<b>852</b>	<b>42.7</b>	857	42.5	4	849	42.9	<b>841</b>	<b>43.3</b>	839	43.4		
435.gromacs	4	444	64.3	<b>446</b>	<b>64.0</b>	449	63.7	4	432	66.2	433	65.9	<b>433</b>	<b>66.0</b>		
436.cactusADM	4	920	51.9	913	52.3	<b>915</b>	<b>52.2</b>	4	920	51.9	913	52.3	<b>915</b>	<b>52.2</b>		
437.leslie3d	4	<b>2303</b>	<b>16.3</b>	2300	16.3	2304	16.3	4	<b>2303</b>	<b>16.3</b>	2300	16.3	2304	16.3		
444.namd	4	<b>575</b>	<b>55.8</b>	574	55.8	576	55.7	4	<b>575</b>	<b>55.8</b>	574	55.8	576	55.7		
447.dealII	4	<b>564</b>	<b>81.1</b>	563	81.2	564	81.1	4	558	82.0	554	82.7	<b>555</b>	<b>82.5</b>		
450.soplex	4	1578	21.1	<b>1581</b>	<b>21.1</b>	1582	21.1	4	1454	22.9	<b>1454</b>	<b>22.9</b>	1456	22.9		
453.povray	4	291	73.2	<b>292</b>	<b>72.9</b>	294	72.4	4	221	96.3	220	96.6	<b>220</b>	<b>96.5</b>		
454.calculix	4	615	53.7	<b>614</b>	<b>53.7</b>	611	54.0	4	603	54.8	598	55.2	<b>602</b>	<b>54.8</b>		
459.GemsFDTD	4	2663	15.9	2696	15.7	<b>2674</b>	<b>15.9</b>	4	2663	15.9	2696	15.7	<b>2674</b>	<b>15.9</b>		
465.tonto	4	857	45.9	858	45.9	<b>857</b>	<b>45.9</b>	4	837	47.0	842	46.8	<b>839</b>	<b>46.9</b>		
470.lbm	4	<b>3059</b>	<b>18.0</b>	3060	18.0	3058	18.0	4	2883	19.1	<b>2884</b>	<b>19.1</b>	2884	19.1		
481.wrf	4	1330	33.6	<b>1331</b>	<b>33.6</b>	1333	33.5	4	1330	33.6	<b>1331</b>	<b>33.6</b>	1333	33.5		
482.sphinx3	4	<b>2834</b>	<b>27.5</b>	2835	27.5	2832	27.5	4	2692	29.0	2684	29.0	<b>2691</b>	<b>29.0</b>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

The system bus runs at 1333 MHz

All binaries were built with 64-bit Intel compiler except:  
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with  
32-bit Intel compiler by changing the path for include and library files.

The PRIMERGY RX300 S3 and the PRIMERGY TX300 S3 are electronically equivalent.

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## General Notes (Continued)

For information about Fujitsu Siemens Computers in your country please see:  
<http://www.fujitsu-siemens.com/countries>

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

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## Base Optimization Flags (Continued)

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

## Peak Compiler Invocation

C benchmarks:

/opt/intel/cc/9.1.047/bin/icc -I/opt/intel/cc/9.1.047/include  
-L/opt/intel/cc/9.1.047/lib

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/cc/9.1.047/bin/icpc  
-I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib

Fortran benchmarks (except as noted below):

ifort

434.zeusmp: /opt/intel/fc/9.1.043/bin/ifort  
-I/opt/intel/fc/9.1.043/include -L/opt/intel/fc/9.1.043/lib

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

433.milc: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

470.lbm: Same as 433.milc

482.sphinx3: -fast

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

450.soplex: Same as 447.dealII

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

Benchmarks using both Fortran and C:

435.gromacs: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

436.cactusADM: basepeak = yes

454.calculix: Same as 435.gromacs

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml)



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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